OB30- Cyclic Interrupt OBs

Project							
Name:	OB30- Cyclic Interrupt OBs	**Creation time:**	3/26/2023 7:58:19 AM	**Last change**	3/26/2023 8:03:30 AM	**Author:**	MahmoudSalama
Last modified by:	Mmuhamed	**Version:**					
Comment:							

Operating system	
Name	**Description**
Operating system	Microsoft Windows 10 Pro
Version of the operating system	6.3.9600.0
Operating system service pack	
Version of the Internet Explorer	11.789.19041.0
Computer name	MMUHAMED-D1
User name	GULSANEGYPT\Mmuhamed
Installation path of the TIA Portal	C:\Program Files\Siemens\Automation\Portal V16

Components		
Name	**Version**	**Release**
TIA Portal Project Server V16 - TIA Portal Project Server Single SetupPackage V16.0 (MUSERVERV16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - SIMATIC S7-PLCSIM V16.0 (S7_PLCSIM_V16)	V16.0	V16.00.00.00_31.00.13.01
TIA Administrator - AWB Licensing Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - AWB Software Management V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - TIA UMC Agent Configurator Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - TIA Administrator V1.0 SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
Siemens Totally Integrated Automation Portal V16 - HM All Editions Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - HM NoBasic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 0 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Multiuser Client Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Version Control Interface SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - STEP 7 Safety Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - STEP 7 Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 03 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 04 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package WCF-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - TIACOMPCHECK Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Openness SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Transfer Mandatory Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
User Management Component - UserManagementComponentx64 V2.7 (UMC64)	V2.7	V02.07.00.00_04.06.00.07
WinCC Runtime Advanced V16.0 - HMIRTM Tagging Package 01 Single SetupPackage V16.0 (HMIRTM_V11)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
SIMATIC HMI License Manager Panel Plugin (x64)	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC WinCC Runtime Advanced Driver (x64)	16.0.0.0	V16.00.00.00_31.02.00.01
ETWEventCollector	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC NCM FWL 64	5.6.0.3	K5.6.0.3_1.1.0.2
NCM GPRS 64	01.02.00.00	V1.2.0.0_2.1.0.1
SIMATIC PLCSIM 64	16.00.00	16.00.00.00_01.00.02.01
SIMATIC Device Drivers	9.2	09.02.04.00_01.04.00.05
TelemetryConnector	1.0.2.57	V01.00.02.57_01.00.00.01
Automation Software Updater	02.05.0300	V02.05.03.00_01.01.00.29
SIEMENS OPC	3.9	03.09.10.00_01.04.00.08
SIMATIC HMI ProSave	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC HMI Symbol Library	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC HMI Touch Input	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC Device Drivers WoW	29.2	29.02.04.00_01.04.00.05
SIMATIC Event Database	5.6	05.06.02.00_01.01.00.01

Totally Integrated Automation Portal

Name	Version	Release
SeCon	2.6	V02.06.01.00_01.08.00.01
WinCC Runtime Advanced Simulator	16.0.0.0	V16.00.00.00_31.02.00.01

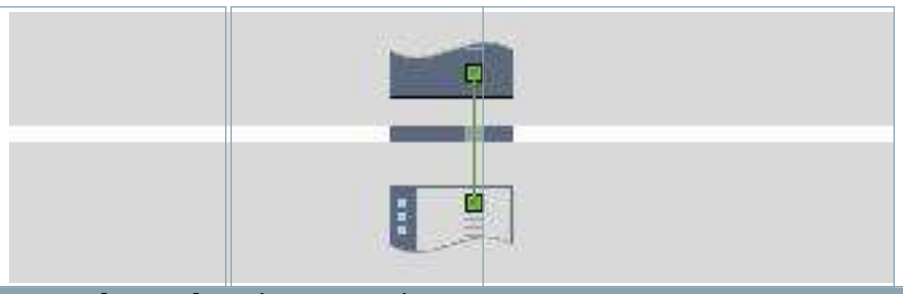
Products

Name	Version	Release
TIA Portal Project Server	V16.0	V16.00.00.00_31.02.00.01
SIMATIC S7-PLCSIM	V16.0	V16.00.00.00_31.00.13.01
TIA Administrator	V1.0	01.00.02.00_01.10.00.01
SIMATIC STEP 7 Prof - STEP 7 Safety - WinCC Adv	V16.0	V16.00.00.00_31.02.00.01
User Management Component	V2.7	V02.07.00.00_00.00.00.00
SIMATIC WinCC Runtime Advanced Simulation	V16.0	V16.00.00.00_31.02.00.01
Automation License Manager	V6.0 + SP5 + Upd1	06.00.05.01_02.01.00.05
S7-PLCSIM	V5.4 + SP8	V05.04.08.01_01.24.00.01
SIMATIC ProSave	V16.0	V16.00.00.00_31.02.00.01
S7-PCT	V3.5 + SP1	K3.5.1.0_1.19.0.1

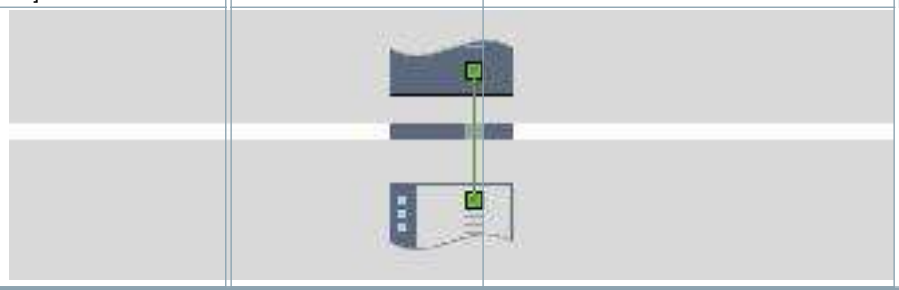
OB30- Cyclic Interrupt OBs

PLC_1 [CPU 1512C-1 PN]

PLC_1					
General\Project information					
Name	PLC_1	**Author**	Mmuhamed	**Comment**	
Rack	0	**Slot**	1		
General\Catalog information					
Short designation	CPU 1512C-1 PN	**Description**	CPU with display; work memory 250 KB code and 1 MB data; 48 ns bit operation time; 4-stage protection concept, technology functions: motion control, closed-loop control, counting and measuring; tracing; Runtime options; for all PROFINET interfaces: transport protocol TCP/IP, secure Open User Communication, S7 communication, S7 routing, IP forwarding, Web server, DNS client, OPC UA: Server DA, Client DA, methods, companion specifications; PROFINET IO controller, supports RT/IRT, performance upgrade PROFINET V2.3, 2 ports, I-Device, MRP, MRPD, isochronous mode, Routing, runtime options; firmware V2.8 with DI32/DQ32, AI5/AQ2: Digital input module DI16 x DC24V, grouping 16; Digital output module DQ16 x DC24V/ 0.5A, grouping 16; Analog input module AI4 x U/I, AI 1xRTD, 16-bit, grouping 5; Analog output module AQ2 x U/I, 16-bit, grouping 2; 6 channels for counting and measuring with incremental encoders 24V (up to 100kHz); 4 channels for PTO, pulse width modulation, frequency output (up to 100kHz)	**Article number**	6ES7 512-1CK01-0AB0
Firmware version	V2.8				
General\Identification & Maintenance					
Plant designation		**Location identifier**		**Installation date**	2023-03-26 08:03:30.533
Additional information					
General\Checksums					
Text lists	FA 70 E8 75 1D 5A 8E 29	**Software**	Not available (compile necessary)		
PROFINET interface [X1]\General					
Name	PROFINET interface_1	**Author**	Mmuhamed	**Comment**	
PROFINET interface [X1]\Ethernet addresses\Interface networked with					
Subnet:	Not connected				
PROFINET interface [X1]\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.1	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface [X1]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_1.profinet interface_1
Converted name:	plcxb1.profinetxainterfacexb1036c	**Device number:**	0		
PROFINET interface [X1]\Time-of-day synchronization\NTP mode					
Note	Time synchronization for all PROFINET interfaces take place within the settings for time synchronization of the PROFINET interface [X1].	**Enable time synchronization via NTP server**	False		IP addresses
Server 1	0.0.0.0	**Server 2**	0.0.0.0	**Server 3**	0.0.0.0
Server 4	0.0.0.0	**Update interval**	10s		
PROFINET interface [X1]\Operating mode					
IO controller	True	**IO system**		**Device number**	0
IO device	False				
PROFINET interface [X1]\Advanced options\Interface options					
Call the user program if communication errors occur	False	**Support device replacement without exchangeable medium**	True	**Permit overwriting of device names of all assigned IO devices**	False
Limit data infeed into the network	True	**Use IEC V2.2 LLDP mode**	False	**Keep-Alive connection monitoring:**	30s
PROFINET interface [X1]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X1]\Advanced options\Real time settings\Synchronization					
RT class:	RT,IRT				
PROFINET interface [X1]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%		
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\General					
Name	Port_1	**Author**	Mmuhamed	**Comment**	
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1 R]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Alternative partners**	False	**Partner port:**	Any partner
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\General					
Name	Port_2	**Author**	Mmuhamed	**Comment**	
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_2 [X1 P2 R]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Alternative partners**	False	**Partner port:**	Any partner
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X1]\Web server access					
Note	The Web server must also be activated in the properties of the PLC.	**Enable Web server via IP address of this interface**	False		
AI 5/AQ 2 [X10]\General					
Name	AI 5/AQ 2_1	**Comment**			
AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics					
Wire break	False	**Short circuit to ground**	False	**Overflow**	False
Underflow	False				
AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Output parameters					
Output type	Voltage	**Output range**	+/- 10V	**Reaction to CPU STOP**	Shutdown
Substitute value					
AI 5/AQ 2 [X10]\AI/AQ configuration\Value status (Quality Information)					
Value status	False				
AI 5/AQ 2 [X10]\Inputs\General\Measuring					
Interference frequency suppression	50Hz				
AI 5/AQ 2 [X10]\Inputs\Channel 0					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdgeEvent**	49272	**Event name:**	

Hardware interrupt:	0	**UpperLimitOne0**	UpperLimitOne0	**Channel number**	0
HwEventTypeLi-mit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdg-eEvent**	49288	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne0**	LowerLimitOne0	**Channel number**	0
HwEventTypeLi-mit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdg-eEvent**	49264	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo0**	UpperLimitTwo0	**Channel number**	0
HwEventTypeLi-mit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdg-eEvent**	49280	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo0**	LowerLimitTwo0	**Channel number**	0
HwEventTypeLi-mit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 1					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coeffi-cient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdg-eEvent**	49273	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne1**	UpperLimitOne1	**Channel number**	1
HwEventTypeLi-mit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdg-eEvent**	49289	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne1**	LowerLimitOne1	**Channel number**	1
HwEventTypeLi-mit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdg-eEvent**	49265	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo1**	UpperLimitTwo1	**Channel number**	1
HwEventTypeLi-mit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdg-eEvent**	49281	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo1**	LowerLimitTwo1	**Channel number**	1
HwEventTypeLi-mit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 2					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 2\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coeffi-cient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdg-eEvent**	49274	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne2**	UpperLimitOne2	**Channel number**	2
HwEventTypeLi-mit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdg-eEvent**	49290	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne2**	LowerLimitOne2	**Channel number**	2
HwEventTypeLi-mit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdg-eEvent**	49266	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo2**	UpperLimitTwo2	**Channel number**	2
HwEventTypeLi-mit2Overrun	6				

AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdgeEvent**	49282	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo2**	LowerLimitTwo2	**Channel number**	2
HwEventTypeLimit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 3					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 3\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdgeEvent**	49275	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne3**	UpperLimitOne3	**Channel number**	3
HwEventTypeLimit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdgeEvent**	49291	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne3**	LowerLimitOne3	**Channel number**	3
HwEventTypeLimit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdgeEvent**	49267	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo3**	UpperLimitTwo3	**Channel number**	3
HwEventTypeLimit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdgeEvent**	49283	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo3**	LowerLimitTwo3	**Channel number**	3
HwEventTypeLimit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 4					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 4\Measuring					
Measurement type	Resistance	**Measuring range**	600Ohm	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdgeEvent**	49276	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne4**	UpperLimitOne4	**Channel number**	4
HwEventTypeLimit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdgeEvent**	49292	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne4**	LowerLimitOne4	**Channel number**	4
HwEventTypeLimit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdgeEvent**	49268	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo4**	UpperLimitTwo4	**Channel number**	4
HwEventTypeLimit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdgeEvent**	49284	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo4**	LowerLimitTwo4	**Channel number**	4
HwEventTypeLimit2Underrun	5				
AI 5/AQ 2 [X10]\Outputs\Channel 0					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Outputs\Channel 0\Diagnostics					
Wire break	False	**Short circuit to ground**	False	**Overflow**	False
Underflow	False				
AI 5/AQ 2 [X10]\Outputs\Channel 0\Output					
Output type	Voltage	**Output range**	+/- 10V	**Reaction to CPU STOP**	Shutdown
Substitute value					

AI 5/AQ 2 [X10]\Outputs\Channel 1					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Outputs\Channel 1\Diagnostics					
Wire break	False	**Short circuit to ground**	False	**Overflow**	False
Underflow	False				
AI 5/AQ 2 [X10]\Outputs\Channel 1\Output					
Output type	Voltage	**Output range**	+/- 10V	**Reaction to CPU STOP**	Shutdown
Substitute value					
AI 5/AQ 2 [X10]\I/O addresses\Input addresses					
Start address	0	**End address**	9	**Organization block**	0
Process image	0				
AI 5/AQ 2 [X10]\I/O addresses\Output addresses					
Start address	0	**End address**	3	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X11]\General					
Name	DI 16/DQ 16_1	**Comment**			
DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\DI/DQ configuration\Value status (Quality Information)					
Value status	False				
DI 16/DQ 16 [X11]\Inputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49152	**Event name:**	
Hardware interrupt:	0	**Rising edge0**	Rising edge0	**Channel number**	0
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49280	**Event name:**	
Hardware interrupt:	0	**Falling edge0**	Falling edge0	**Channel number**	0
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49153	**Event name:**	
Hardware interrupt:	0	**Rising edge1**	Rising edge1	**Channel number**	1
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49281	**Event name:**	
Hardware interrupt:	0	**Falling edge1**	Falling edge1	**Channel number**	1
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49154	**Event name:**	
Hardware interrupt:	0	**Rising edge2**	Rising edge2	**Channel number**	2
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49282	**Event name:**	
Hardware interrupt:	0	**Falling edge2**	Falling edge2	**Channel number**	2
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 3\Input parameters					
Input delay	3.2ms				

DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49155	**Event name:**	
Hardware interrupt:	0	**Rising edge3**	Rising edge3	**Channel number**	3
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49283	**Event name:**	
Hardware interrupt:	0	**Falling edge3**	Falling edge3	**Channel number**	3
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49156	**Event name:**	
Hardware interrupt:	0	**Rising edge4**	Rising edge4	**Channel number**	4
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49284	**Event name:**	
Hardware interrupt:	0	**Falling edge4**	Falling edge4	**Channel number**	4
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49157	**Event name:**	
Hardware interrupt:	0	**Rising edge5**	Rising edge5	**Channel number**	5
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49285	**Event name:**	
Hardware interrupt:	0	**Falling edge5**	Falling edge5	**Channel number**	5
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49158	**Event name:**	
Hardware interrupt:	0	**Rising edge6**	Rising edge6	**Channel number**	6
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49286	**Event name:**	
Hardware interrupt:	0	**Falling edge6**	Falling edge6	**Channel number**	6
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49159	**Event name:**	
Hardware interrupt:	0	**Rising edge7**	Rising edge7	**Channel number**	7
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49287	**Event name:**	
Hardware interrupt:	0	**Falling edge7**	Falling edge7	**Channel number**	7
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 8\Diagnostics					
No supply voltage L+	False				

DI 16/DQ 16 [X11]\Inputs\Channel 8\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49160	**Event name:**	
Hardware interrupt:	0	**Rising edge8**	Rising edge8	**Channel number**	8
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49288	**Event name:**	
Hardware interrupt:	0	**Falling edge8**	Falling edge8	**Channel number**	8
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49161	**Event name:**	
Hardware interrupt:	0	**Rising edge9**	Rising edge9	**Channel number**	9
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49289	**Event name:**	
Hardware interrupt:	0	**Falling edge9**	Falling edge9	**Channel number**	9
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 10					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49162	**Event name:**	
Hardware interrupt:	0	**Rising edge10**	Rising edge10	**Channel number**	10
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49290	**Event name:**	0
Hardware interrupt:	0	**Falling edge10**	Falling edge10	**Channel number**	10
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 11					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49163	**Event name:**	
Hardware interrupt:	0	**Rising edge11**	Rising edge11	**Channel number**	11
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49291	**Event name:**	
Hardware interrupt:	0	**Falling edge11**	Falling edge11	**Channel number**	11
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49164	**Event name:**	
Hardware interrupt:	0	**Rising edge12**	Rising edge12	**Channel number**	12
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49292	**Event name:**	
Hardware interrupt:	0	**Falling edge12**	Falling edge12	**Channel number**	12
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 13					
Parameter settings	From template				

DI 16/DQ 16 [X11]\Inputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 13\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49165	**Event name:**	
Hardware interrupt:	0	**Rising edge13**	Rising edge13	**Channel number**	13
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49293	**Event name:**	
Hardware interrupt:	0	**Falling edge13**	Falling edge13	**Channel number**	13
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49166	**Event name:**	
Hardware interrupt:	0	**Rising edge14**	Rising edge14	**Channel number**	14
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49294	**Event name:**	
Hardware interrupt:	0	**Falling edge14**	Falling edge14	**Channel number**	14
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49167	**Event name:**	
Hardware interrupt:	0	**Rising edge15**	Rising edge15	**Channel number**	15
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49295	**Event name:**	
Hardware interrupt:	0	**Falling edge15**	Falling edge15	**Channel number**	15
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Outputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 0\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 1\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 2\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 3\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 4\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 5\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 5\Output parameters					
Reaction to CPU STOP	Shutdown				

DI 16/DQ 16 [X11]\Outputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 6\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 7\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 8\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 8\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 9\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 10					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 10\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 11					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 11\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 12\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 13					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 13\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 14\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 15\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\I/O addresses\Input addresses					
Start address	10.0	**End address**	11.7	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X11]\I/O addresses\Output addresses					
Start address	4.0	**End address**	5.7	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X12]\General					
Name	DI 16/DQ 16_2	**Comment**			
DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\DI/DQ configuration\Value status (Quality Information)					
Value status	False				
DI 16/DQ 16 [X12]\Inputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 0\Input parameters					
Input delay	3.2ms				

DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49152	**Event name:**	
Hardware interrupt:	0	**Rising edge0**	Rising edge0	**Channel number**	0
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49280	**Event name:**	
Hardware interrupt:	0	**Falling edge0**	Falling edge0	**Channel number**	0
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49153	**Event name:**	
Hardware interrupt:	0	**Rising edge1**	Rising edge1	**Channel number**	1
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49281	**Event name:**	
Hardware interrupt:	0	**Falling edge1**	Falling edge1	**Channel number**	1
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49154	**Event name:**	
Hardware interrupt:	0	**Rising edge2**	Rising edge2	**Channel number**	2
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49282	**Event name:**	
Hardware interrupt:	0	**Falling edge2**	Falling edge2	**Channel number**	2
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49155	**Event name:**	
Hardware interrupt:	0	**Rising edge3**	Rising edge3	**Channel number**	3
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49283	**Event name:**	
Hardware interrupt:	0	**Falling edge3**	Falling edge3	**Channel number**	3
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49156	**Event name:**	
Hardware interrupt:	0	**Rising edge4**	Rising edge4	**Channel number**	4
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49284	**Event name:**	
Hardware interrupt:	0	**Falling edge4**	Falling edge4	**Channel number**	4
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 5\Diagnostics					
No supply voltage L+	False				

DI 16/DQ 16 [X12]\Inputs\Channel 5\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49157	**Event name:**	
Hardware interrupt:	0	**Rising edge5**	Rising edge5	**Channel number**	5
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49285	**Event name:**	
Hardware interrupt:	0	**Falling edge5**	Falling edge5	**Channel number**	5
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49158	**Event name:**	
Hardware interrupt:	0	**Rising edge6**	Rising edge6	**Channel number**	6
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49286	**Event name:**	
Hardware interrupt:	0	**Falling edge6**	Falling edge6	**Channel number**	6
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49159	**Event name:**	
Hardware interrupt:	0	**Rising edge7**	Rising edge7	**Channel number**	7
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49287	**Event name:**	
Hardware interrupt:	0	**Falling edge7**	Falling edge7	**Channel number**	7
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49160	**Event name:**	
Hardware interrupt:	0	**Rising edge8**	Rising edge8	**Channel number**	8
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49288	**Event name:**	
Hardware interrupt:	0	**Falling edge8**	Falling edge8	**Channel number**	8
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49161	**Event name:**	
Hardware interrupt:	0	**Rising edge9**	Rising edge9	**Channel number**	9
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49289	**Event name:**	
Hardware interrupt:	0	**Falling edge9**	Falling edge9	**Channel number**	9
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 10					
Parameter settings	From template				

DI 16/DQ 16 [X12]\Inputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 10\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49162	**Event name:**	
Hardware interrupt:	0	**Rising edge10**	Rising edge10	**Channel number**	10
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49290	**Event name:**	0
Hardware interrupt:	0	**Falling edge10**	Falling edge10	**Channel number**	10
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 11					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49163	**Event name:**	
Hardware interrupt:	0	**Rising edge11**	Rising edge11	**Channel number**	11
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49291	**Event name:**	
Hardware interrupt:	0	**Falling edge11**	Falling edge11	**Channel number**	11
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49164	**Event name:**	
Hardware interrupt:	0	**Rising edge12**	Rising edge12	**Channel number**	12
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49292	**Event name:**	
Hardware interrupt:	0	**Falling edge12**	Falling edge12	**Channel number**	12
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 13					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49165	**Event name:**	
Hardware interrupt:	0	**Rising edge13**	Rising edge13	**Channel number**	13
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49293	**Event name:**	
Hardware interrupt:	0	**Falling edge13**	Falling edge13	**Channel number**	13
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49166	**Event name:**	
Hardware interrupt:	0	**Rising edge14**	Rising edge14	**Channel number**	14
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49294	**Event name:**	
Hardware interrupt:	0	**Falling edge14**	Falling edge14	**Channel number**	14
HwEventTypeFallingEdge	2				

DI 16/DQ 16 [X12]\Inputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49167	**Event name:**	
Hardware interrupt:	0	**Rising edge15**	Rising edge15	**Channel number**	15
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49295	**Event name:**	
Hardware interrupt:	0	**Falling edge15**	Falling edge15	**Channel number**	15
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Outputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 0\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 1\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 2\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 3\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 4\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 5\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 5\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 6\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 7\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 8\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 8\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 9\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 10					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 10\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 11					
Parameter settings	From template				

DI 16/DQ 16 [X12]\Outputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 11\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 12\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 13					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 13\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 14\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 15\Output parameters					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 15\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\I/O addresses\Input addresses					
Start address	12.0	**End address**	13.7	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X12]\I/O addresses\Output addresses					
Start address	6.0	**End address**	7.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\CPU 1511C compatibility					
Front connector assignment like CPU 1511C	False				
High speed counters (HSC)\HSC 1\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 1\General\Project information					
Name	HSC_1	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 1\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 1\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 1\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				

High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirection-ChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirec-tionChanged	10				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossin-gEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroC-rossing	7				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCom-pare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCom-pare0	5				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCom-pare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCom-pare1	6				
High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 1\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in incre-ments)	0				
High speed counters (HSC)\HSC 1\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 1\Hardware inputs/outputs					
Pulse input (A)	X11, Clamp 1 (DI0 / %I10.0)	**Direction input (B)**	X11, Clamp 2 (DI1 / %I10.1)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 1\I/O addresses\Input addresses					
Start address	14.0	**End address**	29.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 1\I/O addresses\Output addresses					
Start address	8.0	**End address**	19.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 2\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 2\General\Project information					
Name	HSC_2	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 2\Channel 0\Operating mode					
Selection of operat-ing mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 2\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 2\Channel 0\Diagnostic interrupts					
Enable diagnostic in-terrupts	False				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureE-vent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an ex-ternal signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				

High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 2\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 2\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 2\Hardware inputs/outputs					
Pulse input (A)	X11, Clamp 4 (DI3 / %I10.3)	**Direction input (B)**	X11, Clamp 5 (DI4 / %I10.4)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 2\I/O addresses\Input addresses					
Start address	30.0	**End address**	45.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 2\I/O addresses\Output addresses					
Start address	20.0	**End address**	31.7	**Organization block**	0
Process image	0				

High speed counters (HSC)\HSC 3\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 3\General\Project information					
Name	HSC_3	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 3\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 3\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 3\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected

High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 3\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 3\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 3\Hardware inputs/outputs					
Pulse input (A)	X11, Clamp 7 (DI6 / %I10.6)	**Direction input (B)**	X11, Clamp 8 (DI7 / %I10.7)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 3\I/O addresses\Input addresses					
Start address	46.0	**End address**	61.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 3\I/O addresses\Output addresses					
Start address	32.0	**End address**	43.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 4\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 4\General\Project information					
Name	HSC_4	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 4\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 4\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 4\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				

High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 4\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 4\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 4\Hardware inputs/outputs					
Pulse input (A)	X12, Clamp 1 (DI0 / %I12.0)	**Direction input (B)**	X12, Clamp 2 (DI1 / %I12.1)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 4\I/O addresses\Input addresses					
Start address	62.0	**End address**	77.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 4\I/O addresses\Output addresses					
Start address	44.0	**End address**	55.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 5\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 5\General\Project information					
Name	HSC_5	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 5\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 5\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 5\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				

High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 5\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 5\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 5\Hardware inputs/outputs					
Pulse input (A)	X12, Clamp 4 (DI3 / %I12.3)	**Direction input (B)**	X12, Clamp 5 (DI4 / %I12.4)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 5\I/O addresses\Input addresses					
Start address	78.0	**End address**	93.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 5\I/O addresses\Output addresses					
Start address	56.0	**End address**	67.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 6\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 6\General\Project information					
Name	HSC_6	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 6\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 6\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0

High speed counters (HSC)\HSC 6\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				

High speed counters (HSC)\HSC 6\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 6\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 6\Hardware inputs/outputs					
Pulse input (A)	X12, Clamp 7 (DI6 / %I12.6)	**Direction input (B)**	X12, Clamp 8 (DI7 / %I12.7)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 6\I/O addresses\Input addresses					
Start address	94.0	**End address**	109.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 6\I/O addresses\Output addresses					
Start address	68.0	**End address**	79.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\CPU 1511C compatibility					
Front connector assignment like CPU 1511C	False				
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information					
Name	Pulse_1	**Comment**			
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO1/PWM1\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		
Pulse generators (PTO/PWM)\PTO1/PWM1\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO1/PWM1\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 21 (DQ0 / %Q4.0): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO1/PWM1\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Input addresses					
Start address	110.0	**End address**	113.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses					
Start address	80.0	**End address**	91.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO2/PWM2\General\Project information					
Name	Pulse_2	**Comment**			
Pulse generators (PTO/PWM)\PTO2/PWM2\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO2/PWM2\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		
Pulse generators (PTO/PWM)\PTO2/PWM2\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO2/PWM2\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 23 (DQ2 / %Q4.2): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO2/PWM2\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Input addresses					
Start address	114.0	**End address**	117.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Output addresses					
Start address	92.0	**End address**	103.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO3/PWM3\General\Project information					
Name	Pulse_3	**Comment**			
Pulse generators (PTO/PWM)\PTO3/PWM3\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO3/PWM3\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		
Pulse generators (PTO/PWM)\PTO3/PWM3\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO3/PWM3\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 25 (DQ4 / %Q4.4): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO3/PWM3\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Input addresses					
Start address	118.0	**End address**	121.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Output addresses					
Start address	104.0	**End address**	115.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO4/PWM4\General\Project information					
Name	Pulse_4	**Comment**			
Pulse generators (PTO/PWM)\PTO4/PWM4\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO4/PWM4\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		

Pulse generators (PTO/PWM)\PTO4/PWM4\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO4/PWM4\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 27 (DQ6 / %Q4.6): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO4/PWM4\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Input addresses					
Start address	122.0	**End address**	125.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Output addresses					
Start address	116.0	**End address**	127.7	**Organization block**	0
Process image	0				
Startup					
Startup after POWER ON	Warm restart - Operating mode before POWER OFF	**Comparison preset to actual configuration**	Startup CPU even if mismatch	**Configuration time**	60000ms
Cycle					
Maximum cycle time	150ms			**Enable minimum cycle time for cyclic OBs**	True
Minimum cycle time	1ms				
Communication load					
Cycle load due to communication	50%				
System and clock memory\System memory bits					
Enable the use of system memory byte	False	**Address of system memory byte (MBx)**	1	**First cycle**	
Diagnostic status changed		**Always 1 (high)**		**Always 0 (low)**	
System and clock memory\Clock memory bits					
Enable the use of clock memory byte	False	**Address of clock memory byte (MBx)**	0	**10 Hz clock**	
5 Hz clock		**2.5 Hz clock**		**2 Hz clock**	
1.25 Hz clock		**1 Hz clock**		**0.625 Hz clock**	
0.5 Hz clock					
SIMATIC Memory Card\Diagnostics					
Aging of the SIMATIC memory card	False	**Threshold value**	80%		
System diagnostics\General					
Activate system diagnostics for this device	True	**Report network faults as maintenance instead of fault**	False		
PLC alarms\General					
Central alarm management in the PLC	True				
Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	True		
Web server\Automatic update					
Enable automatic update	True	**Update interval**	0s		

Web server\User management

User name	User rights
Everybody	

Web server\User-defined web pages

Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number
		index.htm	.htm;.html	333	334

Web server\Overview of interfaces

Device	Interface	Enabled web server access
PLC_1	PROFINET interface_1	False

DNS configuration					
No DNS server address is configured.					
Display\General\Display standby mode					
Time to standby mode	30 minutes				
Display\General\Energy saving mode					
Time to energy saving mode	15 minutes				
Display\General\Display language					
Default language on display	English				
Display\Automatic update					
Time to update	5 seconds				
Display\Password\Display protection					
Enable write access	True	**Enable display protection**	False		
Display\User-defined logo					
User logo activated	False	**Adapt logo**	False	**Resolution**	128x120
Company logo	---				

User interface languages	
Assign project language	**User interface languages**
English (United States)	German
English (United States)	English
English (United States)	French
English (United States)	Spanish
English (United States)	Italian
English (United States)	Japanese
English (United States)	Chinese (simplified)
English (United States)	Korean
English (United States)	Russian
English (United States)	Turkish
English (United States)	Portuguese (Brazil)

Time of day\Local time					
Time zone	(UTC) Dublin, Edinburgh, Lisbon, London				
Time of day\Daylight saving time					
Activate daylight saving time	True	**Difference between standard and daylight saving time**	60 mins		
Time of day\Daylight saving time\Start of daylight saving time					
Selection of the week	Last	**Selection of the weekday**	Sunday	**of**	March
at	01:00 a.m.				
Time of day\Daylight saving time\Start of standard time					
Selection of the week	Last	**Selection of the weekday**	Sunday	**of**	October
at	02:00 a.m.				
Protection					
Level of protection	Full access (no protection)				
Protection\Connection mechanisms					
Permit access with PUT/GET communication from remote partner	False				
Protection\Security event					
Summarize security events in case of high message volume	True	**Length of an interval**	20	**Unit**	seconds
OPC UA\Accessibility of the server					
Activate OPC UA server	False				
System power supply\General					
General	Connection to supply voltage L+				

System power supply\Power segment overview		
Module	**Slot**	**Supply/consumption**
PLC_1	1	10.00W
	Summary	10.00W

Configuration control\Configuration control for central configuration	
Allow reconfiguration of device via the user program	False

Connection resources\				
	Station resources - Reserved - Maximum	**Station resources - Reserved - Configured**	**Station resources - Dynamic - Configured**	**Module resources - PLC_1 [CPU 1512C-1 PN] - Configured**
Maximum number of resources:		10	78	88
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	4	0	0	0
S7 communication:	0	-	0	0
Open user communication:	0	-	0	0
Web communication:	2	-	-	-
OPC UA client/server communication:	0	-	-	-
Other communication:	-	-	0	0
Total resources used:		0	0	0
Available resources:		10	78	88

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	OB	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	9	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	10 Bytes	-	0	1 8
O	0	3	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 8
I	10	11	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
O	4	5	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
I	12	13	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
O	6	7	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
I	14	29	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 16
O	8	19	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 16
I	30	45	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 17
O	20	31	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 17
I	46	61	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 18
O	32	43	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 18
I	62	77	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 19
O	44	55	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 19
I	78	93	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 20
O	56	67	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 20
I	94	109	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 21
O	68	79	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 21
I	110	113	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 32
O	80	91	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 32
I	114	117	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 33
O	92	103	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 33
I	118	121	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 34
O	104	115	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 34
I	122	125	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 35
O	116	127	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 35

Runtime licenses\OPC UA\Runtime licenses			
Type of required license	None	**Type of purchased license**	No license
Runtime licenses\ProDiag\Supervisions			
Number of used supervisions	0		
Runtime licenses\ProDiag\Runtime licenses			
Number of required licenses	None (<= 25 supervisions)	**Used ProDiag licenses**	No license
Runtime licenses\Energy Suite\Energy objects			
Number of configured energy objects	0		
Runtime licenses\Energy Suite\Runtime licenses			
Total number of licensed energy objects	0		
Runtime licenses\Energy Suite\Runtime licenses\Number of purchased licenses			
License type '5 energy objects'	No license	**License type '10 energy objects'**	No license

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN]

Software units

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Program blocks

Main [OB1]

Main Properties							
General							
Name	Main	**Number**	1	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title	"Main Program Sweep (Cycle)"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Remanence	Bool		=True, if remanent data are available
Temp			
Constant			

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Program blocks

Cyclic interrupt [OB30]

Cyclic interrupt Properties							
General							
Name	Cyclic interrupt	**Number**	30	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Event_Count	Int		Events discarded
Temp			
Constant			

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Program blocks

Cyclic interrupt_1 [OB31]

Cyclic interrupt_1 Properties							
General							
Name	Cyclic interrupt_1	**Number**	31	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Event_Count	Int		Events discarded
Temp			
Constant			

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN]

Technology objects

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN]

PLC tags

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / PLC tags

Default tag table [61]

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN]

PLC data types

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN]

Traces

Name

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Traces

Measurements

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Traces

Combined measurements

Name

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / OPC UA communication

Server interfaces

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / OPC UA communication

Client interfaces

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / PLC supervisions & alarms

Supervisions

This folder is empty.

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / PLC supervisions & alarms

PLC alarms

PLC alarms
No entries

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / PLC supervisions & alarms

System alarms

System alarms
No entries

OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN]

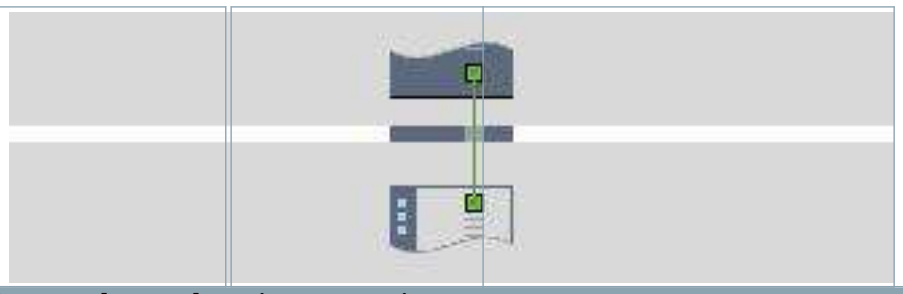
PLC alarm text lists

This folder is empty.

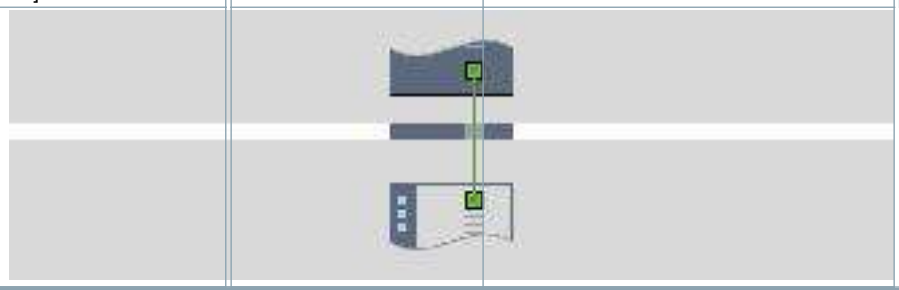
OB30- Cyclic Interrupt OBs / PLC_1 [CPU 1512C-1 PN] / Local modules

PLC_1 [CPU 1512C-1 PN]

PLC_1					
General\Project information					
Name	PLC_1	**Author**	Mmuhamed	**Comment**	
Rack	0	**Slot**	1		
General\Catalog information					
Short designation	CPU 1512C-1 PN	**Description**	CPU with display; work memory 250 KB code and 1 MB data; 48 ns bit operation time; 4-stage protection concept, technology functions: motion control, closed-loop control, counting and measuring; tracing; Runtime options; for all PROFINET interfaces: transport protocol TCP/IP, secure Open User Communication, S7 communication, S7 routing, IP forwarding, Web server, DNS client, OPC UA: Server DA, Client DA, methods, companion specifications; PROFINET IO controller, supports RT/IRT, performance upgrade PROFINET V2.3, 2 ports, I-Device, MRP, MRPD, isochronous mode, Routing, runtime options; firmware V2.8 with DI32/DQ32, AI5/AQ2: Digital input module DI16 x DC24V, grouping 16; Digital output module DQ16 x DC24V/ 0.5A, grouping 16; Analog input module AI4 x U/I, AI 1xRTD, 16-bit, grouping 5; Analog output module AQ2 x U/I, 16-bit, grouping 2; 6 channels for counting and measuring with incremental encoders 24V (up to 100kHz); 4 channels for PTO, pulse width modulation, frequency output (up to 100kHz)	**Article number**	6ES7 512-1CK01-0AB0
Firmware version	V2.8				
General\Identification & Maintenance					
Plant designation		**Location identifier**		**Installation date**	2023-03-26 08:03:30.533
Additional information					
General\Checksums					
Text lists	FA 70 E8 75 1D 5A 8E 29	**Software**	Not available (compile necessary)		
PROFINET interface [X1]\General					
Name	PROFINET interface_1	**Author**	Mmuhamed	**Comment**	
PROFINET interface [X1]\Ethernet addresses\Interface networked with					
Subnet:	Not connected				
PROFINET interface [X1]\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.1	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface [X1]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_1.profinet interface_1
Converted name:	plcxb1.profinetxainterfacexb1036c	**Device number:**	0		
PROFINET interface [X1]\Time-of-day synchronization\NTP mode					
Note	Time synchronization for all PROFINET interfaces take place within the settings for time synchronization of the PROFINET interface [X1].	**Enable time synchronization via NTP server**	False		IP addresses
Server 1	0.0.0.0	**Server 2**	0.0.0.0	**Server 3**	0.0.0.0
Server 4	0.0.0.0	**Update interval**	10s		
PROFINET interface [X1]\Operating mode					
IO controller	True	**IO system**		**Device number**	0
IO device	False				
PROFINET interface [X1]\Advanced options\Interface options					
Call the user program if communication errors occur	False	**Support device replacement without exchangeable medium**	True	**Permit overwriting of device names of all assigned IO devices**	False
Limit data infeed into the network	True	**Use IEC V2.2 LLDP mode**	False	**Keep-Alive connection monitoring:**	30s
PROFINET interface [X1]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X1]\Advanced options\Real time settings\Synchronization					
RT class:	RT,IRT				
PROFINET interface [X1]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%		
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\General					
Name	Port_1	**Author**	Mmuhamed	**Comment**	
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1 R]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Alternative partners**	False	**Partner port:**	Any partner
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\General					
Name	Port_2	**Author**	Mmuhamed	**Comment**	
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_2 [X1 P2 R]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Alternative partners**	False	**Partner port:**	Any partner
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X1]\Web server access					
Note	The Web server must also be activated in the properties of the PLC.	**Enable Web server via IP address of this interface**	False		
AI 5/AQ 2 [X10]\General					
Name	AI 5/AQ 2_1	**Comment**			
AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics					
Wire break	False	**Short circuit to ground**	False	**Overflow**	False
Underflow	False				
AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Output parameters					
Output type	Voltage	**Output range**	+/- 10V	**Reaction to CPU STOP**	Shutdown
Substitute value					
AI 5/AQ 2 [X10]\AI/AQ configuration\Value status (Quality Information)					
Value status	False				
AI 5/AQ 2 [X10]\Inputs\General\Measuring					
Interference frequency suppression	50Hz				
AI 5/AQ 2 [X10]\Inputs\Channel 0					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdgeEvent**	49272	**Event name:**	

Hardware interrupt:	0	**UpperLimitOne0**	UpperLimitOne0	**Channel number**	0
HwEventTypeLi-mit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdg-eEvent**	49288	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne0**	LowerLimitOne0	**Channel number**	0
HwEventTypeLi-mit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdg-eEvent**	49264	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo0**	UpperLimitTwo0	**Channel number**	0
HwEventTypeLi-mit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdg-eEvent**	49280	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo0**	LowerLimitTwo0	**Channel number**	0
HwEventTypeLi-mit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 1					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coeffi-cient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdg-eEvent**	49273	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne1**	UpperLimitOne1	**Channel number**	1
HwEventTypeLi-mit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdg-eEvent**	49289	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne1**	LowerLimitOne1	**Channel number**	1
HwEventTypeLi-mit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdg-eEvent**	49265	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo1**	UpperLimitTwo1	**Channel number**	1
HwEventTypeLi-mit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdg-eEvent**	49281	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo1**	LowerLimitTwo1	**Channel number**	1
HwEventTypeLi-mit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 2					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 2\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coeffi-cient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdg-eEvent**	49274	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne2**	UpperLimitOne2	**Channel number**	2
HwEventTypeLi-mit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdg-eEvent**	49290	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne2**	LowerLimitOne2	**Channel number**	2
HwEventTypeLi-mit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdg-eEvent**	49266	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo2**	UpperLimitTwo2	**Channel number**	2
HwEventTypeLi-mit2Overrun	6				

AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdgeEvent**	49282	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo2**	LowerLimitTwo2	**Channel number**	2
HwEventTypeLimit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 3					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 3\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdgeEvent**	49275	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne3**	UpperLimitOne3	**Channel number**	3
HwEventTypeLimit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdgeEvent**	49291	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne3**	LowerLimitOne3	**Channel number**	3
HwEventTypeLimit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdgeEvent**	49267	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo3**	UpperLimitTwo3	**Channel number**	3
HwEventTypeLimit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdgeEvent**	49283	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo3**	LowerLimitTwo3	**Channel number**	3
HwEventTypeLimit2Underrun	5				
AI 5/AQ 2 [X10]\Inputs\Channel 4					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Diagnostics					
Overflow	False	**Underflow**	False	**Wire break**	False
Current limit for wire break diagnostics					
AI 5/AQ 2 [X10]\Inputs\Channel 4\Measuring					
Measurement type	Resistance	**Measuring range**	600Ohm	**Temperature coefficient**	
Temperature unit		**Smoothing**	None		
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
High limit 1		**Low limit 1**		**High limit 2**	
Low limit 2					
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt high limit 1	0	**RidPrefixFallingEdgeEvent**	49276	**Event name:**	
Hardware interrupt:	0	**UpperLimitOne4**	UpperLimitOne4	**Channel number**	4
HwEventTypeLimit1Overrun	4				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt low limit 1	0	**RidPrefixFallingEdgeEvent**	49292	**Event name:**	
Hardware interrupt:	0	**LowerLimitOne4**	LowerLimitOne4	**Channel number**	4
HwEventTypeLimit1Underrun	3				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt high limit 2	0	**RidPrefixFallingEdgeEvent**	49268	**Event name:**	
Hardware interrupt:	0	**UpperLimitTwo4**	UpperLimitTwo4	**Channel number**	4
HwEventTypeLimit2Overrun	6				
AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts					
Hardware interrupt low limit 2	0	**RidPrefixFallingEdgeEvent**	49284	**Event name:**	
Hardware interrupt:	0	**LowerLimitTwo4**	LowerLimitTwo4	**Channel number**	4
HwEventTypeLimit2Underrun	5				
AI 5/AQ 2 [X10]\Outputs\Channel 0					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Outputs\Channel 0\Diagnostics					
Wire break	False	**Short circuit to ground**	False	**Overflow**	False
Underflow	False				
AI 5/AQ 2 [X10]\Outputs\Channel 0\Output					
Output type	Voltage	**Output range**	+/- 10V	**Reaction to CPU STOP**	Shutdown
Substitute value					

AI 5/AQ 2 [X10]\Outputs\Channel 1					
Parameter settings	Manual				
AI 5/AQ 2 [X10]\Outputs\Channel 1\Diagnostics					
Wire break	False	**Short circuit to ground**	False	**Overflow**	False
Underflow	False				
AI 5/AQ 2 [X10]\Outputs\Channel 1\Output					
Output type	Voltage	**Output range**	+/- 10V	**Reaction to CPU STOP**	Shutdown
Substitute value					
AI 5/AQ 2 [X10]\I/O addresses\Input addresses					
Start address	0	**End address**	9	**Organization block**	0
Process image	0				
AI 5/AQ 2 [X10]\I/O addresses\Output addresses					
Start address	0	**End address**	3	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X11]\General					
Name	DI 16/DQ 16_1	**Comment**			
DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\DI/DQ configuration\Value status (Quality Information)					
Value status	False				
DI 16/DQ 16 [X11]\Inputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49152	**Event name:**	
Hardware interrupt:	0	**Rising edge0**	Rising edge0	**Channel number**	0
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49280	**Event name:**	
Hardware interrupt:	0	**Falling edge0**	Falling edge0	**Channel number**	0
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49153	**Event name:**	
Hardware interrupt:	0	**Rising edge1**	Rising edge1	**Channel number**	1
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49281	**Event name:**	
Hardware interrupt:	0	**Falling edge1**	Falling edge1	**Channel number**	1
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49154	**Event name:**	
Hardware interrupt:	0	**Rising edge2**	Rising edge2	**Channel number**	2
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49282	**Event name:**	
Hardware interrupt:	0	**Falling edge2**	Falling edge2	**Channel number**	2
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 3\Input parameters					
Input delay	3.2ms				

DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49155	**Event name:**	
Hardware interrupt:	0	**Rising edge3**	Rising edge3	**Channel number**	3
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49283	**Event name:**	
Hardware interrupt:	0	**Falling edge3**	Falling edge3	**Channel number**	3
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49156	**Event name:**	
Hardware interrupt:	0	**Rising edge4**	Rising edge4	**Channel number**	4
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49284	**Event name:**	
Hardware interrupt:	0	**Falling edge4**	Falling edge4	**Channel number**	4
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49157	**Event name:**	
Hardware interrupt:	0	**Rising edge5**	Rising edge5	**Channel number**	5
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49285	**Event name:**	
Hardware interrupt:	0	**Falling edge5**	Falling edge5	**Channel number**	5
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49158	**Event name:**	
Hardware interrupt:	0	**Rising edge6**	Rising edge6	**Channel number**	6
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49286	**Event name:**	
Hardware interrupt:	0	**Falling edge6**	Falling edge6	**Channel number**	6
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49159	**Event name:**	
Hardware interrupt:	0	**Rising edge7**	Rising edge7	**Channel number**	7
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49287	**Event name:**	
Hardware interrupt:	0	**Falling edge7**	Falling edge7	**Channel number**	7
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 8\Diagnostics					
No supply voltage L+	False				

DI 16/DQ 16 [X11]\Inputs\Channel 8\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49160	**Event name:**	
Hardware interrupt:	0	**Rising edge8**	Rising edge8	**Channel number**	8
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49288	**Event name:**	
Hardware interrupt:	0	**Falling edge8**	Falling edge8	**Channel number**	8
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49161	**Event name:**	
Hardware interrupt:	0	**Rising edge9**	Rising edge9	**Channel number**	9
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49289	**Event name:**	
Hardware interrupt:	0	**Falling edge9**	Falling edge9	**Channel number**	9
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 10					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49162	**Event name:**	
Hardware interrupt:	0	**Rising edge10**	Rising edge10	**Channel number**	10
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49290	**Event name:**	0
Hardware interrupt:	0	**Falling edge10**	Falling edge10	**Channel number**	10
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 11					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49163	**Event name:**	
Hardware interrupt:	0	**Rising edge11**	Rising edge11	**Channel number**	11
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49291	**Event name:**	
Hardware interrupt:	0	**Falling edge11**	Falling edge11	**Channel number**	11
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49164	**Event name:**	
Hardware interrupt:	0	**Rising edge12**	Rising edge12	**Channel number**	12
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49292	**Event name:**	
Hardware interrupt:	0	**Falling edge12**	Falling edge12	**Channel number**	12
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 13					
Parameter settings	From template				

DI 16/DQ 16 [X11]\Inputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 13\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49165	**Event name:**	
Hardware interrupt:	0	**Rising edge13**	Rising edge13	**Channel number**	13
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49293	**Event name:**	
Hardware interrupt:	0	**Falling edge13**	Falling edge13	**Channel number**	13
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49166	**Event name:**	
Hardware interrupt:	0	**Rising edge14**	Rising edge14	**Channel number**	14
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49294	**Event name:**	
Hardware interrupt:	0	**Falling edge14**	Falling edge14	**Channel number**	14
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Inputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49167	**Event name:**	
Hardware interrupt:	0	**Rising edge15**	Rising edge15	**Channel number**	15
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49295	**Event name:**	
Hardware interrupt:	0	**Falling edge15**	Falling edge15	**Channel number**	15
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X11]\Outputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 0\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 1\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 2\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 3\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 4\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 5\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 5\Output parameters					
Reaction to CPU STOP	Shutdown				

DI 16/DQ 16 [X11]\Outputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 6\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 7\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 8\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 8\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 9\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 10					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 10\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 11					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 11\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 12\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 13					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 13\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 14\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\Outputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X11]\Outputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X11]\Outputs\Channel 15\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X11]\I/O addresses\Input addresses					
Start address	10.0	**End address**	11.7	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X11]\I/O addresses\Output addresses					
Start address	4.0	**End address**	5.7	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X12]\General					
Name	DI 16/DQ 16_2	**Comment**			
DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\DI/DQ configuration\Value status (Quality Information)					
Value status	False				
DI 16/DQ 16 [X12]\Inputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 0\Input parameters					
Input delay	3.2ms				

DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49152	**Event name:**	
Hardware interrupt:	0	**Rising edge0**	Rising edge0	**Channel number**	0
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49280	**Event name:**	
Hardware interrupt:	0	**Falling edge0**	Falling edge0	**Channel number**	0
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49153	**Event name:**	
Hardware interrupt:	0	**Rising edge1**	Rising edge1	**Channel number**	1
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49281	**Event name:**	
Hardware interrupt:	0	**Falling edge1**	Falling edge1	**Channel number**	1
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49154	**Event name:**	
Hardware interrupt:	0	**Rising edge2**	Rising edge2	**Channel number**	2
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49282	**Event name:**	
Hardware interrupt:	0	**Falling edge2**	Falling edge2	**Channel number**	2
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49155	**Event name:**	
Hardware interrupt:	0	**Rising edge3**	Rising edge3	**Channel number**	3
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49283	**Event name:**	
Hardware interrupt:	0	**Falling edge3**	Falling edge3	**Channel number**	3
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49156	**Event name:**	
Hardware interrupt:	0	**Rising edge4**	Rising edge4	**Channel number**	4
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49284	**Event name:**	
Hardware interrupt:	0	**Falling edge4**	Falling edge4	**Channel number**	4
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 5\Diagnostics					
No supply voltage L+	False				

DI 16/DQ 16 [X12]\Inputs\Channel 5\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49157	**Event name:**	
Hardware interrupt:	0	**Rising edge5**	Rising edge5	**Channel number**	5
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49285	**Event name:**	
Hardware interrupt:	0	**Falling edge5**	Falling edge5	**Channel number**	5
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49158	**Event name:**	
Hardware interrupt:	0	**Rising edge6**	Rising edge6	**Channel number**	6
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49286	**Event name:**	
Hardware interrupt:	0	**Falling edge6**	Falling edge6	**Channel number**	6
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49159	**Event name:**	
Hardware interrupt:	0	**Rising edge7**	Rising edge7	**Channel number**	7
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49287	**Event name:**	
Hardware interrupt:	0	**Falling edge7**	Falling edge7	**Channel number**	7
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49160	**Event name:**	
Hardware interrupt:	0	**Rising edge8**	Rising edge8	**Channel number**	8
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49288	**Event name:**	
Hardware interrupt:	0	**Falling edge8**	Falling edge8	**Channel number**	8
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49161	**Event name:**	
Hardware interrupt:	0	**Rising edge9**	Rising edge9	**Channel number**	9
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49289	**Event name:**	
Hardware interrupt:	0	**Falling edge9**	Falling edge9	**Channel number**	9
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 10					
Parameter settings	From template				

DI 16/DQ 16 [X12]\Inputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 10\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49162	**Event name:**	
Hardware interrupt:	0	**Rising edge10**	Rising edge10	**Channel number**	10
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49290	**Event name:**	0
Hardware interrupt:	0	**Falling edge10**	Falling edge10	**Channel number**	10
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 11					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49163	**Event name:**	
Hardware interrupt:	0	**Rising edge11**	Rising edge11	**Channel number**	11
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49291	**Event name:**	
Hardware interrupt:	0	**Falling edge11**	Falling edge11	**Channel number**	11
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49164	**Event name:**	
Hardware interrupt:	0	**Rising edge12**	Rising edge12	**Channel number**	12
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49292	**Event name:**	
Hardware interrupt:	0	**Falling edge12**	Falling edge12	**Channel number**	12
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 13					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49165	**Event name:**	
Hardware interrupt:	0	**Rising edge13**	Rising edge13	**Channel number**	13
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49293	**Event name:**	
Hardware interrupt:	0	**Falling edge13**	Falling edge13	**Channel number**	13
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Inputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49166	**Event name:**	
Hardware interrupt:	0	**Rising edge14**	Rising edge14	**Channel number**	14
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49294	**Event name:**	
Hardware interrupt:	0	**Falling edge14**	Falling edge14	**Channel number**	14
HwEventTypeFallingEdge	2				

DI 16/DQ 16 [X12]\Inputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Input parameters					
Input delay	3.2ms				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts					
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49167	**Event name:**	
Hardware interrupt:	0	**Rising edge15**	Rising edge15	**Channel number**	15
HwEventTypeRisingEdge	1				
DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts					
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49295	**Event name:**	
Hardware interrupt:	0	**Falling edge15**	Falling edge15	**Channel number**	15
HwEventTypeFallingEdge	2				
DI 16/DQ 16 [X12]\Outputs\Channel 0					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 0\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 0\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 1					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 1\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 1\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 2					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 2\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 2\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 3					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 3\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 3\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 4					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 4\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 4\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 5					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 5\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 5\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 6					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 6\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 6\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 7					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 7\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 7\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 8					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 8\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 8\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 9					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 9\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 9\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 10					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 10\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 10\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 11					
Parameter settings	From template				

DI 16/DQ 16 [X12]\Outputs\Channel 11\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 11\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 12					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 12\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 12\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 13					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 13\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 13\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 14					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 14\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 14\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\Outputs\Channel 15					
Parameter settings	From template				
DI 16/DQ 16 [X12]\Outputs\Channel 15\Diagnostics					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 15\Output parameters					
No supply voltage L+	False				
DI 16/DQ 16 [X12]\Outputs\Channel 15\Output parameters					
Reaction to CPU STOP	Shutdown				
DI 16/DQ 16 [X12]\I/O addresses\Input addresses					
Start address	12.0	**End address**	13.7	**Organization block**	0
Process image	0				
DI 16/DQ 16 [X12]\I/O addresses\Output addresses					
Start address	6.0	**End address**	7.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\CPU 1511C compatibility					
Front connector assignment like CPU 1511C	False				
High speed counters (HSC)\HSC 1\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 1\General\Project information					
Name	HSC_1	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 1\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 1\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 1\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				

High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirection-ChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirec-tionChanged	10				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossin-gEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroC-rossing	7				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCom-pare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCom-pare0	5				
High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCom-pare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCom-pare1	6				
High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 1\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in incre-ments)	0				
High speed counters (HSC)\HSC 1\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 1\Hardware inputs/outputs					
Pulse input (A)	X11, Clamp 1 (DI0 / %I10.0)	**Direction input (B)**	X11, Clamp 2 (DI1 / %I10.1)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 1\I/O addresses\Input addresses					
Start address	14.0	**End address**	29.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 1\I/O addresses\Output addresses					
Start address	8.0	**End address**	19.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 2\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 2\General\Project information					
Name	HSC_2	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 2\Channel 0\Operating mode					
Selection of operat-ing mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 2\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 2\Channel 0\Diagnostic interrupts					
Enable diagnostic in-terrupts	False				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureE-vent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an ex-ternal signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				

High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 2\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 2\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 2\Hardware inputs/outputs					
Pulse input (A)	X11, Clamp 4 (DI3 / %I10.3)	**Direction input (B)**	X11, Clamp 5 (DI4 / %I10.4)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 2\I/O addresses\Input addresses					
Start address	30.0	**End address**	45.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 2\I/O addresses\Output addresses					
Start address	20.0	**End address**	31.7	**Organization block**	0
Process image	0				

High speed counters (HSC)\HSC 3\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 3\General\Project information					
Name	HSC_3	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 3\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 3\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 3\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected

High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 3\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 3\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 3\Hardware inputs/outputs					
Pulse input (A)	X11, Clamp 7 (DI6 / %I10.6)	**Direction input (B)**	X11, Clamp 8 (DI7 / %I10.7)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 3\I/O addresses\Input addresses					
Start address	46.0	**End address**	61.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 3\I/O addresses\Output addresses					
Start address	32.0	**End address**	43.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 4\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 4\General\Project information					
Name	HSC_4	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 4\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 4\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 4\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				

High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 4\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 4\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 4\Hardware inputs/outputs					
Pulse input (A)	X12, Clamp 1 (DI0 / %I12.0)	**Direction input (B)**	X12, Clamp 2 (DI1 / %I12.1)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 4\I/O addresses\Input addresses					
Start address	62.0	**End address**	77.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 4\I/O addresses\Output addresses					
Start address	44.0	**End address**	55.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 5\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 5\General\Project information					
Name	HSC_5	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 5\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 5\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0
High speed counters (HSC)\HSC 5\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				

High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				
High speed counters (HSC)\HSC 5\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 5\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 5\Hardware inputs/outputs					
Pulse input (A)	X12, Clamp 4 (DI3 / %I12.3)	**Direction input (B)**	X12, Clamp 5 (DI4 / %I12.4)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 5\I/O addresses\Input addresses					
Start address	78.0	**End address**	93.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 5\I/O addresses\Output addresses					
Start address	56.0	**End address**	67.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 6\General\Enable					
Activate this high-speed counter	False				
High speed counters (HSC)\HSC 6\General\Project information					
Name	HSC_6	**Author**	Mmuhamed	**Comment**	
High speed counters (HSC)\HSC 6\Channel 0\Operating mode					
Selection of operating mode	Operating with technology object "Counting and measurement"				
High speed counters (HSC)\HSC 6\Channel 0\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for DQ0**	0	**Substitute value for DQ1**	0

High speed counters (HSC)\HSC 6\Channel 0\Diagnostic interrupts					
Enable diagnostic interrupts	False				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
New capture value available	0	**RidPrefixCaptureEvent**	49280	**Event name**	
Hardware interrupt	0	**Capture value0**	Capture value0	**Channel number**	0
HwEventTypeCapture	8				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Synchronization of the counter by an external signal	0	**RidPrefixSyncEvent**	49296	**Event name**	
Hardware interrupt	0	**Synchronization0**	Synchronization0	**Channel number**	0
HwEventTypeSync	9				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate start	0	**RidPrefixGateStartEvent**	49168	**Event name**	
Hardware interrupt	0	**Gate start0**	Gate start0	**Channel number**	0
HwEventTypeGateStart	1				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events					
Gate stop	0	**RidPrefixGateStopEvent**	49184	**Event name**	
Hardware interrupt	0	**Gate stop0**	Gate stop0	**Channel number**	0
HwEventTypeGateStop	2				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Overflow (high counting limit violated)	0	**RidPrefixOverflowEvent**	49200	**Event name**	
Hardware interrupt	0	**Overflow0**	Overflow0	**Channel number**	0
HwEventTypeOverflow	3				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Underflow (low counting limit violated)	0	**RidPrefixUnderflowEvent**	49216	**Event name**	
Hardware interrupt	0	**Underflow0**	Underflow0	**Channel number**	0
HwEventTypeUnderflow	4				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Direction reversal	0	**RidPrefixDirectionChangedEvent**	49312	**Event name**	
Hardware interrupt	0	**Direction reversal0**	Direction reversal0	**Channel number**	0
HwEventTypeDirectionChanged	10				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Zero crossing	0	**RidPrefixZeroCrossingEvent**	49264	**Event name**	
Hardware interrupt	0	**Zero crossing0**	Zero crossing0	**Channel number**	0
HwEventTypeZeroCrossing	7				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ0 occurred	0	**RidPrefixCompare0Event**	49232	**Event name**	
Hardware interrupt	0	**Compare event DQ00**	Compare event DQ00	**Channel number**	0
HwEventTypeCompare0	5				
High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value					
Comparison event for DQ1 occurred	0	**RidPrefixCompare1Event**	49248	**Event name**	
Hardware interrupt	0	**Compare event DQ10**	Compare event DQ10	**Channel number**	0
HwEventTypeCompare1	6				
High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Specify input signals/encoder type					
Signal type	Pulse (A) and direction (B)	**Invert direction**	False		
High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Additional parameters					
Signal evaluation	Single	**Filter frequency**	100 kHz	**Reaction to signal N**	No reaction to signal N
High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counting limits and start value					
High counting limit	2147483647	**Start value**	0	**Low counting limit**	-2147483648
High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counter behavior at limits and gate start					
Reaction to violation of a counting limit	Continue counting	**Reset when counting limit is violated**	To opposite counting limit	**Reaction to gate start**	Continue with current value
High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0					
Set function of DI	Digital input without function	**HSC DI0**	None	**Input delay**	No input for DI0 selected
High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1					
Set function of DI	Digital input without function	**HSC DI1**	None	**Input delay**	No input for DI1 selected
High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0					
Set output	Between comparison value 0 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ0**	0
HSC DQ0	Only available via feedback interface				
High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1					
Set output	Between comparison value 1 and high limit	**Comparison value 0**	0	**Comparison value 1**	10
Count direction	In both directions	**Pulse duration**	500.0ms	**Substitute value for DQ1**	0
HSC DQ1	None				

High speed counters (HSC)\HSC 6\Channel 0\Hysteresis\Set hysteresis range					
Hysteresis (in increments)	0				
High speed counters (HSC)\HSC 6\Channel 0\Measured value\Specify measured value					
Measured variable	Frequency	**Update time**	10.000ms		
High speed counters (HSC)\HSC 6\Hardware inputs/outputs					
Pulse input (A)	X12, Clamp 7 (DI6 / %I12.6)	**Direction input (B)**	X12, Clamp 8 (DI7 / %I12.7)	**Reset input (N)**	None
HSC DI0	None	**HSC DI1**	None	**HSC DQ0**	Only available via feedback interface
HSC DQ1	None				
High speed counters (HSC)\HSC 6\I/O addresses\Input addresses					
Start address	94.0	**End address**	109.7	**Organization block**	0
Process image	0				
High speed counters (HSC)\HSC 6\I/O addresses\Output addresses					
Start address	68.0	**End address**	79.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\CPU 1511C compatibility					
Front connector assignment like CPU 1511C	False				
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information					
Name	Pulse_1	**Comment**			
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO1/PWM1\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		
Pulse generators (PTO/PWM)\PTO1/PWM1\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO1/PWM1\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 21 (DQ0 / %Q4.0): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO1/PWM1\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Input addresses					
Start address	110.0	**End address**	113.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses					
Start address	80.0	**End address**	91.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO2/PWM2\General\Project information					
Name	Pulse_2	**Comment**			
Pulse generators (PTO/PWM)\PTO2/PWM2\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO2/PWM2\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		
Pulse generators (PTO/PWM)\PTO2/PWM2\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO2/PWM2\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 23 (DQ2 / %Q4.2): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO2/PWM2\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Input addresses					
Start address	114.0	**End address**	117.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Output addresses					
Start address	92.0	**End address**	103.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO3/PWM3\General\Project information					
Name	Pulse_3	**Comment**			
Pulse generators (PTO/PWM)\PTO3/PWM3\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO3/PWM3\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		
Pulse generators (PTO/PWM)\PTO3/PWM3\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO3/PWM3\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 25 (DQ4 / %Q4.4): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO3/PWM3\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Input addresses					
Start address	118.0	**End address**	121.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Output addresses					
Start address	104.0	**End address**	115.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO4/PWM4\General\Project information					
Name	Pulse_4	**Comment**			
Pulse generators (PTO/PWM)\PTO4/PWM4\General\Operating mode					
Operating mode	Deactivated				
Pulse generators (PTO/PWM)\PTO4/PWM4\Reaction to CPU STOP					
Reaction to CPU STOP	Output substitute value	**Substitute value for pulse output (DQA)**	0		

Pulse generators (PTO/PWM)\PTO4/PWM4\Diagnostic interrupts					
No supply voltage L+	False				
Pulse generators (PTO/PWM)\PTO4/PWM4\Hardware inputs/outputs					
Pulse output (DQA)	X11, Clamp 27 (DQ6 / %Q4.6): 10 kHz / 0.5 A or 100 kHz / 0.1 A	**High-speed output (0.1 A)**	False		
Pulse generators (PTO/PWM)\PTO4/PWM4\Parameters					
Output format	Per 100				
Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Input addresses					
Start address	122.0	**End address**	125.7	**Organization block**	0
Process image	0				
Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Output addresses					
Start address	116.0	**End address**	127.7	**Organization block**	0
Process image	0				
Startup					
Startup after POWER ON	Warm restart - Operating mode before POWER OFF	**Comparison preset to actual configuration**	Startup CPU even if mismatch	**Configuration time**	60000ms
Cycle					
Maximum cycle time	150ms			**Enable minimum cycle time for cyclic OBs**	True
Minimum cycle time	1ms				
Communication load					
Cycle load due to communication	50%				
System and clock memory\System memory bits					
Enable the use of system memory byte	False	**Address of system memory byte (MBx)**	1	**First cycle**	
Diagnostic status changed		**Always 1 (high)**		**Always 0 (low)**	
System and clock memory\Clock memory bits					
Enable the use of clock memory byte	False	**Address of clock memory byte (MBx)**	0	**10 Hz clock**	
5 Hz clock		**2.5 Hz clock**		**2 Hz clock**	
1.25 Hz clock		**1 Hz clock**		**0.625 Hz clock**	
0.5 Hz clock					
SIMATIC Memory Card\Diagnostics					
Aging of the SIMATIC memory card	False	**Threshold value**	80%		
System diagnostics\General					
Activate system diagnostics for this device	True	**Report network faults as maintenance instead of fault**	False		
PLC alarms\General					
Central alarm management in the PLC	True				
Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	True		
Web server\Automatic update					
Enable automatic update	True	**Update interval**	0s		

Web server\User management

User name	User rights
Everybody	

Web server\User-defined web pages

Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number
		index.htm	.htm;.html	333	334

Web server\Overview of interfaces

Device	Interface	Enabled web server access
PLC_1	PROFINET interface_1	False

DNS configuration					
No DNS server address is configured.					
Display\General\Display standby mode					
Time to standby mode	30 minutes				
Display\General\Energy saving mode					
Time to energy saving mode	15 minutes				
Display\General\Display language					
Default language on display	English				
Display\Automatic update					
Time to update	5 seconds				
Display\Password\Display protection					
Enable write access	True	**Enable display protection**	False		
Display\User-defined logo					
User logo activated	False	**Adapt logo**	False	**Resolution**	128x120
Company logo	---				

User interface languages	
Assign project language	**User interface languages**
English (United States)	German
English (United States)	English
English (United States)	French
English (United States)	Spanish
English (United States)	Italian
English (United States)	Japanese
English (United States)	Chinese (simplified)
English (United States)	Korean
English (United States)	Russian
English (United States)	Turkish
English (United States)	Portuguese (Brazil)

Time of day\Local time					
Time zone	(UTC) Dublin, Edinburgh, Lisbon, London				
Time of day\Daylight saving time					
Activate daylight saving time	True	**Difference between standard and daylight saving time**	60 mins		
Time of day\Daylight saving time\Start of daylight saving time					
Selection of the week	Last	**Selection of the weekday**	Sunday	**of**	March
at	01:00 a.m.				
Time of day\Daylight saving time\Start of standard time					
Selection of the week	Last	**Selection of the weekday**	Sunday	**of**	October
at	02:00 a.m.				
Protection					
Level of protection	Full access (no protection)				
Protection\Connection mechanisms					
Permit access with PUT/GET communication from remote partner	False				
Protection\Security event					
Summarize security events in case of high message volume	True	**Length of an interval**	20	**Unit**	seconds
OPC UA\Accessibility of the server					
Activate OPC UA server	False				
System power supply\General					
General	Connection to supply voltage L+				

System power supply\Power segment overview		
Module	**Slot**	**Supply/consumption**
PLC_1	1	10.00W
	Summary	10.00W

Configuration control\Configuration control for central configuration	
Allow reconfiguration of device via the user program	False

Connection resources\				
	Station resources - Reserved - Maximum	**Station resources - Reserved - Configured**	**Station resources - Dynamic - Configured**	**Module resources - PLC_1 [CPU 1512C-1 PN] - Configured**
Maximum number of resources:		10	78	88
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	4	0	0	0
S7 communication:	0	-	0	0
Open user communication:	0	-	0	0
Web communication:	2	-	-	-
OPC UA client/server communication:	0	-	-	-
Other communication:	-	-	0	0
Total resources used:		0	0	0
Available resources:		10	78	88

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	OB	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	9	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	10 Bytes	-	0	1 8
O	0	3	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 8
I	10	11	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
O	4	5	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
I	12	13	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
O	6	7	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
I	14	29	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 16
O	8	19	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 16
I	30	45	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 17
O	20	31	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 17
I	46	61	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 18
O	32	43	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 18
I	62	77	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 19
O	44	55	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 19
I	78	93	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 20
O	56	67	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 20
I	94	109	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 21
O	68	79	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 21
I	110	113	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 32
O	80	91	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 32
I	114	117	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 33
O	92	103	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 33
I	118	121	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 34
O	104	115	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 34
I	122	125	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 35
O	116	127	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 35

Runtime licenses\OPC UA\Runtime licenses			
Type of required license	None	**Type of purchased license**	No license
Runtime licenses\ProDiag\Supervisions			
Number of used supervisions	0		
Runtime licenses\ProDiag\Runtime licenses			
Number of required licenses	None (<= 25 supervisions)	**Used ProDiag licenses**	No license
Runtime licenses\Energy Suite\Energy objects			
Number of configured energy objects	0		
Runtime licenses\Energy Suite\Runtime licenses			
Total number of licensed energy objects	0		
Runtime licenses\Energy Suite\Runtime licenses\Number of purchased licenses			
License type '5 energy objects'	No license	**License type '10 energy objects'**	No license

OB30- Cyclic Interrupt OBs

Ungrouped devices

This folder is empty.

OB30- Cyclic Interrupt OBs

Security settings

This folder is empty.

OB30- Cyclic Interrupt OBs / Cross-device functions / Project traces

Measurements

This folder is empty.

OB30- Cyclic Interrupt OBs / Common data

Alarm classes

Alarm classes			
Name	**Display name**	**Acknowledgment**	**Priority**
Acknowledgement	A	True	0
No Acknowledgement	NA	False	0

OB30- Cyclic Interrupt OBs / Common data

Logs

This folder is empty.

OB30- Cyclic Interrupt OBs / Languages & resources

Project languages

Languages
Reference language
English (United States)

Editing language
English (United States)

Other project languages
Empty

OB30- Cyclic Interrupt OBs / Languages & resources / Project texts

Project texts

Project texts		
English (United States)	**Category**	**Reference**
"Main Program Sweep (Cycle)"	Block comment	OB30- Cyclic Interrupt OBs\PLC_1 [CPU 1512C-1 PN]\Program blocks\Main [OB1]\Block title
A	Alarm class text	OB30- Cyclic Interrupt OBs\Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
A	Alarm class text	OB30- Cyclic Interrupt OBs\Acknowledgement\ShortName
NA	Alarm class text	OB30- Cyclic Interrupt OBs\No Acknowledgement\AlarmClassData_IDisplayNaming_Display-Name
NA	Alarm class text	OB30- Cyclic Interrupt OBs\No Acknowledgement\ShortName