



Totally Integrated Automation Portal					
<b>OrganizationBlocksTutorial</b>					
<b>Project</b>					
<b>Name:</b>	OrganizationBlocksTutorial	<b>Creation time:</b>	3/21/2023 5:33:26 AM	<b>Last change</b>	3/23/2023 12:59:40 PM
<b>Last modified by:</b>	Mmuhamed	<b>Version:</b>		<b>Author:</b>	MahmoudSalama
<b>Comment:</b>					
<b>Operating system</b>					
<b>Name</b>			<b>Description</b>		
Operating system			Microsoft Windows 10 Pro		
Version of the operating system			6.3.9600.0		
Operating system service pack					
Version of the Internet Explorer			11.789.19041.0		
Computer name			MMUHAMED-D1		
User name			GULSANEGYPT\mmuhamed		
Installation path of the TIA Portal			C:\Program Files\Siemens\Automation\Portal V16		
<b>Components</b>					
<b>Name</b>	<b>Version</b>	<b>Release</b>			
TIA Portal Project Server V16 - TIA Portal Project Server Single SetupPackage V16.0 (MUSERVERV16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - SIMATIC S7-PLCSIM V16.0 (S7_PLCSIM_V16)	V16.0	V16.00.00.00_31.00.13.01			
TIA Administrator - AWB Licensing Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01			
TIA Administrator - AWB Software Management V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01			
TIA Administrator - TIA UMC Agent Configurator Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01			
TIA Administrator - TIA Administrator V1.0 SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01			
Siemens Totally Integrated Automation Portal V16 - HM All Editions Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - HM NoBasic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 0 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - Multiuser Client Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - Version Control Interface SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - STEP 7 Safety Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - STEP 7 Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 03 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 04 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package WCF-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01			
Siemens Totally Integrated Automation Portal V16 - TIACOMPCHCK Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - Openness SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - WinCC Transfer Mandatory Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
User Management Component - UserManagementComponentx64 V2.7 (UMC64)	V2.7	V02.07.00.00_04.06.00.07			
WinCC Runtime Advanced V16.0 - HMIRTM Tagging Package 01 Single SetupPackage V16.0 (HMIRTM_V11)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01			
SIMATIC HMI License Manager Panel Plugin (x64)	16.0.0.0	V16.00.00.00_31.02.00.01			
SIMATIC WinCC Runtime Advanced Driver (x64)	16.0.0.0	V16.00.00.00_31.02.00.01			
ETWEventCollector	16.0.0.0	V16.00.00.00_31.02.00.01			
SIMATIC NCM FWL 64	5.6.0.3	K5.6.0.3_1.1.0.2			
NCM GPRS 64	01.02.00.00	V1.2.0.0_2.1.0.1			
SIMATIC PLCSIM 64	16.00.00	16.00.00.00_01.00.02.01			
SIMATIC Device Drivers	9.2	09.02.04.00_01.04.00.05			
TelemetryConnector	1.0.2.57	V01.00.02.57_01.00.00.01			
Automation Software Updater	02.05.0300	V02.05.03.00_01.01.00.29			
SIEMENS OPC	3.9	03.09.10.00_01.04.00.08			
SIMATIC HMI ProSave	16.0.0.0	V16.00.00.00_31.02.00.01			
SIMATIC HMI Symbol Library	16.0.0.0	V16.00.00.00_31.02.00.01			
SIMATIC HMI Touch Input	16.0.0.0	V16.00.00.00_31.02.00.01			
SIMATIC Device Drivers WoW	29.2	29.02.04.00_01.04.00.05			
SIMATIC Event Database	5.6	05.06.02.00_01.01.00.01			

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<b>Name</b>	<b>Version</b>	<b>Release</b>	
SeCon	2.6	V02.06.01.00_01.08.00.01	
WinCC Runtime Advanced Simulator	16.0.0.0	V16.00.00.00_31.02.00.01	
<b>Products</b>			
<b>Name</b>	<b>Version</b>	<b>Release</b>	
TIA Portal Project Server	V16.0	V16.00.00.00_31.02.00.01	
SIMATIC S7-PLCSIM	V16.0	V16.00.00.00_31.00.13.01	
TIA Administrator	V1.0	01.00.02.00_01.10.00.01	
SIMATIC STEP 7 Prof - STEP 7 Safety - WinCC Adv	V16.0	V16.00.00.00_31.02.00.01	
User Management Component	V2.7	V02.07.00.00_00.00.00.00	
SIMATIC WinCC Runtime Advanced Simulation	V16.0	V16.00.00.00_31.02.00.01	
Automation License Manager	V6.0 + SP5 + Upd1	06.00.05.01_02.01.00.05	
S7-PLCSIM	V5.4 + SP8	V05.04.08.01_01.24.00.01	
SIMATIC ProSave	V16.0	V16.00.00.00_31.02.00.01	
S7-PCT	V3.5 + SP1	K3.5.1.0_1.19.0.1	

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<b>OrganizationBlocksTutorial</b>					
<b>PLC_1 [CPU 1512C-1 PN]</b>					
<b>PLC_1</b>					
<b>General\Project information</b>					
Name	PLC_1	Author	Mmuhamed	Comment	
Rack	0	Slot	1		
<b>General\Catalog information</b>					
Short designation	CPU 1512C-1 PN	Description	CPU with display; work memory 250 KB code and 1 MB data; 48 ns bit operation time; 4-stage protection concept, technology functions: motion control, closed-loop control, counting and measuring; tracing; Runtime options; for all PROFINET interfaces: transport protocol TCP/IP, secure Open User Communication, S7 communication, S7 routing, IP forwarding, Web server, DNS client, OPC UA: Server DA, Client DA, methods, companion specifications; PROFINET IO controller, supports RT/IRT, performance upgrade PROFINET V2.3, 2 ports, I-Device, MRP, MRPD, isochronous mode, Routing, runtime options; firmware V2.8 with DI32/DQ32, AI5/AQ2: Digital input module DI16 x DC24V, grouping 16; Digital output module DQ16 x DC24V/0.5A, grouping 16; Analog input module AI4 x U/I, AI 1xRTD, 16-bit, grouping 5; Analog output module AQ2 x U/I, 16-bit, grouping 2; 6 channels for counting and measuring with incremental encoders 24V (up to 100kHz); 4 channels for PTO, pulse width modulation, frequency output (up to 100kHz)	Article number	6ES7 512-1CK01-0AB0
Firmware version	V2.8				
<b>General\Identification &amp; Maintenance</b>					
Plant designation		Location identifier		Installation date	2023-03-21 05:38:08.327
Additional information					
<b>General\Checksums</b>					
Text lists	FA 70 E8 75 1D 5A 8E 29	Software	5E 57 4F FC 0E 48 39 CC		
<b>PROFINET interface [X1]\General</b>					
Name	PROFINET interface_1	Author	Mmuhamed	Comment	
<b>PROFINET interface [X1]\Ethernet addresses\Interface networked with</b>					
Subnet:	Not connected				
<b>PROFINET interface [X1]\Ethernet addresses\IP protocol</b>					
IP configuration	Set IP address in the project	IP address:	192.168.0.1	Subnet mask:	255.255.255.0
Use router	False				
<b>PROFINET interface [X1]\Ethernet addresses\PROFINET</b>					
PROFINET device name is set directly at the device	False	Generate PROFINET device name automatically	True	PROFINET device name:	plc_1.profinet interface_1
Converted name:	plcxb1.profinetxinterfacexb1036c	Device number:	0		
<b>PROFINET interface [X1]\Time-of-day synchronization\NTP mode</b>					
Note	Time synchronization for all PROFINET interfaces take place within the settings for time synchronization of the PROFINET interface [X1].	Enable time synchronization via NTP server	False		IP addresses
Server 1	0.0.0.0	Server 2	0.0.0.0	Server 3	0.0.0.0
Server 4	0.0.0.0	Update interval	10s		
<b>PROFINET interface [X1]\Operating mode</b>					
IO controller	True	IO system		Device number	0
IO device	False				
<b>PROFINET interface [X1]\Advanced options\Interface options</b>					
Call the user program if communication errors occur	False	Support device replacement without exchangeable medium	True	Permit overwriting of device names of all assigned IO devices	False
Limit data infeed into the network	True	Use IEC V2.2 LLDP mode	False	Keep-Alive connection monitoring:	30s
<b>PROFINET interface [X1]\Advanced options\Real time settings\IO communication</b>					
Send clock:	1.000ms				
<b>PROFINET interface [X1]\Advanced options\Real time settings\Synchronization</b>					
RT class:	RT,IRT				
<b>PROFINET interface [X1]\Advanced options\Real time settings\Real time options</b>					
Calculated bandwidth for cyclic IO data:	0.000ms	Calculated bandwidth for cyclic IO data:	0.000%		
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\General</b>					
Name	Port_1	Author	Mmuhamed	Comment	
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Local port:</b>					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1 R]	Medium:	Copper	Cable name:	---

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<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Partner port:</b>					
Monitoring of partner port is not possible	Alternative partners	False	Partner port:	Any partner	
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Activate</b>					
Activate this port for use	True				
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Connection</b>					
Transmission rate / duplex:	Automatic	Monitor	False	Enable autonegotiation	True
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Boundaries</b>					
End of detection of accessible devices	False	End of topology discovery	False	End of the sync domain	False
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\General</b>					
Name	Port_2	Author	Mmuhamed	Comment	
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Local port:</b>					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_2 [X1 P2 R]	Medium:	Copper	Cable name:	---
					
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Partner port:</b>					
Monitoring of partner port is not possible	Alternative partners	False	Partner port:	Any partner	
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Activate</b>					
Activate this port for use	True				
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Connection</b>					
Transmission rate / duplex:	Automatic	Monitor	False	Enable autonegotiation	True
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Boundaries</b>					
End of detection of accessible devices	False	End of topology discovery	False	End of the sync domain	False
<b>PROFINET interface [X1]\Web server access</b>					
Note	The Web server must also be activated in the properties of the PLC.	Enable Web server via IP address of this interface	False		
<b>AI 5/AQ 2 [X10]\General</b>					
Name	AI 5/AQ 2_1	Comment			
<b>AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics</b>					
Overflow	False	Underflow	False	Wire break	False
Current limit for wire break diagnostics					
<b>AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Measuring</b>					
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient	
Temperature unit	Smoothing		None		
<b>AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics</b>					
Wire break	False	Short circuit to ground	False	Overflow	False
Underflow	False				
<b>AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Output parameters</b>					
Output type	Voltage	Output range	+/- 10V	Reaction to CPU STOP	Shutdown
Substitute value					
<b>AI 5/AQ 2 [X10]\AI/AQ configuration\Value status (Quality Information)</b>					
Value status	False				
<b>AI 5/AQ 2 [X10]\Inputs\General\Measuring</b>					
Interference frequency suppression	50Hz				
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0</b>					
Parameter settings	Manual				
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Diagnostics</b>					
Overflow	False	Underflow	False	Wire break	False
Current limit for wire break diagnostics					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Measuring</b>					
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient	
Temperature unit	Smoothing		None		
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts</b>					
High limit 1	Low limit 1		High limit 2		
Low limit 2					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\</b>					
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49272	Event name:	

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Hardware interrupt:	0	UpperLimitOne0	UpperLimitOne0	Channel number	0	
HwEventTypeLimit1Overrun	4					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49288	Event name:		
Hardware interrupt:	0	LowerLimitOne0	LowerLimitOne0	Channel number	0	
HwEventTypeLimit1Underrun	3					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49264	Event name:		
Hardware interrupt:	0	UpperLimitTwo0	UpperLimitTwo0	Channel number	0	
HwEventTypeLimit2Overrun	6					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49280	Event name:		
Hardware interrupt:	0	LowerLimitTwo0	LowerLimitTwo0	Channel number	0	
HwEventTypeLimit2Underrun	5					
AI 5/AQ 2 [X10]\Inputs\Channel 1						
Parameter settings	Manual					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Diagnostics						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
AI 5/AQ 2 [X10]\Inputs\Channel 1\Measuring						
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient		
Temperature unit		Smoothing	None			
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49273	Event name:		
Hardware interrupt:	0	UpperLimitOne1	UpperLimitOne1	Channel number	1	
HwEventTypeLimit1Overrun	4					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49289	Event name:		
Hardware interrupt:	0	LowerLimitOne1	LowerLimitOne1	Channel number	1	
HwEventTypeLimit1Underrun	3					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49265	Event name:		
Hardware interrupt:	0	UpperLimitTwo1	UpperLimitTwo1	Channel number	1	
HwEventTypeLimit2Overrun	6					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49281	Event name:		
Hardware interrupt:	0	LowerLimitTwo1	LowerLimitTwo1	Channel number	1	
HwEventTypeLimit2Underrun	5					
AI 5/AQ 2 [X10]\Inputs\Channel 2						
Parameter settings	Manual					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Diagnostics						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
AI 5/AQ 2 [X10]\Inputs\Channel 2\Measuring						
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient		
Temperature unit		Smoothing	None			
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49274	Event name:		
Hardware interrupt:	0	UpperLimitOne2	UpperLimitOne2	Channel number	2	
HwEventTypeLimit1Overrun	4					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49290	Event name:		
Hardware interrupt:	0	LowerLimitOne2	LowerLimitOne2	Channel number	2	
HwEventTypeLimit1Underrun	3					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49266	Event name:		
Hardware interrupt:	0	UpperLimitTwo2	UpperLimitTwo2	Channel number	2	
HwEventTypeLimit2Overrun	6					



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<b>AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\</b>						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49282	Event name:		
Hardware interrupt:	0	LowerLimitTwo2	LowerLimitTwo2	Channel number	2	
HwEventTypeLimit2Underrun	5					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3</b>						
Parameter settings	Manual					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Diagnostics</b>						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Measuring</b>						
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient		
Temperature unit		Smoothing	None			
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts</b>						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49275	Event name:		
Hardware interrupt:	0	UpperLimitOne3	UpperLimitOne3	Channel number	3	
HwEventTypeLimit1Overrun	4					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49291	Event name:		
Hardware interrupt:	0	LowerLimitOne3	LowerLimitOne3	Channel number	3	
HwEventTypeLimit1Underrun	3					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49267	Event name:		
Hardware interrupt:	0	UpperLimitTwo3	UpperLimitTwo3	Channel number	3	
HwEventTypeLimit2Overrun	6					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49283	Event name:		
Hardware interrupt:	0	LowerLimitTwo3	LowerLimitTwo3	Channel number	3	
HwEventTypeLimit2Underrun	5					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4</b>						
Parameter settings	Manual					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Diagnostics</b>						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Measuring</b>						
Measurement type	Resistance	Measuring range	600Ohm	Temperature coefficient		
Temperature unit		Smoothing	None			
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts</b>						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49276	Event name:		
Hardware interrupt:	0	UpperLimitOne4	UpperLimitOne4	Channel number	4	
HwEventTypeLimit1Overrun	4					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49292	Event name:		
Hardware interrupt:	0	LowerLimitOne4	LowerLimitOne4	Channel number	4	
HwEventTypeLimit1Underrun	3					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49268	Event name:		
Hardware interrupt:	0	UpperLimitTwo4	UpperLimitTwo4	Channel number	4	
HwEventTypeLimit2Overrun	6					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49284	Event name:		
Hardware interrupt:	0	LowerLimitTwo4	LowerLimitTwo4	Channel number	4	
HwEventTypeLimit2Underrun	5					
<b>AI 5/AQ 2 [X10]\Outputs\Channel 0</b>						
Parameter settings	Manual					
<b>AI 5/AQ 2 [X10]\Outputs\Channel 0\Diagnostics</b>						
Wire break	False	Short circuit to ground	False	Overflow	False	
Underflow	False					
<b>AI 5/AQ 2 [X10]\Outputs\Channel 0\Output</b>						
Output type	Voltage	Output range	+/- 10V	Reaction to CPU STOP	Shutdown	
Substitute value						

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<b>AI 5/AQ 2 [X10]\Outputs\Channel 1</b>					
Parameter settings	Manual				
<b>AI 5/AQ 2 [X10]\Outputs\Channel 1\Diagnostics</b>					
Wire break	False	Short circuit to ground	False	Overflow	False
Underflow	False				
<b>AI 5/AQ 2 [X10]\Outputs\Channel 1\Output</b>					
Output type	Voltage	Output range	+/- 10V	Reaction to CPU STOP	Shutdown
Substitute value					
<b>AI 5/AQ 2 [X10]\I/O addresses\Input addresses</b>					
Start address	0	End address	9	Organization block	0
Process image	0				
<b>AI 5/AQ 2 [X10]\I/O addresses\Output addresses</b>					
Start address	0	End address	3	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X11]\General</b>					
Name	DI 16/DQ 16_1	Comment			
<b>DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\DI/DQ configuration\Value status (Quality Information)</b>					
Value status	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49152	Event name:	
Hardware interrupt:	0	Rising edge0	Rising edge0	Channel number	0
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49280	Event name:	
Hardware interrupt:	0	Falling edge0	Falling edge0	Channel number	0
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49153	Event name:	
Hardware interrupt:	0	Rising edge1	Rising edge1	Channel number	1
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49281	Event name:	
Hardware interrupt:	0	Falling edge1	Falling edge1	Channel number	1
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49154	Event name:	
Hardware interrupt:	0	Rising edge2	Rising edge2	Channel number	2
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49282	Event name:	
Hardware interrupt:	0	Falling edge2	Falling edge2	Channel number	2
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Input parameters</b>					
Input delay	3.2ms				

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<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49155	Event name:	
Hardware interrupt:	0	Rising edge3	Rising edge3	Channel number	3
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49283	Event name:	
Hardware interrupt:	0	Falling edge3	Falling edge3	Channel number	3
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49156	Event name:	
Hardware interrupt:	0	Rising edge4	Rising edge4	Channel number	4
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49284	Event name:	
Hardware interrupt:	0	Falling edge4	Falling edge4	Channel number	4
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49157	Event name:	
Hardware interrupt:	0	Rising edge5	Rising edge5	Channel number	5
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49285	Event name:	
Hardware interrupt:	0	Falling edge5	Falling edge5	Channel number	5
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49158	Event name:	
Hardware interrupt:	0	Rising edge6	Rising edge6	Channel number	6
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49286	Event name:	
Hardware interrupt:	0	Falling edge6	Falling edge6	Channel number	6
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49159	Event name:	
Hardware interrupt:	0	Rising edge7	Rising edge7	Channel number	7
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49287	Event name:	
Hardware interrupt:	0	Falling edge7	Falling edge7	Channel number	7
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				



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<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49160	Event name:	
Hardware interrupt:	0	Rising edge8	Rising edge8	Channel number	8
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49288	Event name:	
Hardware interrupt:	0	Falling edge8	Falling edge8	Channel number	8
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49161	Event name:	
Hardware interrupt:	0	Rising edge9	Rising edge9	Channel number	9
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49289	Event name:	
Hardware interrupt:	0	Falling edge9	Falling edge9	Channel number	9
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49162	Event name:	
Hardware interrupt:	0	Rising edge10	Rising edge10	Channel number	10
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49290	Event name:	0
Hardware interrupt:	0	Falling edge10	Falling edge10	Channel number	10
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49163	Event name:	
Hardware interrupt:	0	Rising edge11	Rising edge11	Channel number	11
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49291	Event name:	
Hardware interrupt:	0	Falling edge11	Falling edge11	Channel number	11
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49164	Event name:	
Hardware interrupt:	0	Rising edge12	Rising edge12	Channel number	12
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49292	Event name:	
Hardware interrupt:	0	Falling edge12	Falling edge12	Channel number	12
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13</b>					
Parameter settings	From template				

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<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49165	Event name:	
Hardware interrupt:	0	Rising edge13	Rising edge13	Channel number	13
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49293	Event name:	
Hardware interrupt:	0	Falling edge13	Falling edge13	Channel number	13
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49166	Event name:	
Hardware interrupt:	0	Rising edge14	Rising edge14	Channel number	14
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49294	Event name:	
Hardware interrupt:	0	Falling edge14	Falling edge14	Channel number	14
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49167	Event name:	
Hardware interrupt:	0	Rising edge15	Rising edge15	Channel number	15
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49295	Event name:	
Hardware interrupt:	0	Falling edge15	Falling edge15	Channel number	15
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 0\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 1\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 2\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 3\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 4\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 5\Output parameters</b>					
Reaction to CPU STOP	Shutdown				

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<b>DI 16/DQ 16 [X11]\Outputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 6\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 7\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 8\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 9\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 10</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 10\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 11</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 11\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 12\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 13</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 13\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 14\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 15\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\I/O addresses\Input addresses</b>					
Start address	10.0	End address	11.7	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X11]\I/O addresses\Output addresses</b>					
Start address	4.0	End address	5.7	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X12]\General</b>					
Name	DI 16/DQ 16_2	Comment			
<b>DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\DI/DQ configuration\Value status (Quality Information)</b>					
Value status	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Input parameters</b>					
Input delay	3.2ms				

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<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49152	Event name:	
Hardware interrupt:	0	Rising edge0	Rising edge0	Channel number	0
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49280	Event name:	
Hardware interrupt:	0	Falling edge0	Falling edge0	Channel number	0
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49153	Event name:	
Hardware interrupt:	0	Rising edge1	Rising edge1	Channel number	1
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49281	Event name:	
Hardware interrupt:	0	Falling edge1	Falling edge1	Channel number	1
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49154	Event name:	
Hardware interrupt:	0	Rising edge2	Rising edge2	Channel number	2
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49282	Event name:	
Hardware interrupt:	0	Falling edge2	Falling edge2	Channel number	2
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49155	Event name:	
Hardware interrupt:	0	Rising edge3	Rising edge3	Channel number	3
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49283	Event name:	
Hardware interrupt:	0	Falling edge3	Falling edge3	Channel number	3
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49156	Event name:	
Hardware interrupt:	0	Rising edge4	Rising edge4	Channel number	4
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49284	Event name:	
Hardware interrupt:	0	Falling edge4	Falling edge4	Channel number	4
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				



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<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49157	Event name:	
Hardware interrupt:	0	Rising edge5	Rising edge5	Channel number	5
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49285	Event name:	
Hardware interrupt:	0	Falling edge5	Falling edge5	Channel number	5
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49158	Event name:	
Hardware interrupt:	0	Rising edge6	Rising edge6	Channel number	6
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49286	Event name:	
Hardware interrupt:	0	Falling edge6	Falling edge6	Channel number	6
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49159	Event name:	
Hardware interrupt:	0	Rising edge7	Rising edge7	Channel number	7
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49287	Event name:	
Hardware interrupt:	0	Falling edge7	Falling edge7	Channel number	7
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49160	Event name:	
Hardware interrupt:	0	Rising edge8	Rising edge8	Channel number	8
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49288	Event name:	
Hardware interrupt:	0	Falling edge8	Falling edge8	Channel number	8
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49161	Event name:	
Hardware interrupt:	0	Rising edge9	Rising edge9	Channel number	9
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49289	Event name:	
Hardware interrupt:	0	Falling edge9	Falling edge9	Channel number	9
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10</b>					
Parameter settings	From template				



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<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49162	Event name:	
Hardware interrupt:	0	Rising edge10	Rising edge10	Channel number	10
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49290	Event name:	0
Hardware interrupt:	0	Falling edge10	Falling edge10	Channel number	10
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49163	Event name:	
Hardware interrupt:	0	Rising edge11	Rising edge11	Channel number	11
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49291	Event name:	
Hardware interrupt:	0	Falling edge11	Falling edge11	Channel number	11
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49164	Event name:	
Hardware interrupt:	0	Rising edge12	Rising edge12	Channel number	12
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49292	Event name:	
Hardware interrupt:	0	Falling edge12	Falling edge12	Channel number	12
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49165	Event name:	
Hardware interrupt:	0	Rising edge13	Rising edge13	Channel number	13
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49293	Event name:	
Hardware interrupt:	0	Falling edge13	Falling edge13	Channel number	13
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49166	Event name:	
Hardware interrupt:	0	Rising edge14	Rising edge14	Channel number	14
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49294	Event name:	
Hardware interrupt:	0	Falling edge14	Falling edge14	Channel number	14
HwEventTypeFallingEdge	2				

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<b>DI 16/DQ 16 [X12]\Inputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49167	Event name:	
Hardware interrupt:	0	Rising edge15	Rising edge15	Channel number	15
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49295	Event name:	
Hardware interrupt:	0	Falling edge15	Falling edge15	Channel number	15
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 0\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 1\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 2\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 3\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 4\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 5\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 6\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 7\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 8\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 9\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 10</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 10\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 11</b>					
Parameter settings	From template				

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<b>DI 16/DQ 16 [X12]\Outputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 11\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 12\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 13</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 13\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 14\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 15\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\I/O addresses\Input addresses</b>					
Start address	12.0	End address	13.7	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X12]\I/O addresses\Output addresses</b>					
Start address	6.0	End address	7.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\CPU 1511C compatibility</b>					
Front connector assignment like CPU 1511C	False				
<b>High speed counters (HSC)\HSC 1\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 1\General\Project information</b>					
Name	HSC_1	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 1\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 1\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 1\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				

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<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirection-ChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)		Invert direction	False	
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 1\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 1\Hardware inputs/outputs</b>					
Pulse input (A)	X11, Clamp 1 (DI0 / %I10.0)	Direction input (B)	X11, Clamp 2 (DI1 / %I10.1)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 1\I/O addresses\Input addresses</b>					
Start address	14.0	End address	29.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 1\I/O addresses\Output addresses</b>					
Start address	8.0	End address	19.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 2\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 2\General\Project information</b>					
Name	HSC_2	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 2\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 2\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 2\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				



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<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 2\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 2\Hardware inputs/outputs</b>					
Pulse input (A)	X11, Clamp 4 (DI3 / %I10.3)	Direction input (B)	X11, Clamp 5 (DI4 / %I10.4)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 2\I/O addresses\Input addresses</b>					
Start address	30.0	End address	45.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 2\I/O addresses\Output addresses</b>					
Start address	20.0	End address	31.7	Organization block	0
Process image	0				



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<b>High speed counters (HSC)\HSC 3\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 3\General\Project information</b>					
Name	HSC_3	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 3\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 3\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 3\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected

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<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 3\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 3\Hardware inputs/outputs</b>					
Pulse input (A)	X11, Clamp 7 (DI6 / %I10.6)	Direction input (B)	X11, Clamp 8 (DI7 / %I10.7)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 3\I/O addresses\Input addresses</b>					
Start address	46.0	End address	61.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 3\I/O addresses\Output addresses</b>					
Start address	32.0	End address	43.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 4\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 4\General\Project information</b>					
Name	HSC_4	Author	Mmuhamed	Comment	
<b>High speed counters (HSC)\HSC 4\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 4\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 4\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				

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<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 4\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 4\Hardware inputs/outputs</b>					
Pulse input (A)	X12, Clamp 1 (DI0 / %I12.0)	Direction input (B)	X12, Clamp 2 (DI1 / %I12.1)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 4\I/O addresses\Input addresses</b>					
Start address	62.0	End address	77.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 4\I/O addresses\Output addresses</b>					
Start address	44.0	End address	55.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 5\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 5\General\Project information</b>					
Name	HSC_5	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 5\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 5\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 5\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				

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<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirection-ChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)		Invert direction	False	
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 5\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 5\Hardware inputs/outputs</b>					
Pulse input (A)	X12, Clamp 4 (DI3 / %I12.3)	Direction input (B)	X12, Clamp 5 (DI4 / %I12.4)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 5\I/O addresses\Input addresses</b>					
Start address	78.0	End address	93.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 5\I/O addresses\Output addresses</b>					
Start address	56.0	End address	67.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 6\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 6\General\Project information</b>					
Name	HSC_6	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 6\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 6\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0



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<b>High speed counters (HSC)\HSC 6\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				



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<b>High speed counters (HSC)\HSC 6\Channel 0\Hysteresis\Set hysteresis range</b>				
Hysteresis (in increments)	0			
<b>High speed counters (HSC)\HSC 6\Channel 0\Measured value\Specify measured value</b>				
Measured variable	Frequency	Update time	10.000ms	
<b>High speed counters (HSC)\HSC 6\Hardware inputs/outputs</b>				
Pulse input (A)	X12, Clamp 7 (DI6 / %I12.6)	Direction input (B)	X12, Clamp 8 (DI7 / %I12.7)	Reset input (N)
HSC DI0	None	HSC DI1	None	HSC DQ0
HSC DQ1	None	Only available via feedback interface		
<b>High speed counters (HSC)\HSC 6\I/O addresses\Input addresses</b>				
Start address	94.0	End address	109.7	Organization block
Process image	0			
<b>High speed counters (HSC)\HSC 6\I/O addresses\Output addresses</b>				
Start address	68.0	End address	79.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\CPU 1511C compatibility</b>				
Front connector assignment like CPU 1511C	False			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information</b>				
Name	Pulse_1	Comment		
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Diagnostic interrupts</b>				
No supply voltage L+	False			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Hardware inputs/outputs</b>				
Pulse output (DQA)	X11, Clamp 21 (DQ0 / %Q4.0): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False	
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Parameters</b>				
Output format	Per 100			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Input addresses</b>				
Start address	110.0	End address	113.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses</b>				
Start address	80.0	End address	91.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\General\Project information</b>				
Name	Pulse_2	Comment		
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Diagnostic interrupts</b>				
No supply voltage L+	False			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Hardware inputs/outputs</b>				
Pulse output (DQA)	X11, Clamp 23 (DQ2 / %Q4.2): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False	
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Parameters</b>				
Output format	Per 100			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Input addresses</b>				
Start address	114.0	End address	117.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Output addresses</b>				
Start address	92.0	End address	103.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\General\Project information</b>				
Name	Pulse_3	Comment		
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Diagnostic interrupts</b>				
No supply voltage L+	False			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Hardware inputs/outputs</b>				
Pulse output (DQA)	X11, Clamp 25 (DQ4 / %Q4.4): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False	
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Parameters</b>				
Output format	Per 100			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Input addresses</b>				
Start address	118.0	End address	121.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Output addresses</b>				
Start address	104.0	End address	115.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\General\Project information</b>				
Name	Pulse_4	Comment		
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	

Totally Integrated Automation Portal						
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Diagnostic interrupts</b>						
No supply voltage L+	False					
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Hardware inputs/outputs</b>						
Pulse output (DQA)	X11, Clamp 27 (DQ6 / %Q4.6): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False			
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Parameters</b>						
Output format	Per 100					
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Input addresses</b>						
Start address	122.0	End address	125.7	Organization block	0	
Process image	0					
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Output addresses</b>						
Start address	116.0	End address	127.7	Organization block	0	
Process image	0					
<b>Startup</b>						
Startup after POWER ON	Warm restart - Operating mode before POWER OFF	Comparison preset to actual configuration	Startup CPU even if mismatch	Configuration time	60000ms	
<b>Cycle</b>						
Maximum cycle time	150ms			Enable minimum cycle time for cyclic OBs	True	
Minimum cycle time	1ms					
<b>Communication load</b>						
Cycle load due to communication	50%					
<b>System and clock memory\System memory bits</b>						
Enable the use of system memory byte	False	Address of system memory byte (MBx)	1	First cycle		
Diagnostic status changed		Always 1 (high)		Always 0 (low)		
<b>System and clock memory\Clock memory bits</b>						
Enable the use of clock memory byte	False	Address of clock memory byte (MBx)	0	10 Hz clock		
5 Hz clock		2.5 Hz clock		2 Hz clock		
1.25 Hz clock		1 Hz clock		0.625 Hz clock		
0.5 Hz clock						
<b>SIMATIC Memory Card\Diagnostics</b>						
Aging of the SIMATIC memory card	False	Threshold value	80%			
<b>System diagnostics\General</b>						
Activate system diagnostics for this device	True	Report network faults as maintenance instead of fault	False			
<b>PLC alarms\General</b>						
Central alarm management in the PLC	True					
<b>Web server\General</b>						
Activate web server on this module	False	Permit access only with HTTPS	True			
<b>Web server\Automatic update</b>						
Enable automatic update	True	Update interval	0s			
<b>Web server\User management</b>						
User name			User rights			
Everybody						
<b>Web server\User-defined web pages</b>						
Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number	
		index.htm	.htm;.html	333	334	
<b>Web server\Overview of interfaces</b>						
Device	Interface		Enabled web server access			
PLC_1	PROFINET interface_1		False			
<b>DNS configuration</b>						
No DNS server address is configured.						
<b>Display\General\Display standby mode</b>						
Time to standby mode	30 minutes					
<b>Display\General\Energy saving mode</b>						
Time to energy saving mode	15 minutes					
<b>Display\General\Display language</b>						
Default language on display	English					
<b>Display\Automatic update</b>						
Time to update	5 seconds					
<b>Display\Password\Display protection</b>						
Enable write access	True	Enable display protection	False			
<b>Display\User-defined logo</b>						
User logo activated	False	Adapt logo	False	Resolution	128x120	
Company logo	---					

Totally Integrated Automation Portal				
<b>User interface languages</b>				
<b>Assign project language</b>		<b>User interface languages</b>		
English (United States)		German		
English (United States)		English		
English (United States)		French		
English (United States)		Spanish		
English (United States)		Italian		
English (United States)		Japanese		
English (United States)		Chinese (simplified)		
English (United States)		Korean		
English (United States)		Russian		
English (United States)		Turkish		
English (United States)		Portuguese (Brazil)		
<b>Time of day\Local time</b>				
<b>Time zone</b>	(UTC +02:00) Cairo			
<b>Time of day\Daylight saving time</b>				
<b>Activate daylight saving time</b>	False	<b>Difference between standard and daylight saving time</b>	60mins	
<b>Time of day\Daylight saving time\Start of daylight saving time</b>				
<b>Selection of the week</b>	First	<b>Selection of the weekday</b>	Sunday	<b>of</b> January
<b>at</b>	Midnight			
<b>Time of day\Daylight saving time\Start of standard time</b>				
<b>Selection of the week</b>	First	<b>Selection of the weekday</b>	Sunday	<b>of</b> January
<b>at</b>	Midnight			
<b>Protection</b>				
<b>Level of protection</b>	Full access (no protection)			
<b>Protection\Connection mechanisms</b>				
<b>Permit access with PUT/GET communication from remote partner</b>	False			
<b>Protection\Security event</b>				
<b>Summarize security events in case of high message volume</b>	True	<b>Length of an interval</b>	20	<b>Unit</b> seconds
<b>OPC UA\Accessibility of the server</b>				
<b>Activate OPC UA server</b>	False			
<b>System power supply\General</b>				
<b>General</b>	Connection to supply voltage L+			
<b>System power supply\Power segment overview</b>				
<b>Module</b>	<b>Slot</b>	<b>Supply/consumption</b>		
PLC_1	1	10.00W		
	Summary	10.00W		
<b>Configuration control\Configuration control for central configuration</b>				
<b>Allow reconfiguration of device via the user program</b>	False			
<b>Connection resources\</b>				
	<b>Station resources - Reserved - Maximum</b>	<b>Station resources - Reserved - Configured</b>	<b>Station resources - Dynamic - Configured</b>	<b>Module resources - PLC_1 [CPU 1512C-1 PN] - Configured</b>
Maximum number of resources:	Maximum	10	78	88
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	4	0	0	0
S7 communication:	0	-	0	0
Open user communication:	0	-	0	0
Web communication:	2	-	-	-
OPC UA client/server communication:	0	-	-	-
Other communication:	-	-	0	0
Total resources used:		0	0	0
Available resources:		10	78	88
<b>Overview of addresses\Overview of addresses\Overview of addresses</b>				
<b>Inputs</b>	True	<b>Outputs</b>	True	<b>Address gaps</b> False
<b>Slot</b>	True			

Totally Integrated Automation Portal											
Type	Addr. from	Addr. to	Module	PIP	OB	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	9	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	10 Bytes	-	0	1 8
O	0	3	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 8
I	10	11	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
O	4	5	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
I	12	13	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
O	6	7	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
I	14	29	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 16
O	8	19	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 16
I	30	45	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 17
O	20	31	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 17
I	46	61	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 18
O	32	43	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 18
I	62	77	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 19
O	44	55	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 19
I	78	93	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 20
O	56	67	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 20
I	94	109	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 21
O	68	79	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 21
I	110	113	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 32
O	80	91	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 32
I	114	117	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 33
O	92	103	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 33
I	118	121	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 34
O	104	115	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 34
I	122	125	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 35
O	116	127	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 35
<b>Runtime licenses\OPC UA\Runtime licenses</b>											
Type of required license	None			Type of purchased license	No license						
<b>Runtime licenses\ProDiag\Supervisions</b>											
Number of used supervisions	0										
<b>Runtime licenses\ProDiag\Runtime licenses</b>											
Number of required licenses	None (<= 25 supervisions)			Used ProDiag licenses	No license						
<b>Runtime licenses\Energy Suite\Energy objects</b>											
Number of configured energy objects	0										
<b>Runtime licenses\Energy Suite\Runtime licenses</b>											
Total number of licensed energy objects	0										
<b>Runtime licenses\Energy Suite\Runtime licenses\Number of purchased licenses</b>											
License type '5 energy objects'	No license			License type '10 energy objects'	No license						



## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN]

### Software units

This folder is empty.

## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Program blocks

## Main [OB1]

## Main Properties

## General

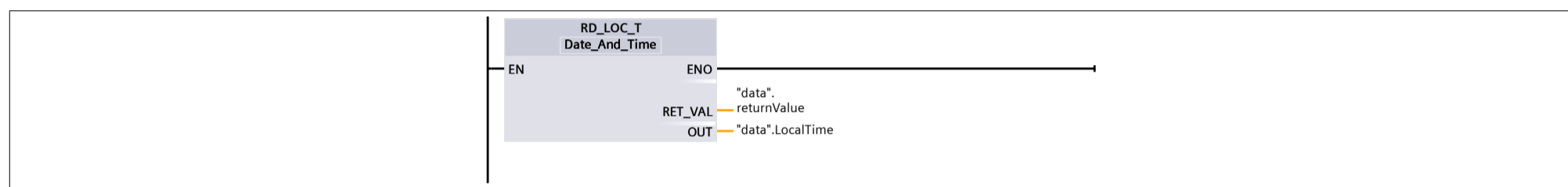
Name	Main	Number	1	Type	OB	Language	LAD
Numbering	Automatic						

## Information

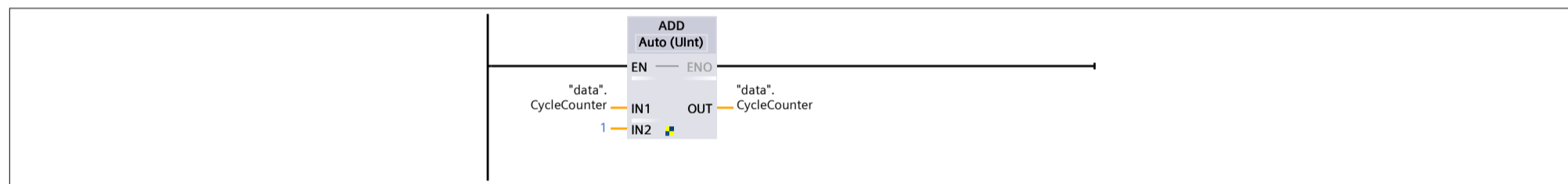
Title	"Main Program Sweep (Cycle)"	Author		Comment		Family	
Version	0.1	User-defined ID					

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Remanence	Bool		=True, if remanent data are available
Temp			
Constant			

## Network 1:



## Network 2: main program cycle execution counter



## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Program blocks

## TimeOfDayInterrupt [OB10]

## TimeOfDayInterrupt Properties

## General

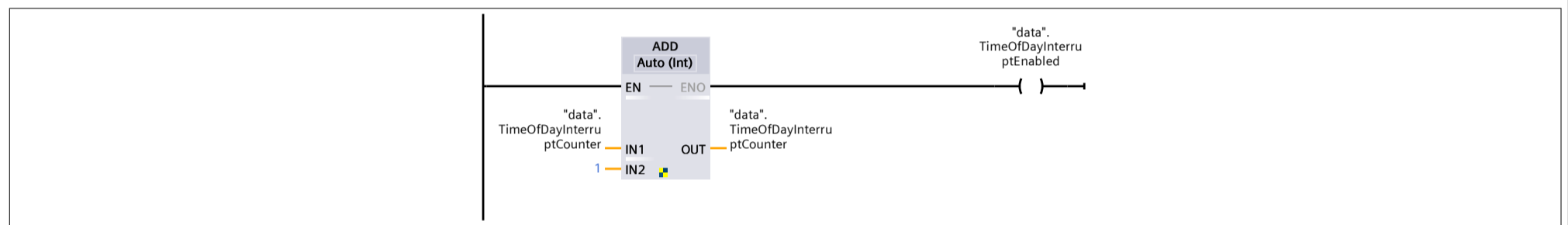
Name	TimeOfDayInterrupt	Number	10	Type	OB	Language	LAD
Numbering	Automatic						

## Information

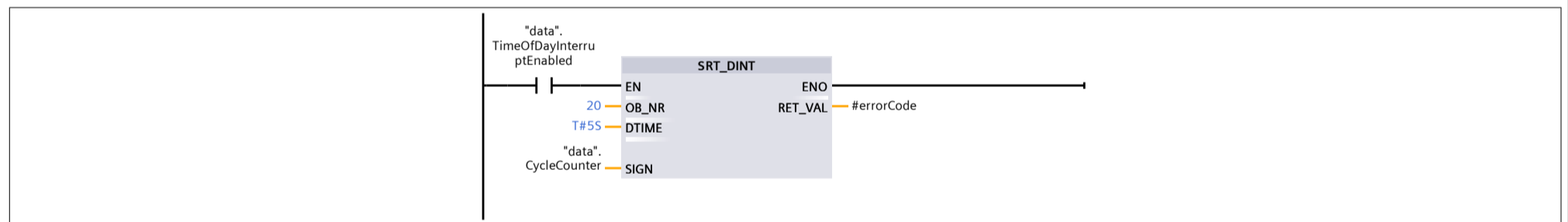
Title		Author		Comment		Family	
Version	0.1	User-defined ID					

Name	Data type	Default value	Comment
▼ Input			
CaughtUp	Bool		OB call is caught up because time was set forward
SecondTime	Bool		OB call is started a second time because time was set backward
▼ Temp			
errorCode	Int		
Constant			

## Network 1:



## Network 2: calling the time delay interrupt OB20



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## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Program blocks

## data [DB1]

## data Properties

## General

Name	data	Number	1	Type	DB	Language	DB
Numbering	Automatic						

## Information

Title		Author		Comment		Family	
Version	0.1	User-defined ID					

Name	Data type	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writ-able from HMI/ OPC UA/ Web API	Visible in HMI engi-neering	Setpoint	Supervi-sion	Comment
▼ Static									
CycleCounter	UInt	0	False	True	True	True	False		
LocalTime	Date_And_Time	DT#1990-01-01-00:00:00	False	True	True	True	False		
returnValue	Word	16#0	False	True	True	True	False		
TimeOfDayInterruptEnabled	Bool	false	False	True	True	True	False		
TimeOfDayInterruptCounter	Int	0	False	True	True	True	False		
CycleCountAtStartOfOB20	UInt	0	False	True	True	True	False		
CycleCountAtExecutionOfOB20	UInt	0	False	True	True	True	False		
OB20delayTimeCycleCount	UInt	0	False	True	True	True	False		



## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Program blocks

## Time delay interrupt [OB20]

## Time delay interrupt Properties

## General

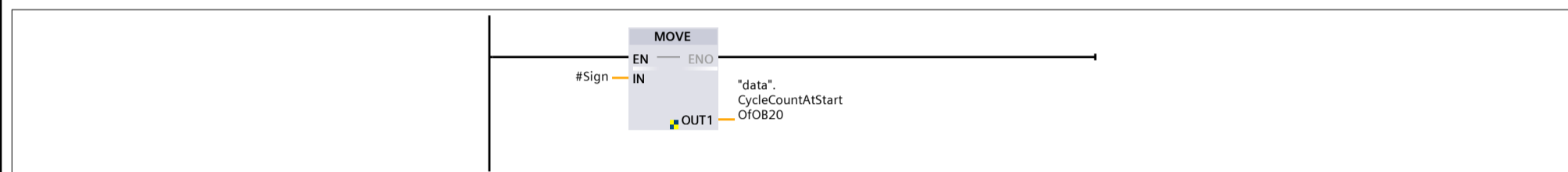
Name	Time delay interrupt	Number	20	Type	OB	Language	LAD
Numbering	Automatic						

## Information

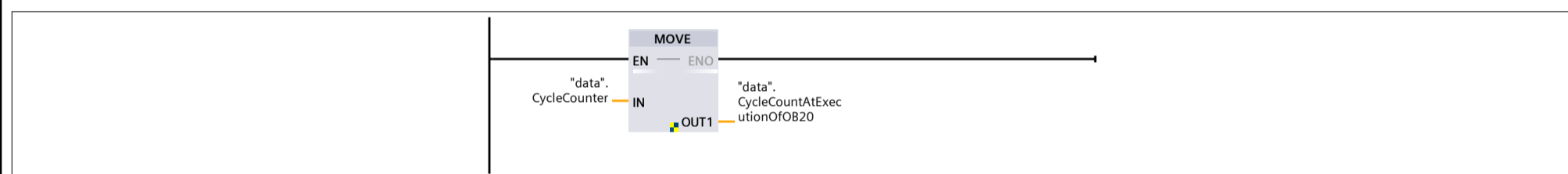
Title		Author		Comment		Family	
Version	0.1	User-defined ID					

Name	Data type	Default value	Comment
▼ Input			
Sign	Word		Identifier input (SIGN) passed to triggering call of SRT_DINT
Temp			
Constant			

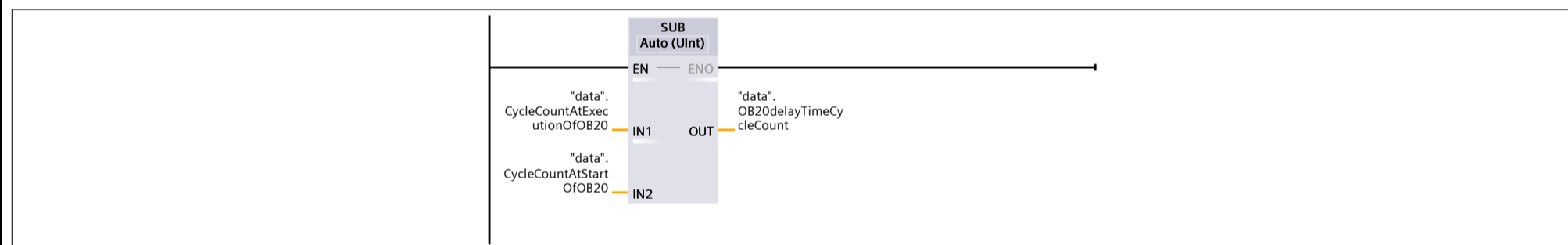
## Network 1: push main OB1 cycle count info at start of OB20 delay time



## Network 2: push main OB1 cycle count info after OB20 delay time( at OB20 calling moment)



## Network 3: how many cycle count have occurred during delay time of OB20



OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Program blocks / System blocks

**Program resources**

This folder is empty.




## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN]

### Technology objects

This folder is empty.

### OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN]




#### PLC tags

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	EnableTimeDelay	Bool	%M4.1	True	True	
	osf	Bool	%M4.0	True	True	
	ScanCounter	DInt	%MD0	True	True	



## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / PLC tags

## Default tag table [64]

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	EnableTimeDelay	Bool	%M4.1	True	True	
	osf	Bool	%M4.0	True	True	
	ScanCounter	DInt	%MD0	True	True	

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN]

**PLC data types**

This folder is empty.

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment
------	---------	----------------	-------------	---------

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Watch and force tables

Watch table\_1

Name	Address	Display format	Modify value	Comment
"TimeOfDayInterruptCounter"	%MD4	DEC+/-		

## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN]

### Traces

Name



OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Traces

**Measurements**

This folder is empty.

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / Traces

Combined measurements

Name

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / OPC UA communication

**Server interfaces**

This folder is empty.

Totally Integrated Automation Portal		
<p>OrganizationBlocksTutorial / PLC_1 [CPU 1512C-1 PN] / OPC UA communication</p> <p><b>Client interfaces</b></p> <p>This folder is empty.</p>		

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / PLC supervisions & alarms

**Supervisions**

This folder is empty.

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / PLC supervisions & alarms

**PLC alarms**

**PLC alarms**

No entries



## OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN] / PLC supervisions &amp; alarms

## System alarms






System alarms			
Name	 SDIAG_ALCAT_SUBMODUL_MSG_0002	Type	PLC alarm
ID	1	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_MODUL_MSG_0003	Type	PLC alarm
ID	2	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_RACK_MSG_0004	Type	PLC alarm
ID	3	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_DEVICE_MSG_0005	Type	PLC alarm
ID	4	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_IOSYSTEM_MSG_0006	Type	PLC alarm
ID	5	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#276K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CPU_OST_MSG_000D	Type	PLC alarm
ID	6	Location	PLC_1
Alarm text	CPU status message: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	

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<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CPU_INFO_MSG_000F	<b>Type</b>	PLC alarm
<b>ID</b>	7	<b>Location</b>	PLC_1
<b>Alarm text</b>	CPU info: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CPU_ERR_MSG_0010	<b>Type</b>	PLC alarm
<b>ID</b>	8	<b>Location</b>	PLC_1
<b>Alarm text</b>	CPU error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CPU_MD_MSG_0011	<b>Type</b>	PLC alarm
<b>ID</b>	9	<b>Location</b>	PLC_1
<b>Alarm text</b>	CPU maintenance demanded: @1W%t#7W@ @6W%t#257K@ / @5W%t#7W@ @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CPU_MR_MSG1_0012	<b>Type</b>	PLC alarm
<b>ID</b>	10	<b>Location</b>	PLC_1
<b>Alarm text</b>	CPU maintenance required: @1W%t#7W@ @6W%t#257K@ / @5W%t#7W@ @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CPU_TMPERR_MSG_0013	<b>Type</b>	PLC alarm
<b>ID</b>	11	<b>Location</b>	PLC_1
<b>Alarm text</b>	Temporary CPU error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CH_ERR_MSG_0015	<b>Type</b>	PLC alarm
<b>ID</b>	12	<b>Location</b>	PLC_1
<b>Alarm text</b>	Error: @1W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@ @6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	

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<b>Name</b>	 SDIAG_ALCAT_ECH_ERR_MSG_0016	<b>Type</b>	PLC alarm
<b>ID</b>	13	<b>Location</b>	PLC_1
<b>Alarm text</b>	Error: @1W%t#7W@ - @5W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CH_MD_MSG_0018	<b>Type</b>	PLC alarm
<b>ID</b>	14	<b>Location</b>	PLC_1
<b>Alarm text</b>	Maintenance demanded:@1W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_ECH_MD_MSG_0019	<b>Type</b>	PLC alarm
<b>ID</b>	15	<b>Location</b>	PLC_1
<b>Alarm text</b>	Maintenance demanded:@1W%t#7W@ - @5W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CH_MR_MSG_001B	<b>Type</b>	PLC alarm
<b>ID</b>	16	<b>Location</b>	PLC_1
<b>Alarm text</b>	Maintenance required:@1W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_ECH_MR_MSG_001C	<b>Type</b>	PLC alarm
<b>ID</b>	17	<b>Location</b>	PLC_1
<b>Alarm text</b>	Maintenance required:@1W%t#7W@ - @5W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_SUB_ERR_MSG_001E	<b>Type</b>	PLC alarm
<b>ID</b>	18	<b>Location</b>	PLC_1
<b>Alarm text</b>	Error: @1W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	True	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	

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Name	 SDIAG_ALCAT_ESUB_ERR_MSG_001F	Type	PLC alarm
ID	19	Location	PLC_1
Alarm text	Error: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SUB_MD_MSG_0021	Type	PLC alarm
ID	20	Location	PLC_1
Alarm text	Maintenance demanded: @1W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ESUB_MD_MSG_0022	Type	PLC alarm
ID	21	Location	PLC_1
Alarm text	Maintenance demanded: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SUB_MR_MSG_0024	Type	PLC alarm
ID	22	Location	PLC_1
Alarm text	Maintenance required: @1W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ESUB_MR_MSG_0025	Type	PLC alarm
ID	23	Location	PLC_1
Alarm text	Maintenance required: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CONFIG_INFO_0028	Type	PLC alarm
ID	24	Location	PLC_1
Alarm text	Info: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CONFIG_REPORT_0029	Type	PLC alarm
ID	25	Location	PLC_1



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Alarm text	Info: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SECU_EV_MSG_005E	Type	PLC alarm
ID	26	Location	PLC_1
Alarm text	Security event: @1W%t#7W@ @5W%t#7W@ @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	Security
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SECU_EV_INFO_005F	Type	PLC alarm
ID	27	Location	PLC_1
Alarm text	Security information: @1W%t#7W@ @5W%t#7W@ @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	Security
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_USER_MSG_0080	Type	PLC alarm
ID	28	Location	PLC_1
Alarm text	User message: @1W%t#2W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_PLC_MSG_00FF	Type	PLC alarm
ID	29	Location	PLC_1
Alarm text	PLC notification: @1W%t#7W@ @5W%t#7W@ @6W%t#256K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	True	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SUBMODUL_MSG_0102	Type	PLC alarm
ID	30	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_MODUL_MSG_0103	Type	PLC alarm
ID	31	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0

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Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_RACK_MSG_0104	Type	PLC alarm
ID	32	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_DEVICE_MSG_0105	Type	PLC alarm
ID	33	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_IOSYSTEM_MSG_0106	Type	PLC alarm
ID	34	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @5W%t#7W@ @6W%t#276K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CPU_OST_MSG_010D	Type	PLC alarm
ID	35	Location	PLC_1
Alarm text	CPU status message: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CPU_ERR_MSG_0110	Type	PLC alarm
ID	36	Location	PLC_1
Alarm text	CPU error: @1W%t#7W@ @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CPU_MD_MSG_0111	Type	PLC alarm
ID	37	Location	PLC_1
Alarm text	CPU maintenance demanded: @1W%t#7W@ @6W%t#257K@ / @5W%t#7W@ @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	



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Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CPU_MR_MSG1_0112	Type	PLC alarm
ID	38	Location	PLC_1
Alarm text	CPU maintenance required: @1W%t#7W@ @6W%t#257K@ / @5W%t#7W@ @6W%t#258K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CH_ERR_MSG_0115	Type	PLC alarm
ID	39	Location	PLC_1
Alarm text	Error: @1W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ECH_ERR_MSG_0116	Type	PLC alarm
ID	40	Location	PLC_1
Alarm text	Error: @1W%t#7W@ - @5W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CH_MD_MSG_0118	Type	PLC alarm
ID	41	Location	PLC_1
Alarm text	Maintenance demanded:@1W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ECH_MD_MSG_0119	Type	PLC alarm
ID	42	Location	PLC_1
Alarm text	Maintenance demanded:@1W%t#7W@ - @5W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_CH_MR_MSG_011B	Type	PLC alarm
ID	43	Location	PLC_1
Alarm text	Maintenance required:@1W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	

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Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ECH_MR_MSG_011C	Type	PLC alarm
ID	44	Location	PLC_1
Alarm text	Maintenance required: @1W%t#7W@ - @5W%t#7W@ on @8W%t#280K@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SUB_ERR_MSG_011E	Type	PLC alarm
ID	45	Location	PLC_1
Alarm text	Error: @1W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ESUB_ERR_MSG_011F	Type	PLC alarm
ID	46	Location	PLC_1
Alarm text	Error: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SUB_MD_MSG_0121	Type	PLC alarm
ID	47	Location	PLC_1
Alarm text	Maintenance demanded: @1W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_ESUB_MD_MSG_0122	Type	PLC alarm
ID	48	Location	PLC_1
Alarm text	Maintenance demanded: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	
Additional text 8		Additional text 9	
Name	 SDIAG_ALCAT_SUB_MR_MSG_0124	Type	PLC alarm
ID	49	Location	PLC_1
Alarm text	Maintenance required: @1W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	Info text	Short name: @6W%t#260K@ Order number: @6W%t#265K@
Alarm class	No Acknowledgement	Acknowledgment	False
Information only	False	Priority	0
Report	False	Created by	System diagnostics
Date created	3/22/2023 1:24 PM	Last change	3/22/2023 1:24 PM
Group ID	0	Additional text 1	PLC_1
Additional text 2		Additional text 3	
Additional text 4		Additional text 5	
Additional text 6		Additional text 7	



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<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_ESUB_MR_MSG_0125	<b>Type</b>	PLC alarm
<b>ID</b>	50	<b>Location</b>	PLC_1
<b>Alarm text</b>	Maintenance required: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	False	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_CONFIG_INFO_0128	<b>Type</b>	PLC alarm
<b>ID</b>	51	<b>Location</b>	PLC_1
<b>Alarm text</b>	Info: @1W%t#7W@ - @5W%t#7W@ @6W%t#257K@ @6W%t#262K@ @6W%t#263K@ @8W%t#7W@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	False	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	
<b>Name</b>	 SDIAG_ALCAT_PLC_MSG_01FF	<b>Type</b>	PLC alarm
<b>ID</b>	52	<b>Location</b>	PLC_1
<b>Alarm text</b>	PLC notification: @1W%t#7W@ @5W%t#7W@ @6W%t#256K@ @6W%t#262K@ @6W%t#263K@	<b>Info text</b>	Short name: @6W%t#260K@ Order number: @6W%t#265K@
<b>Alarm class</b>	No Acknowledgement	<b>Acknowledgment</b>	False
<b>Information only</b>	False	<b>Priority</b>	0
<b>Report</b>	False	<b>Created by</b>	System diagnostics
<b>Date created</b>	3/22/2023 1:24 PM	<b>Last change</b>	3/22/2023 1:24 PM
<b>Group ID</b>	0	<b>Additional text 1</b>	PLC_1
<b>Additional text 2</b>		<b>Additional text 3</b>	
<b>Additional text 4</b>		<b>Additional text 5</b>	
<b>Additional text 6</b>		<b>Additional text 7</b>	
<b>Additional text 8</b>		<b>Additional text 9</b>	

OrganizationBlocksTutorial / PLC\_1 [CPU 1512C-1 PN]

**PLC alarm text lists**

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<b>Organization</b> BlocksTutorial / PLC_1 [CPU 1512C-1 PN] / Local modules <b>PLC_1 [CPU 1512C-1 PN]</b>					
<b>PLC_1</b>					
<b>General\Project information</b>					
Name	PLC_1	Author	Mmuhaled	Comment	
Rack	0	Slot	1		
<b>General\Catalog information</b>					
Short designation	CPU 1512C-1 PN	Description	CPU with display; work memory 250 KB code and 1 MB data; 48 ns bit operation time; 4-stage protection concept, technology functions: motion control, closed-loop control, counting and measuring; tracing; Runtime options; for all PROFINET interfaces: transport protocol TCP/IP, secure Open User Communication, S7 communication, S7 routing, IP forwarding, Web server, DNS client, OPC UA: Server DA, Client DA, methods, companion specifications; PROFINET IO controller, supports RT/IRT, performance upgrade PROFINET V2.3, 2 ports, I-Device, MRP, MRPD, isochronous mode, Routing, runtime options; firmware V2.8 with DI32/DQ32, AI5/AQ2: Digital input module DI16 x DC24V, grouping 16; Digital output module DQ16 x DC24V/0.5A, grouping 16; Analog input module AI4 x U/I, AI 1xRTD, 16-bit, grouping 5; Analog output module AQ2 x U/I, 16-bit, grouping 2; 6 channels for counting and measuring with incremental encoders 24V (up to 100kHz); 4 channels for PTO, pulse width modulation, frequency output (up to 100kHz)	Article number	6ES7 512-1CK01-0AB0
Firmware version	V2.8				
<b>General\Identification &amp; Maintenance</b>					
Plant designation		Location identifier		Installation date	2023-03-21 05:38:08.327
Additional information					
<b>General\Checksums</b>					
Text lists	FA 70 E8 75 1D 5A 8E 29	Software	5E 57 4F FC 0E 48 39 CC		
<b>PROFINET interface [X1]\General</b>					
Name	PROFINET interface_1	Author	Mmuhaled	Comment	
<b>PROFINET interface [X1]\Ethernet addresses\Interface networked with</b>					
Subnet:	Not connected				
<b>PROFINET interface [X1]\Ethernet addresses\IP protocol</b>					
IP configuration	Set IP address in the project	IP address:	192.168.0.1	Subnet mask:	255.255.255.0
Use router	False				
<b>PROFINET interface [X1]\Ethernet addresses\PROFINET</b>					
PROFINET device name is set directly at the device	False	Generate PROFINET device name automatically	True	PROFINET device name:	plc_1.profinet interface_1
Converted name:	plcxb1.profinetxinterfacexb1036c	Device number:	0		
<b>PROFINET interface [X1]\Time-of-day synchronization\NTP mode</b>					
Note	Time synchronization for all PROFINET interfaces take place within the settings for time synchronization of the PROFINET interface [X1].	Enable time synchronization via NTP server	False		IP addresses
Server 1	0.0.0.0	Server 2	0.0.0.0	Server 3	0.0.0.0
Server 4	0.0.0.0	Update interval	10s		
<b>PROFINET interface [X1]\Operating mode</b>					
IO controller	True	IO system		Device number	0
IO device	False				
<b>PROFINET interface [X1]\Advanced options\Interface options</b>					
Call the user program if communication errors occur	False	Support device replacement without exchangeable medium	True	Permit overwriting of device names of all assigned IO devices	False
Limit data infeed into the network	True	Use IEC V2.2 LLDP mode	False	Keep-Alive connection monitoring:	30s
<b>PROFINET interface [X1]\Advanced options\Real time settings\IO communication</b>					
Send clock:	1.000ms				
<b>PROFINET interface [X1]\Advanced options\Real time settings\Synchronization</b>					
RT class:	RT,IRT				
<b>PROFINET interface [X1]\Advanced options\Real time settings\Real time options</b>					
Calculated bandwidth for cyclic IO data:	0.000ms	Calculated bandwidth for cyclic IO data:	0.000%		
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\General</b>					
Name	Port_1	Author	Mmuhaled	Comment	
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Local port:</b>					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1 R]	Medium:	Copper	Cable name:	---

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<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port interconnection\Partner port:</b>					
	Monitoring of partner port is not possible	<b>Alternative partners</b>	False	<b>Partner port:</b>	Any partner
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Activate</b>					
<b>Activate this port for use</b>	True				
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Connection</b>					
<b>Transmission rate / duplex:</b>	Automatic	<b>Monitor</b>	False	<b>Enable autonegotiation</b>	True
<b>PROFINET interface [X1]\Advanced options\Port [X1 P1 R]\Port options\Boundaries</b>					
<b>End of detection of accessible devices</b>	False	<b>End of topology discovery</b>	False	<b>End of the sync domain</b>	False
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\General</b>					
<b>Name</b>	Port_2	<b>Author</b>	Mmuhamed	<b>Comment</b>	
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Local port:</b>					
<b>Local port:</b>	PLC_1\PROFINET interface_1 [X1]\Port_2 [X1 P2 R]	<b>Medium:</b>	Copper	<b>Cable name:</b>	---
					
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port interconnection\Partner port:</b>					
	Monitoring of partner port is not possible	<b>Alternative partners</b>	False	<b>Partner port:</b>	Any partner
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Activate</b>					
<b>Activate this port for use</b>	True				
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Connection</b>					
<b>Transmission rate / duplex:</b>	Automatic	<b>Monitor</b>	False	<b>Enable autonegotiation</b>	True
<b>PROFINET interface [X1]\Advanced options\Port [X1 P2 R]\Port options\Boundaries</b>					
<b>End of detection of accessible devices</b>	False	<b>End of topology discovery</b>	False	<b>End of the sync domain</b>	False
<b>PROFINET interface [X1]\Web server access</b>					
<b>Note</b>	The Web server must also be activated in the properties of the PLC.	<b>Enable Web server via IP address of this interface</b>	False		
<b>AI 5/AQ 2 [X10]\General</b>					
<b>Name</b>	AI 5/AQ 2_1	<b>Comment</b>			
<b>AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics</b>					
<b>Overflow</b>	False	<b>Underflow</b>	False	<b>Wire break</b>	False
<b>Current limit for wire break diagnostics</b>					
<b>AI 5/AQ 2 [X10]\Channel template\Inputs\Apply to all channels that use the template\Measuring</b>					
<b>Measurement type</b>	Voltage	<b>Measuring range</b>	+/- 10V	<b>Temperature coefficient</b>	
<b>Temperature unit</b>	<b>Smoothing</b>		None		
<b>AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics</b>					
<b>Wire break</b>	False	<b>Short circuit to ground</b>	False	<b>Overflow</b>	False
<b>Underflow</b>	False				
<b>AI 5/AQ 2 [X10]\Channel template\Outputs\Apply to all channels that use the template\Output parameters</b>					
<b>Output type</b>	Voltage	<b>Output range</b>	+/- 10V	<b>Reaction to CPU STOP</b>	Shutdown
<b>Substitute value</b>					
<b>AI 5/AQ 2 [X10]\AI/AQ configuration\Value status (Quality Information)</b>					
<b>Value status</b>	False				
<b>AI 5/AQ 2 [X10]\Inputs\General\Measuring</b>					
<b>Interference frequency suppression</b>	50Hz				
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0</b>					
<b>Parameter settings</b>	Manual				
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Diagnostics</b>					
<b>Overflow</b>	False	<b>Underflow</b>	False	<b>Wire break</b>	False
<b>Current limit for wire break diagnostics</b>					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Measuring</b>					
<b>Measurement type</b>	Voltage	<b>Measuring range</b>	+/- 10V	<b>Temperature coefficient</b>	
<b>Temperature unit</b>	<b>Smoothing</b>		None		
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts</b>					
<b>High limit 1</b>		<b>Low limit 1</b>		<b>High limit 2</b>	
<b>Low limit 2</b>					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\</b>					
<b>Hardware interrupt high limit 1</b>	0	<b>RidPrefixFallingEdgeEvent</b>	49272	<b>Event name:</b>	



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Hardware interrupt:	0	UpperLimitOne0	UpperLimitOne0	Channel number	0	
HwEventTypeLimit1Overrun	4					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49288	Event name:		
Hardware interrupt:	0	LowerLimitOne0	LowerLimitOne0	Channel number	0	
HwEventTypeLimit1Underrun	3					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49264	Event name:		
Hardware interrupt:	0	UpperLimitTwo0	UpperLimitTwo0	Channel number	0	
HwEventTypeLimit2Overrun	6					
AI 5/AQ 2 [X10]\Inputs\Channel 0\Hardware interrupts\						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49280	Event name:		
Hardware interrupt:	0	LowerLimitTwo0	LowerLimitTwo0	Channel number	0	
HwEventTypeLimit2Underrun	5					
AI 5/AQ 2 [X10]\Inputs\Channel 1						
Parameter settings	Manual					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Diagnostics						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
AI 5/AQ 2 [X10]\Inputs\Channel 1\Measuring						
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient		
Temperature unit		Smoothing	None			
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49273	Event name:		
Hardware interrupt:	0	UpperLimitOne1	UpperLimitOne1	Channel number	1	
HwEventTypeLimit1Overrun	4					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49289	Event name:		
Hardware interrupt:	0	LowerLimitOne1	LowerLimitOne1	Channel number	1	
HwEventTypeLimit1Underrun	3					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49265	Event name:		
Hardware interrupt:	0	UpperLimitTwo1	UpperLimitTwo1	Channel number	1	
HwEventTypeLimit2Overrun	6					
AI 5/AQ 2 [X10]\Inputs\Channel 1\Hardware interrupts\						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49281	Event name:		
Hardware interrupt:	0	LowerLimitTwo1	LowerLimitTwo1	Channel number	1	
HwEventTypeLimit2Underrun	5					
AI 5/AQ 2 [X10]\Inputs\Channel 2						
Parameter settings	Manual					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Diagnostics						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
AI 5/AQ 2 [X10]\Inputs\Channel 2\Measuring						
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient		
Temperature unit		Smoothing	None			
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49274	Event name:		
Hardware interrupt:	0	UpperLimitOne2	UpperLimitOne2	Channel number	2	
HwEventTypeLimit1Overrun	4					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49290	Event name:		
Hardware interrupt:	0	LowerLimitOne2	LowerLimitOne2	Channel number	2	
HwEventTypeLimit1Underrun	3					
AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49266	Event name:		
Hardware interrupt:	0	UpperLimitTwo2	UpperLimitTwo2	Channel number	2	
HwEventTypeLimit2Overrun	6					

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<b>AI 5/AQ 2 [X10]\Inputs\Channel 2\Hardware interrupts\</b>						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49282	Event name:		
Hardware interrupt:	0	LowerLimitTwo2	LowerLimitTwo2	Channel number	2	
HwEventTypeLimit2Underrun	5					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3</b>						
Parameter settings	Manual					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Diagnostics</b>						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Measuring</b>						
Measurement type	Voltage	Measuring range	+/- 10V	Temperature coefficient		
Temperature unit		Smoothing	None			
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts</b>						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49275	Event name:		
Hardware interrupt:	0	UpperLimitOne3	UpperLimitOne3	Channel number	3	
HwEventTypeLimit1Overrun	4					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49291	Event name:		
Hardware interrupt:	0	LowerLimitOne3	LowerLimitOne3	Channel number	3	
HwEventTypeLimit1Underrun	3					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49267	Event name:		
Hardware interrupt:	0	UpperLimitTwo3	UpperLimitTwo3	Channel number	3	
HwEventTypeLimit2Overrun	6					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 3\Hardware interrupts\</b>						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49283	Event name:		
Hardware interrupt:	0	LowerLimitTwo3	LowerLimitTwo3	Channel number	3	
HwEventTypeLimit2Underrun	5					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4</b>						
Parameter settings	Manual					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Diagnostics</b>						
Overflow	False	Underflow	False	Wire break	False	
Current limit for wire break diagnostics						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Measuring</b>						
Measurement type	Resistance	Measuring range	600Ohm	Temperature coefficient		
Temperature unit		Smoothing	None			
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts</b>						
High limit 1		Low limit 1		High limit 2		
Low limit 2						
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt high limit 1	0	RidPrefixFallingEdgeEvent	49276	Event name:		
Hardware interrupt:	0	UpperLimitOne4	UpperLimitOne4	Channel number	4	
HwEventTypeLimit1Overrun	4					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt low limit 1	0	RidPrefixFallingEdgeEvent	49292	Event name:		
Hardware interrupt:	0	LowerLimitOne4	LowerLimitOne4	Channel number	4	
HwEventTypeLimit1Underrun	3					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt high limit 2	0	RidPrefixFallingEdgeEvent	49268	Event name:		
Hardware interrupt:	0	UpperLimitTwo4	UpperLimitTwo4	Channel number	4	
HwEventTypeLimit2Overrun	6					
<b>AI 5/AQ 2 [X10]\Inputs\Channel 4\Hardware interrupts\</b>						
Hardware interrupt low limit 2	0	RidPrefixFallingEdgeEvent	49284	Event name:		
Hardware interrupt:	0	LowerLimitTwo4	LowerLimitTwo4	Channel number	4	
HwEventTypeLimit2Underrun	5					
<b>AI 5/AQ 2 [X10]\Outputs\Channel 0</b>						
Parameter settings	Manual					
<b>AI 5/AQ 2 [X10]\Outputs\Channel 0\Diagnostics</b>						
Wire break	False	Short circuit to ground	False	Overflow	False	
Underflow	False					
<b>AI 5/AQ 2 [X10]\Outputs\Channel 0\Output</b>						
Output type	Voltage	Output range	+/- 10V	Reaction to CPU STOP	Shutdown	
Substitute value						

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<b>AI 5/AQ 2 [X10]\Outputs\Channel 1</b>					
Parameter settings	Manual				
<b>AI 5/AQ 2 [X10]\Outputs\Channel 1\Diagnostics</b>					
Wire break	False	Short circuit to ground	False	Overflow	False
Underflow	False				
<b>AI 5/AQ 2 [X10]\Outputs\Channel 1\Output</b>					
Output type	Voltage	Output range	+/- 10V	Reaction to CPU STOP	Shutdown
Substitute value					
<b>AI 5/AQ 2 [X10]\I/O addresses\Input addresses</b>					
Start address	0	End address	9	Organization block	0
Process image	0				
<b>AI 5/AQ 2 [X10]\I/O addresses\Output addresses</b>					
Start address	0	End address	3	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X11]\General</b>					
Name	DI 16/DQ 16_1	Comment			
<b>DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Channel template\Inputs\Apply to all channels that use the template\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Channel template\Outputs\Apply to all channels that use the template\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\DI/DQ configuration\Value status (Quality Information)</b>					
Value status	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49152	Event name:	
Hardware interrupt:	0	Rising edge0	Rising edge0	Channel number	0
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49280	Event name:	
Hardware interrupt:	0	Falling edge0	Falling edge0	Channel number	0
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49153	Event name:	
Hardware interrupt:	0	Rising edge1	Rising edge1	Channel number	1
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49281	Event name:	
Hardware interrupt:	0	Falling edge1	Falling edge1	Channel number	1
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49154	Event name:	
Hardware interrupt:	0	Rising edge2	Rising edge2	Channel number	2
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49282	Event name:	
Hardware interrupt:	0	Falling edge2	Falling edge2	Channel number	2
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Input parameters</b>					
Input delay	3.2ms				

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<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49155	Event name:	
Hardware interrupt:	0	Rising edge3	Rising edge3	Channel number	3
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49283	Event name:	
Hardware interrupt:	0	Falling edge3	Falling edge3	Channel number	3
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49156	Event name:	
Hardware interrupt:	0	Rising edge4	Rising edge4	Channel number	4
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49284	Event name:	
Hardware interrupt:	0	Falling edge4	Falling edge4	Channel number	4
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49157	Event name:	
Hardware interrupt:	0	Rising edge5	Rising edge5	Channel number	5
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49285	Event name:	
Hardware interrupt:	0	Falling edge5	Falling edge5	Channel number	5
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49158	Event name:	
Hardware interrupt:	0	Rising edge6	Rising edge6	Channel number	6
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49286	Event name:	
Hardware interrupt:	0	Falling edge6	Falling edge6	Channel number	6
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49159	Event name:	
Hardware interrupt:	0	Rising edge7	Rising edge7	Channel number	7
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49287	Event name:	
Hardware interrupt:	0	Falling edge7	Falling edge7	Channel number	7
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				

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<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49160	Event name:	
Hardware interrupt:	0	Rising edge8	Rising edge8	Channel number	8
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49288	Event name:	
Hardware interrupt:	0	Falling edge8	Falling edge8	Channel number	8
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49161	Event name:	
Hardware interrupt:	0	Rising edge9	Rising edge9	Channel number	9
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49289	Event name:	
Hardware interrupt:	0	Falling edge9	Falling edge9	Channel number	9
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49162	Event name:	
Hardware interrupt:	0	Rising edge10	Rising edge10	Channel number	10
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 10\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49290	Event name:	0
Hardware interrupt:	0	Falling edge10	Falling edge10	Channel number	10
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49163	Event name:	
Hardware interrupt:	0	Rising edge11	Rising edge11	Channel number	11
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 11\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49291	Event name:	
Hardware interrupt:	0	Falling edge11	Falling edge11	Channel number	11
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49164	Event name:	
Hardware interrupt:	0	Rising edge12	Rising edge12	Channel number	12
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 12\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49292	Event name:	
Hardware interrupt:	0	Falling edge12	Falling edge12	Channel number	12
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13</b>					
Parameter settings	From template				



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<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49165	Event name:	
Hardware interrupt:	0	Rising edge13	Rising edge13	Channel number	13
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 13\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49293	Event name:	
Hardware interrupt:	0	Falling edge13	Falling edge13	Channel number	13
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49166	Event name:	
Hardware interrupt:	0	Rising edge14	Rising edge14	Channel number	14
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 14\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49294	Event name:	
Hardware interrupt:	0	Falling edge14	Falling edge14	Channel number	14
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49167	Event name:	
Hardware interrupt:	0	Rising edge15	Rising edge15	Channel number	15
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X11]\Inputs\Channel 15\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49295	Event name:	
Hardware interrupt:	0	Falling edge15	Falling edge15	Channel number	15
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 0\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 1\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 2\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 3\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 4\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 5\Output parameters</b>					
Reaction to CPU STOP	Shutdown				

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<b>DI 16/DQ 16 [X11]\Outputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 6\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 7\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 8\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 9\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 10</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 10\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 11</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 11\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 12\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 13</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 13\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 14\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X11]\Outputs\Channel 15\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X11]\I/O addresses\Input addresses</b>					
Start address	10.0	End address	11.7	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X11]\I/O addresses\Output addresses</b>					
Start address	4.0	End address	5.7	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X12]\General</b>					
Name	DI 16/DQ 16_2	Comment			
<b>DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Channel template\Inputs\Apply to all channels that use the template\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Channel template\Outputs\Apply to all channels that use the template\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\DI/DQ configuration\Value status (Quality Information)</b>					
Value status	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Input parameters</b>					
Input delay	3.2ms				



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<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49152	Event name:	
Hardware interrupt:	0	Rising edge0	Rising edge0	Channel number	0
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 0\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49280	Event name:	
Hardware interrupt:	0	Falling edge0	Falling edge0	Channel number	0
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49153	Event name:	
Hardware interrupt:	0	Rising edge1	Rising edge1	Channel number	1
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 1\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49281	Event name:	
Hardware interrupt:	0	Falling edge1	Falling edge1	Channel number	1
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49154	Event name:	
Hardware interrupt:	0	Rising edge2	Rising edge2	Channel number	2
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 2\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49282	Event name:	
Hardware interrupt:	0	Falling edge2	Falling edge2	Channel number	2
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49155	Event name:	
Hardware interrupt:	0	Rising edge3	Rising edge3	Channel number	3
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 3\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49283	Event name:	
Hardware interrupt:	0	Falling edge3	Falling edge3	Channel number	3
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49156	Event name:	
Hardware interrupt:	0	Rising edge4	Rising edge4	Channel number	4
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 4\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49284	Event name:	
Hardware interrupt:	0	Falling edge4	Falling edge4	Channel number	4
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				

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<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49157	Event name:	
Hardware interrupt:	0	Rising edge5	Rising edge5	Channel number	5
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 5\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49285	Event name:	
Hardware interrupt:	0	Falling edge5	Falling edge5	Channel number	5
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49158	Event name:	
Hardware interrupt:	0	Rising edge6	Rising edge6	Channel number	6
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 6\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49286	Event name:	
Hardware interrupt:	0	Falling edge6	Falling edge6	Channel number	6
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49159	Event name:	
Hardware interrupt:	0	Rising edge7	Rising edge7	Channel number	7
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 7\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49287	Event name:	
Hardware interrupt:	0	Falling edge7	Falling edge7	Channel number	7
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49160	Event name:	
Hardware interrupt:	0	Rising edge8	Rising edge8	Channel number	8
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 8\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49288	Event name:	
Hardware interrupt:	0	Falling edge8	Falling edge8	Channel number	8
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49161	Event name:	
Hardware interrupt:	0	Rising edge9	Rising edge9	Channel number	9
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 9\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49289	Event name:	
Hardware interrupt:	0	Falling edge9	Falling edge9	Channel number	9
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10</b>					
Parameter settings	From template				

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<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49162	Event name:	
Hardware interrupt:	0	Rising edge10	Rising edge10	Channel number	10
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 10\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49290	Event name:	0
Hardware interrupt:	0	Falling edge10	Falling edge10	Channel number	10
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49163	Event name:	
Hardware interrupt:	0	Rising edge11	Rising edge11	Channel number	11
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 11\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49291	Event name:	
Hardware interrupt:	0	Falling edge11	Falling edge11	Channel number	11
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49164	Event name:	
Hardware interrupt:	0	Rising edge12	Rising edge12	Channel number	12
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 12\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49292	Event name:	
Hardware interrupt:	0	Falling edge12	Falling edge12	Channel number	12
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49165	Event name:	
Hardware interrupt:	0	Rising edge13	Rising edge13	Channel number	13
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 13\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49293	Event name:	
Hardware interrupt:	0	Falling edge13	Falling edge13	Channel number	13
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49166	Event name:	
Hardware interrupt:	0	Rising edge14	Rising edge14	Channel number	14
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 14\Hardware interrupts</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49294	Event name:	
Hardware interrupt:	0	Falling edge14	Falling edge14	Channel number	14
HwEventTypeFallingEdge	2				

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<b>DI 16/DQ 16 [X12]\Inputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Input parameters</b>					
Input delay	3.2ms				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts\</b>					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49167	Event name:	
Hardware interrupt:	0	Rising edge15	Rising edge15	Channel number	15
HwEventTypeRisingEdge	1				
<b>DI 16/DQ 16 [X12]\Inputs\Channel 15\Hardware interrupts\</b>					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49295	Event name:	
Hardware interrupt:	0	Falling edge15	Falling edge15	Channel number	15
HwEventTypeFallingEdge	2				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 0</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 0\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 0\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 1</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 1\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 1\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 2</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 2\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 2\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 3</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 3\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 3\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 4</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 4\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 4\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 5</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 5\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 5\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 6</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 6\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 6\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 7</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 7\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 7\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 8</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 8\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 8\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 9</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 9\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 9\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 10</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 10\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 10\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 11</b>					
Parameter settings	From template				



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<b>DI 16/DQ 16 [X12]\Outputs\Channel 11\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 11\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 12</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 12\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 12\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 13</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 13\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 13\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 14</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 14\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 14\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 15</b>					
Parameter settings	From template				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 15\Diagnostics</b>					
No supply voltage L+	False				
<b>DI 16/DQ 16 [X12]\Outputs\Channel 15\Output parameters</b>					
Reaction to CPU STOP	Shutdown				
<b>DI 16/DQ 16 [X12]\I/O addresses\Input addresses</b>					
Start address	12.0	End address	13.7	Organization block	0
Process image	0				
<b>DI 16/DQ 16 [X12]\I/O addresses\Output addresses</b>					
Start address	6.0	End address	7.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\CPU 1511C compatibility</b>					
Front connector assignment like CPU 1511C	False				
<b>High speed counters (HSC)\HSC 1\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 1\General\Project information</b>					
Name	HSC_1	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 1\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 1\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 1\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				

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<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirection-ChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)		Invert direction	False	
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 1\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 1\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 1\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 1\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 1\Hardware inputs/outputs</b>					
Pulse input (A)	X11, Clamp 1 (DI0 / %I10.0)	Direction input (B)	X11, Clamp 2 (DI1 / %I10.1)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 1\I/O addresses\Input addresses</b>					
Start address	14.0	End address	29.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 1\I/O addresses\Output addresses</b>					
Start address	8.0	End address	19.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 2\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 2\General\Project information</b>					
Name	HSC_2	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 2\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 2\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 2\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				

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<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 2\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 2\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 2\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 2\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 2\Hardware inputs/outputs</b>					
Pulse input (A)	X11, Clamp 4 (DI3 / %I10.3)	Direction input (B)	X11, Clamp 5 (DI4 / %I10.4)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 2\I/O addresses\Input addresses</b>					
Start address	30.0	End address	45.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 2\I/O addresses\Output addresses</b>					
Start address	20.0	End address	31.7	Organization block	0
Process image	0				



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<b>High speed counters (HSC)\HSC 3\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 3\General\Project information</b>					
Name	HSC_3	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 3\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 3\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 3\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 3\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected

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<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 3\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 3\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 3\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 3\Hardware inputs/outputs</b>					
Pulse input (A)	X11, Clamp 7 (DI6 / %I10.6)	Direction input (B)	X11, Clamp 8 (DI7 / %I10.7)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 3\I/O addresses\Input addresses</b>					
Start address	46.0	End address	61.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 3\I/O addresses\Output addresses</b>					
Start address	32.0	End address	43.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 4\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 4\General\Project information</b>					
Name	HSC_4	Author	Mmuhamed	Comment	
<b>High speed counters (HSC)\HSC 4\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 4\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 4\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				

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<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 4\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 4\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 4\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 4\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 4\Hardware inputs/outputs</b>					
Pulse input (A)	X12, Clamp 1 (DI0 / %I12.0)	Direction input (B)	X12, Clamp 2 (DI1 / %I12.1)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 4\I/O addresses\Input addresses</b>					
Start address	62.0	End address	77.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 4\I/O addresses\Output addresses</b>					
Start address	44.0	End address	55.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 5\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 5\General\Project information</b>					
Name	HSC_5	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 5\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 5\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0
<b>High speed counters (HSC)\HSC 5\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				

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<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirection-ChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)		Invert direction	False	
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 5\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 5\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 5\Channel 0\Hysteresis\Set hysteresis range</b>					
Hysteresis (in increments)	0				
<b>High speed counters (HSC)\HSC 5\Channel 0\Measured value\Specify measured value</b>					
Measured variable	Frequency	Update time	10.000ms		
<b>High speed counters (HSC)\HSC 5\Hardware inputs/outputs</b>					
Pulse input (A)	X12, Clamp 4 (DI3 / %I12.3)	Direction input (B)	X12, Clamp 5 (DI4 / %I12.4)	Reset input (N)	None
HSC DI0	None	HSC DI1	None	HSC DQ0	Only available via feedback interface
HSC DQ1	None				
<b>High speed counters (HSC)\HSC 5\I/O addresses\Input addresses</b>					
Start address	78.0	End address	93.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 5\I/O addresses\Output addresses</b>					
Start address	56.0	End address	67.7	Organization block	0
Process image	0				
<b>High speed counters (HSC)\HSC 6\General\Enable</b>					
Activate this high-speed counter	False				
<b>High speed counters (HSC)\HSC 6\General\Project information</b>					
Name	HSC_6	Author	Mmuhammed	Comment	
<b>High speed counters (HSC)\HSC 6\Channel 0\Operating mode</b>					
Selection of operating mode	Operating with technology object "Counting and measurement"				
<b>High speed counters (HSC)\HSC 6\Channel 0\Reaction to CPU STOP</b>					
Reaction to CPU STOP	Output substitute value	Substitute value for DQ0	0	Substitute value for DQ1	0



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<b>High speed counters (HSC)\HSC 6\Channel 0\Diagnostic interrupts</b>					
Enable diagnostic interrupts	False				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
New capture value available	0	RidPrefixCaptureEvent	49280	Event name	
Hardware interrupt	0	Capture value0	Capture value0	Channel number	0
HwEventTypeCapture	8				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Synchronization of the counter by an external signal	0	RidPrefixSyncEvent	49296	Event name	
Hardware interrupt	0	Synchronization0	Synchronization0	Channel number	0
HwEventTypeSync	9				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate start	0	RidPrefixGateStartEvent	49168	Event name	
Hardware interrupt	0	Gate start0	Gate start0	Channel number	0
HwEventTypeGateStart	1				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt triggered by external events\</b>					
Gate stop	0	RidPrefixGateStopEvent	49184	Event name	
Hardware interrupt	0	Gate stop0	Gate stop0	Channel number	0
HwEventTypeGateStop	2				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Overflow (high counting limit violated)	0	RidPrefixOverflowEvent	49200	Event name	
Hardware interrupt	0	Overflow0	Overflow0	Channel number	0
HwEventTypeOverflow	3				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Underflow (low counting limit violated)	0	RidPrefixUnderflowEvent	49216	Event name	
Hardware interrupt	0	Underflow0	Underflow0	Channel number	0
HwEventTypeUnderflow	4				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Direction reversal	0	RidPrefixDirectionChangedEvent	49312	Event name	
Hardware interrupt	0	Direction reversal0	Direction reversal0	Channel number	0
HwEventTypeDirectionChanged	10				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Zero crossing	0	RidPrefixZeroCrossingEvent	49264	Event name	
Hardware interrupt	0	Zero crossing0	Zero crossing0	Channel number	0
HwEventTypeZeroCrossing	7				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ0 occurred	0	RidPrefixCompare0Event	49232	Event name	
Hardware interrupt	0	Compare event DQ00	Compare event DQ00	Channel number	0
HwEventTypeCompare0	5				
<b>High speed counters (HSC)\HSC 6\Channel 0\Hardware interrupts\Hardware interrupt by counter value/position value\</b>					
Comparison event for DQ1 occurred	0	RidPrefixCompare1Event	49248	Event name	
Hardware interrupt	0	Compare event DQ10	Compare event DQ10	Channel number	0
HwEventTypeCompare1	6				
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Specify input signals/encoder type</b>					
Signal type	Pulse (A) and direction (B)	Invert direction	False		
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter inputs\Additional parameters</b>					
Signal evaluation	Single	Filter frequency	100 kHz	Reaction to signal N	No reaction to signal N
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counting limits and start value</b>					
High counting limit	2147483647	Start value	0	Low counting limit	-2147483648
<b>High speed counters (HSC)\HSC 6\Channel 0\Counter behavior\Counter behavior at limits and gate start</b>					
Reaction to violation of a counting limit	Continue counting	Reset when counting limit is violated	To opposite counting limit	Reaction to gate start	Continue with current value
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI0\Function of DI0</b>					
Set function of DI	Digital input without function	HSC DI0	None	Input delay	No input for DI0 selected
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of inputs\Behavior of DI1\Function of DI1</b>					
Set function of DI	Digital input without function	HSC DI1	None	Input delay	No input for DI1 selected
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ0\Function of DQ0</b>					
Set output	Between comparison value 0 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ0	0
HSC DQ0	Only available via feedback interface				
<b>High speed counters (HSC)\HSC 6\Channel 0\Behavior of outputs\Behavior of DQ1\Function of DQ1</b>					
Set output	Between comparison value 1 and high limit	Comparison value 0	0	Comparison value 1	10
Count direction	In both directions	Pulse duration	500.0ms	Substitute value for DQ1	0
HSC DQ1	None				

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<b>High speed counters (HSC)\HSC 6\Channel 0\Hysteresis\Set hysteresis range</b>				
Hysteresis (in increments)	0			
<b>High speed counters (HSC)\HSC 6\Channel 0\Measured value\Specify measured value</b>				
Measured variable	Frequency	Update time	10.000ms	
<b>High speed counters (HSC)\HSC 6\Hardware inputs/outputs</b>				
Pulse input (A)	X12, Clamp 7 (DI6 / %I12.6)	Direction input (B)	X12, Clamp 8 (DI7 / %I12.7)	Reset input (N)
HSC DI0	None	HSC DI1	None	HSC DQ0
HSC DQ1	None	Only available via feedback interface		
<b>High speed counters (HSC)\HSC 6\I/O addresses\Input addresses</b>				
Start address	94.0	End address	109.7	Organization block
Process image	0			
<b>High speed counters (HSC)\HSC 6\I/O addresses\Output addresses</b>				
Start address	68.0	End address	79.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\CPU 1511C compatibility</b>				
Front connector assignment like CPU 1511C	False			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information</b>				
Name	Pulse_1	Comment		
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Diagnostic interrupts</b>				
No supply voltage L+	False			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Hardware inputs/outputs</b>				
Pulse output (DQA)	X11, Clamp 21 (DQ0 / %Q4.0): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False	
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\Parameters</b>				
Output format	Per 100			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Input addresses</b>				
Start address	110.0	End address	113.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses</b>				
Start address	80.0	End address	91.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\General\Project information</b>				
Name	Pulse_2	Comment		
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Diagnostic interrupts</b>				
No supply voltage L+	False			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Hardware inputs/outputs</b>				
Pulse output (DQA)	X11, Clamp 23 (DQ2 / %Q4.2): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False	
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\Parameters</b>				
Output format	Per 100			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Input addresses</b>				
Start address	114.0	End address	117.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO2/PWM2\I/O addresses\Output addresses</b>				
Start address	92.0	End address	103.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\General\Project information</b>				
Name	Pulse_3	Comment		
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Diagnostic interrupts</b>				
No supply voltage L+	False			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Hardware inputs/outputs</b>				
Pulse output (DQA)	X11, Clamp 25 (DQ4 / %Q4.4): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False	
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\Parameters</b>				
Output format	Per 100			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Input addresses</b>				
Start address	118.0	End address	121.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO3/PWM3\I/O addresses\Output addresses</b>				
Start address	104.0	End address	115.7	Organization block
Process image	0			
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\General\Project information</b>				
Name	Pulse_4	Comment		
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\General\Operating mode</b>				
Operating mode	Deactivated			
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Reaction to CPU STOP</b>				
Reaction to CPU STOP	Output substitute value	Substitute value for pulse output (DQA)	0	



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<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Diagnostic interrupts</b>						
No supply voltage L+	False					
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Hardware inputs/outputs</b>						
Pulse output (DQA)	X11, Clamp 27 (DQ6 / %Q4.6): 10 kHz / 0.5 A or 100 kHz / 0.1 A	High-speed output (0.1 A)	False			
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\Parameters</b>						
Output format	Per 100					
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Input addresses</b>						
Start address	122.0	End address	125.7	Organization block	0	
Process image	0					
<b>Pulse generators (PTO/PWM)\PTO4/PWM4\I/O addresses\Output addresses</b>						
Start address	116.0	End address	127.7	Organization block	0	
Process image	0					
<b>Startup</b>						
Startup after POWER ON	Warm restart - Operating mode before POWER OFF	Comparison preset to actual configuration	Startup CPU even if mismatch	Configuration time	60000ms	
<b>Cycle</b>						
Maximum cycle time	150ms			Enable minimum cycle time for cyclic OBs	True	
Minimum cycle time	1ms					
<b>Communication load</b>						
Cycle load due to communication	50%					
<b>System and clock memory\System memory bits</b>						
Enable the use of system memory byte	False	Address of system memory byte (MBx)	1	First cycle		
Diagnostic status changed		Always 1 (high)		Always 0 (low)		
<b>System and clock memory\Clock memory bits</b>						
Enable the use of clock memory byte	False	Address of clock memory byte (MBx)	0	10 Hz clock		
5 Hz clock		2.5 Hz clock		2 Hz clock		
1.25 Hz clock		1 Hz clock		0.625 Hz clock		
0.5 Hz clock						
<b>SIMATIC Memory Card\Diagnostics</b>						
Aging of the SIMATIC memory card	False	Threshold value	80%			
<b>System diagnostics\General</b>						
Activate system diagnostics for this device	True	Report network faults as maintenance instead of fault	False			
<b>PLC alarms\General</b>						
Central alarm management in the PLC	True					
<b>Web server\General</b>						
Activate web server on this module	False	Permit access only with HTTPS	True			
<b>Web server\Automatic update</b>						
Enable automatic update	True	Update interval	0s			
<b>Web server\User management</b>						
User name			User rights			
Everybody						
<b>Web server\User-defined web pages</b>						
Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number	
		index.htm	.htm;.html	333	334	
<b>Web server\Overview of interfaces</b>						
Device	Interface			Enabled web server access		
PLC_1	PROFINET interface_1			False		
<b>DNS configuration</b>						
No DNS server address is configured.						
<b>Display\General\Display standby mode</b>						
Time to standby mode	30 minutes					
<b>Display\General\Energy saving mode</b>						
Time to energy saving mode	15 minutes					
<b>Display\General\Display language</b>						
Default language on display	English					
<b>Display\Automatic update</b>						
Time to update	5 seconds					
<b>Display&gt;Password\Display protection</b>						
Enable write access	True	Enable display protection	False			
<b>Display\User-defined logo</b>						
User logo activated	False	Adapt logo	False	Resolution	128x120	
Company logo	---					

Totally Integrated Automation Portal					
<b>User interface languages</b>					
<b>Assign project language</b>			<b>User interface languages</b>		
English (United States)			German		
English (United States)			English		
English (United States)			French		
English (United States)			Spanish		
English (United States)			Italian		
English (United States)			Japanese		
English (United States)			Chinese (simplified)		
English (United States)			Korean		
English (United States)			Russian		
English (United States)			Turkish		
English (United States)			Portuguese (Brazil)		
<b>Time of day\Local time</b>					
<b>Time zone</b>		(UTC +02:00) Cairo			
<b>Time of day\Daylight saving time</b>					
<b>Activate daylight saving time</b>		False		<b>Difference between standard and daylight saving time</b> 60mins	
<b>Time of day\Daylight saving time\Start of daylight saving time</b>					
<b>Selection of the week</b>		First		<b>Selection of the weekday</b> Sunday <b>of</b> January	
<b>at</b>		Midnight			
<b>Time of day\Daylight saving time\Start of standard time</b>					
<b>Selection of the week</b>		First		<b>Selection of the weekday</b> Sunday <b>of</b> January	
<b>at</b>		Midnight			
<b>Protection</b>					
<b>Level of protection</b>		Full access (no protection)			
<b>Protection\Connection mechanisms</b>					
<b>Permit access with PUT/GET communication from remote partner</b>		False			
<b>Protection\Security event</b>					
<b>Summarize security events in case of high message volume</b>		True		<b>Length of an interval</b> 20 <b>Unit</b> seconds	
<b>OPC UA\Accessibility of the server</b>					
<b>Activate OPC UA server</b>		False			
<b>System power supply\General</b>					
<b>General</b>		Connection to supply voltage L+			
<b>System power supply\Power segment overview</b>					
<b>Module</b>		<b>Slot</b>		<b>Supply/consumption</b>	
PLC_1		1		10.00W	
		Summary		10.00W	
<b>Configuration control\Configuration control for central configuration</b>					
<b>Allow reconfiguration of device via the user program</b>		False			
<b>Connection resources\</b>					
	<b>Station resources - Reserved - Maximum</b>	<b>Station resources - Reserved - Configured</b>	<b>Station resources - Dynamic - Configured</b>	<b>Module resources - PLC_1 [CPU 1512C-1 PN] - Configured</b>	
Maximum number of resources:	Maximum	10	78	88	
	Maximum	Configured	Configured	Configured	
PG communication:	4	-	-	-	
HMI communication:	4	0	0	0	
S7 communication:	0	-	0	0	
Open user communication:	0	-	0	0	
Web communication:	2	-	-	-	
OPC UA client/server communication:	0	-	-	-	
Other communication:	-	-	0	0	
Total resources used:		0	0	0	
Available resources:		10	78	88	
<b>Overview of addresses\Overview of addresses\Overview of addresses</b>					
<b>Inputs</b>		True		<b>Outputs</b> True <b>Address gaps</b> False	
<b>Slot</b>		True			

Totally Integrated Automation Portal											
Type	Addr. from	Addr. to	Module	PIP	OB	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	9	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	10 Bytes	-	0	1 8
O	0	3	AI 5/AQ 2_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 8
I	10	11	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
O	4	5	DI 16/DQ 16_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 9
I	12	13	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
O	6	7	DI 16/DQ 16_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	2 Bytes	-	0	1 10
I	14	29	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 16
O	8	19	HSC_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 16
I	30	45	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 17
O	20	31	HSC_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 17
I	46	61	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 18
O	32	43	HSC_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 18
I	62	77	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 19
O	44	55	HSC_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 19
I	78	93	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 20
O	56	67	HSC_5	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 20
I	94	109	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	16 Bytes	-	0	1 21
O	68	79	HSC_6	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 21
I	110	113	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 32
O	80	91	Pulse_1	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 32
I	114	117	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 33
O	92	103	Pulse_2	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 33
I	118	121	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 34
O	104	115	Pulse_3	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 34
I	122	125	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	4 Bytes	-	0	1 35
O	116	127	Pulse_4	Automatic update	-	PLC_1 [CPU 1512C-1 PN]	-	12 Bytes	-	0	1 35
<b>Runtime licenses\OPC UA\Runtime licenses</b>											
Type of required license	None			Type of purchased license	No license						
<b>Runtime licenses\ProDiag\Supervisions</b>											
Number of used supervisions	0										
<b>Runtime licenses\ProDiag\Runtime licenses</b>											
Number of required licenses	None (<= 25 supervisions)			Used ProDiag licenses	No license						
<b>Runtime licenses\Energy Suite\Energy objects</b>											
Number of configured energy objects	0										
<b>Runtime licenses\Energy Suite\Runtime licenses</b>											
Total number of licensed energy objects	0										
<b>Runtime licenses\Energy Suite\Runtime licenses\Number of purchased licenses</b>											
License type '5 energy objects'	No license			License type '10 energy objects'	No license						

## OrganizationBlocksTutorial

### Ungrouped devices

This folder is empty.

## OrganizationBlocksTutorial

### Security settings

This folder is empty.

OrganizationBlocksTutorial / Cross-device functions / Project traces

**Measurements**

This folder is empty.



## OrganizationBlocksTutorial / Common data

### Alarm classes

Alarm classes			
Name	Display name	Acknowledgment	Priority
Acknowledgement	A	True	0
No Acknowledgement	NA	False	0

## OrganizationBlocksTutorial / Common data

### Logs

This folder is empty.

Totally Integrated Automation Portal									
<p><b>OrganizationBlocksTutorial / Languages &amp; resources</b></p> <p><b>Project languages</b></p> <table border="1"><tr><td data-bbox="136 341 2053 379"><b>Languages</b></td></tr><tr><td data-bbox="136 379 2053 418"><b>Reference language</b></td></tr><tr><td data-bbox="136 418 2053 457">English (United States)</td></tr><tr><td data-bbox="136 457 2053 495"><b>Editing language</b></td></tr><tr><td data-bbox="136 495 2053 534">English (United States)</td></tr><tr><td data-bbox="136 534 2053 572"><b>Other project languages</b></td></tr><tr><td data-bbox="136 572 2053 611">Empty</td></tr></table>			<b>Languages</b>	<b>Reference language</b>	English (United States)	<b>Editing language</b>	English (United States)	<b>Other project languages</b>	Empty
<b>Languages</b>									
<b>Reference language</b>									
English (United States)									
<b>Editing language</b>									
English (United States)									
<b>Other project languages</b>									
Empty									

## OrganizationBlocksTutorial / Languages & resources / Project texts

### Project texts

Project texts		
English (United States)	Category	Reference
"Main Program Sweep (Cycle)"	Block comment	OrganizationBlocksTutorial\PLC_1 [CPU 1512C-1 PN]\Program blocks\Main [OB1]\Block title
A	Alarm class text	OrganizationBlocksTutorial\Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
A	Alarm class text	OrganizationBlocksTutorial\Acknowledgement\ShortName
calling the time delay interrupt OB20	Block comment	OrganizationBlocksTutorial\PLC_1 [CPU 1512C-1 PN]\Program blocks\TimeOfDayInterrupt [OB10]\Network 2\Title
how many cycle count have occurred during delay time of OB20	Block comment	OrganizationBlocksTutorial\PLC_1 [CPU 1512C-1 PN]\Program blocks\Time delay interrupt [OB20]\Network 3\Title
main program cycle execution counter	Block comment	OrganizationBlocksTutorial\PLC_1 [CPU 1512C-1 PN]\Program blocks\Main [OB1]\Network 2\Title
NA	Alarm class text	OrganizationBlocksTutorial\No Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
NA	Alarm class text	OrganizationBlocksTutorial\No Acknowledgement\ShortName
push main OB1 cycle count info after OB20 delay time( at OB20 calling moment)	Block comment	OrganizationBlocksTutorial\PLC_1 [CPU 1512C-1 PN]\Program blocks\Time delay interrupt [OB20]\Network 2\Title
push main OB1 cycle count info at start of OB20 delay time	Block comment	OrganizationBlocksTutorial\PLC_1 [CPU 1512C-1 PN]\Program blocks\Time delay interrupt [OB20]\Network 1\Title