

Instrumentation Tools

Totally Integrated Automation Portal		
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AnalogInputSignalProcessing

Project							
Name:	AnalogInputSignalProcessing	Creation time:	6/4/2023 10:47:54 AM	Last change	6/4/2023 12:38:48 PM	Author:	MahmoudSalama
Last modified by:	Mmuhammed	Version:					
Comment:							

Operating system	
Name	Description
Operating system	Microsoft Windows 10 Pro
Version of the operating system	6.3.9600.0
Operating system service pack	
Version of the Internet Explorer	11.789.19041.0
Computer name	MMUHAMED-D1
User name	GULSANEGYPT\mmuhammed
Installation path of the TIA Portal	C:\Program Files\Siemens\Automation\Portal V16

Components		
Name	Version	Release
TIA Portal Project Server V16 - TIA Portal Project Server Single SetupPackage V16.0 (MUSERVERV16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - SIMATIC S7-PLCSIM V16.0 (S7_PLCSIM_V16)	V16.0	V16.00.00.00_31.00.13.01
TIA Administrator - AWB Licensing Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - AWB Software Management V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - TIA UMC Agent Configurator Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - TIA Administrator V1.0 SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
Siemens Totally Integrated Automation Portal V16 - HM All Editions Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - HM NoBasic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 0 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Multiuser Client Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Version Control Interface SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - STEP 7 Safety Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - STEP 7 Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 03 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 04 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package WCF-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - TIACOMPCKECHK Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Openness SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Transfer Mandatory Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
User Management Component - UserManagementComponentx64 V2.7 (UMC64)	V2.7	V02.07.00.00_04.06.00.07
WinCC Runtime Advanced V16.0 - HMIRTM Tagging Package 01 Single SetupPackage V16.0 (HMIRTM_V11)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
SIMATIC HMI License Manager Panel Plugin (x64)	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC WinCC Runtime Advanced Driver (x64)	16.0.0.0	V16.00.00.00_31.02.00.01
ETWEventCollector	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC NCM FWL 64	5.6.0.3	K5.6.0.3_1.1.0.2
NCM GPRS 64	01.02.00.00	V1.2.0.0_2.1.0.1
SIMATIC PLCSIM 64	16.00.00	16.00.00.00_01.00.02.01
SIMATIC Device Drivers	9.2	09.02.04.00_01.04.00.05
TelemetryConnector	1.0.2.57	V01.00.02.57_01.00.00.01
Automation Software Updater	02.05.0300	V02.05.03.00_01.01.00.29
SIEMENS OPC	3.9	03.09.10.00_01.04.00.08
SIMATIC HMI ProSave	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC HMI Symbol Library	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC HMI Touch Input	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC Device Drivers WoW	29.2	29.02.04.00_01.04.00.05

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Name	Version	Release
SIMATIC Event Database	5.6	05.06.02.00_01.01.00.01
SeCon	2.6	V02.06.01.00_01.08.00.01
WinCC Runtime Advanced Simulator	16.0.0.0	V16.00.00.00_31.02.00.01
Products		
Name	Version	Release
TIA Portal Project Server	V16.0	V16.00.00.00_31.02.00.01
SIMATIC S7-PLCSIM	V16.0	V16.00.00.00_31.00.13.01
TIA Administrator	V1.0	01.00.02.00_01.10.00.01
SIMATIC STEP 7 Prof - STEP 7 Safety - WinCC Adv	V16.0	V16.00.00.00_31.02.00.01
User Management Component	V2.7	V02.07.00.00_00.00.00.00
SIMATIC WinCC Runtime Advanced Simulation	V16.0	V16.00.00.00_31.02.00.01
Automation License Manager	V6.0 + SP5 + Upd1	06.00.05.01_02.01.00.05
S7-PLCSIM	V5.4 + SP8	V05.04.08.01_01.24.00.01
SIMATIC ProSave	V16.0	V16.00.00.00_31.02.00.01
S7-PCT	V3.5 + SP1	K3.5.1.0_1.19.0.1


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<h2 style="margin: 0;">AnalogInputSignalProcessing</h2> <h3 style="margin: 0;">PLC_1 [CPU 1214C AC/DC/Rly]</h3>					
PLC_1					
General\Project information					
Name	PLC_1	Author	Mmuhamed	Comment	
Slot	1	Rack	0		
General\Catalog information					
Short designation	CPU 1214C AC/DC/Rly	Description	Work memory 100 KB; 120/240VAC power supply with DI14 x 24VDC SINK/SOURCE, DQ10 x relay and AI2 on board; 6 high-speed counters and 4 pulse outputs on-board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; PROFINET IO controller, I-device, transport protocol TCP/IP, secure Open User Communication, S7 communication, Web server, OPC UA: Server DA	Article number	6ES7 214-1BG40-0XB0
Firmware version	V4.4				
General\Identification & Maintenance					
Plant designation		Location identifier		Installation date	2023-06-04 10:49:48.612
Additional information					
General\Checksums					
Text lists	FA 70 E8 75 1D 5A 8E 29	Software	DF AD 8A 2D 4B 3E 96 BB		
PROFINET interface [X1]\General					
Name	PROFINET interface_1	Author	Mmuhamed	Comment	
PROFINET interface [X1]\General\Project information					
Name	DI 14/DQ 10_1	Comment		Name	AI 2_1
Comment					
PROFINET interface [X1]\Ethernet addresses\Interface networked with					
Subnet:	Not connected				
PROFINET interface [X1]\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	IP address:	192.168.0.1	Subnet mask:	255.255.255.0
Use router	False				
PROFINET interface [X1]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	Generate PROFINET device name automatically	True	PROFINET device name:	plc_1
Converted name:	plcxb1d0ed	Device number:	0		
PROFINET interface [X1]\Time synchronization					
Enable time synchronization via NTP server	Enable time synchronization via NTP server		IP addresses	Server 1	0.0.0.0
Server 2	0.0.0.0	Server 3	0.0.0.0	Server 4	0.0.0.0
Update interval	10sec			CPU synchronizes the modules of the device.	No synchronization
PROFINET interface [X1]\Digital inputs\Channel0					
Channel address	I0.0	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel0\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49152	Event name:	0
Hardware interrupt:	0	Rising edge0	Rising edge0		
PROFINET interface [X1]\Digital inputs\Channel0\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49280	Event name:	0
Hardware interrupt:	0	Falling edge0	Falling edge0		
PROFINET interface [X1]\Digital inputs\Channel1					
Channel address	I0.1	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel1\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49153	Event name:	0
Hardware interrupt:	0	Rising edge1	Rising edge1		
PROFINET interface [X1]\Digital inputs\Channel1\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49281	Event name:	0
Hardware interrupt:	0	Falling edge1	Falling edge1		
PROFINET interface [X1]\Digital inputs\Channel2					
Channel address	I0.2	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel2\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49154	Event name:	0
Hardware interrupt:	0	Rising edge2	Rising edge2		
PROFINET interface [X1]\Digital inputs\Channel2\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49282	Event name:	0
Hardware interrupt:	0	Falling edge2	Falling edge2		
PROFINET interface [X1]\Digital inputs\Channel3					
Channel address	I0.3	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel3\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49155	Event name:	0
Hardware interrupt:	0	Rising edge3	Rising edge3		

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PROFINET interface [X1]\Digital inputs\Channel3\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49283	Event name:	0
Hardware interrupt:	0	Falling edge3	Falling edge3		
PROFINET interface [X1]\Digital inputs\Channel4\					
Channel address	I0.4	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel4\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49156	Event name:	0
Hardware interrupt:	0	Rising edge4	Rising edge4		
PROFINET interface [X1]\Digital inputs\Channel4\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49284	Event name:	0
Hardware interrupt:	0	Falling edge4	Falling edge4		
PROFINET interface [X1]\Digital inputs\Channel5\					
Channel address	I0.5	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel5\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49157	Event name:	0
Hardware interrupt:	0	Rising edge5	Rising edge5		
PROFINET interface [X1]\Digital inputs\Channel5\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49285	Event name:	0
Hardware interrupt:	0	Falling edge5	Falling edge5		
PROFINET interface [X1]\Digital inputs\Channel6\					
Channel address	I0.6	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel6\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49158	Event name:	0
Hardware interrupt:	0	Rising edge6	Rising edge6		
PROFINET interface [X1]\Digital inputs\Channel6\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49286	Event name:	0
Hardware interrupt:	0	Falling edge6	Falling edge6		
PROFINET interface [X1]\Digital inputs\Channel7\					
Channel address	I0.7	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel7\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49159	Event name:	0
Hardware interrupt:	0	Rising edge7	Rising edge7		
PROFINET interface [X1]\Digital inputs\Channel7\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49287	Event name:	0
Hardware interrupt:	0	Falling edge7	Falling edge7		
PROFINET interface [X1]\Digital inputs\Channel8\					
Channel address	I1.0	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel8\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49160	Event name:	0
Hardware interrupt:	0	Rising edge8	Rising edge8		
PROFINET interface [X1]\Digital inputs\Channel8\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49288	Event name:	0
Hardware interrupt:	0	Falling edge8	Falling edge8		
PROFINET interface [X1]\Digital inputs\Channel9\					
Channel address	I1.1	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel9\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49161	Event name:	0
Hardware interrupt:	0	Rising edge9	Rising edge9		
PROFINET interface [X1]\Digital inputs\Channel9\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49289	Event name:	0
Hardware interrupt:	0	Falling edge9	Falling edge9		
PROFINET interface [X1]\Digital inputs\Channel10\					
Channel address	I1.2	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel10\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49162	Event name:	0
Hardware interrupt:	0	Rising edge10	Rising edge10		
PROFINET interface [X1]\Digital inputs\Channel10\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49290	Event name:	0
Hardware interrupt:	0	Falling edge10	Falling edge10		
PROFINET interface [X1]\Digital inputs\Channel11\					
Channel address	I1.3	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel11\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49163	Event name:	0
Hardware interrupt:	0	Rising edge11	Rising edge11		
PROFINET interface [X1]\Digital inputs\Channel11\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49291	Event name:	0
Hardware interrupt:	0	Falling edge11	Falling edge11		
PROFINET interface [X1]\Digital inputs\Channel12\					
Channel address	I1.4	Input filters	6.4 milliseC	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel13\					
Channel address	I1.5	Input filters	6.4 milliseC	Enable pulse catch	0

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PROFINET interface [X1]\Analog inputs\Noise reduction					
Integration time	50 Hz (20 ms)				
PROFINET interface [X1]\Analog inputs\Channel0					
Channel address	IW64	Measurement type	Voltage	Voltage range	0..10 V
Smoothing	Weak (4 cycles)			Enable overflow diagnostics	1
PROFINET interface [X1]\Analog inputs\Channel1					
Channel address	IW66	Measurement type	Voltage	Voltage range	0..10 V
Smoothing	Weak (4 cycles)			Enable overflow diagnostics	1
PROFINET interface [X1]\Digital outputs					
Reaction to CPU STOP	Use substitute value				
PROFINET interface [X1]\Digital outputs\Channel0					
Channel address	Q0.0	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel1					
Channel address	Q0.1	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel2					
Channel address	Q0.2	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel3					
Channel address	Q0.3	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel4					
Channel address	Q0.4	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel5					
Channel address	Q0.5	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel6					
Channel address	Q0.6	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel7					
Channel address	Q0.7	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel8					
Channel address	Q1.0	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel9					
Channel address	Q1.1	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Operating mode					
IO controller	True	IO system		Device number	0
IO device	False				
PROFINET interface [X1]\I/O addresses\Input addresses					
Start address	0.0	End address	1.7	Organization block	0
Process image	0				
PROFINET interface [X1]\I/O addresses\Input addresses					
Start address	64	End address	67	Organization block	0
Process image	0				
PROFINET interface [X1]\I/O addresses\Output addresses					
Start address	0.0	End address	1.7	Organization block	0
Process image	0				
PROFINET interface [X1]\Advanced options\Interface options					
Support device replacement without exchangeable medium	True	Permit overwriting of device names of all assigned IO devices	False	Use IEC V2.2 LLDP mode	False
Keep-Alive connection monitoring:	30s				
PROFINET interface [X1]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X1]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	Calculated bandwidth for cyclic IO data:	0.000%		
PROFINET interface [X1]\Advanced options\Port [X1 P1]\General					
Name	Port_1	Author	Mmuhammed	Comment	
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1]	Medium:	Copper	Cable name:	---
					

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PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port interconnection\Partner port:					
Monitoring of partner port is not possible		Partner port:	Any partner		
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Connection					
Transmission rate / duplex:	Automatic	Monitor	False	Enable autonegotiation	True
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Boundaries					
End of detection of accessible devices	False	End of topology discovery	False	End of the sync domain	False
PROFINET interface [X1]\Web server access					
Enable Web server for the IP address of this interface	False	The Web server must also be activated in the properties of the PLC.			
High speed counters (HSC)\HSC1\General\Enable					
Enable this high speed counter	0	Enable this high speed counter	0	Enable this high speed counter	0
Enable this high speed counter	0	Enable this high speed counter	0	Enable this high speed counter	0
High speed counters (HSC)\HSC1\General\Project information					
Name	HSC_1	Comment		Name	HSC_2
Comment		Name	HSC_3	Comment	
Name	HSC_4	Comment		Name	HSC_5
Comment		Name	HSC_6	Comment	
High speed counters (HSC)\HSC1\I/O addresses\Input addresses					
Start address	1000.0	End address	1003.7	Start address	1004.0
End address	1007.7	Organization block	0	Start address	1008.0
End address	1011.7	Organization block	0	Process image	0
Start address	1012.0	End address	1015.7	Organization block	0
Process image	0	Start address	1016.0	End address	1019.7
Organization block	0	Process image	0	Start address	1020.0
End address	1023.7	Organization block	0	Process image	0
Organization block	0	Process image	0	Process image	0
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Enable					
Enable this pulse generator	0	Enable this pulse generator	0		
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information					
Name	Pulse_1	Comment		Name	Pulse_2
Comment					
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses					
Start address	1000.0	End address	1001.7	Start address	1002.0
End address	1003.7	Organization block	0	Organization block	0
Process image	0	Process image	0		
Startup					
Startup after POWER ON	Warm restart - mode before POWER OFF	Comparison preset to actual configuration	Startup CPU even if mismatch	Configuration time	60000ms
OBs should be interruptible	1				
Cycle					
Cycle monitoring time	150ms			Enable minimum cycle time for cyclic OBs	0
Minimum cycle time	1ms				
Communication load					
Cycle load due to communication	20%				
System and clock memory\System memory bits					
Enable the use of system memory byte	0	Address of system memory byte (MBx)	1	First cycle	
Diagnostic status changed		Always 1 (high)		Always 0 (low)	
System and clock memory\Clock memory bits					
Enable the use of clock memory byte	0	Address of clock memory byte (MBx)	0	10 Hz clock	
5 Hz clock		2.5 Hz clock		2 Hz clock	
1.25 Hz clock		1 Hz clock		0.625 Hz clock	
0.5 Hz clock					
Web server\General					
Activate Web server on all modules of this device	False	Permit access only with HTTPS	True		
Web server\Automatic update					
Enable automatic update	True	Update interval	0s		
Web server\User management					
User name	Everybody			User rights	
Web server\User-defined web pages					
Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number
		index.htm	.htm;.html	333	334
Web server\Overview of interfaces					
Device	Interface		Enabled web server access		
PLC_1	PROFINET interface_1		False		

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User interface languages				
Assign project language			User interface languages	
English (United States)			German	
English (United States)			English	
English (United States)			French	
English (United States)			Spanish	
English (United States)			Italian	
English (United States)			Chinese (simplified)	
Time of day\Local time				
Time zone	(UTC +01:00) Berlin, Bern, Brussels, Rome, Stockholm, Vienna			
Time of day\Daylight saving time				
Activate daylight saving time	1	Difference between standard and daylight saving time	60mins	
Time of day\Daylight saving time\Start of daylight saving time				
Starting week of the month:	Last		Sunday	of March
at	01:00 a.m.			
Time of day\Daylight saving time\Start of standard time				
	Last		Sunday	of October
at	02:00 a.m.			
Protection & Security				
Level of protection	No protection			
Protection & Security\Connection mechanisms				
Permit access with PUT/GET communication from remote partner	False			
Protection & Security\Security event				
Summarize diagnostics in case of high message volume	True	Length of an interval	20	Unit seconds
Protection & Security\External load memory				
Disable copying from internal load memory to external load memory	False			
Configuration control\Configuration control for central configuration				
Allow to reconfigure the device via the user program	0			
Connection resources\				
	Station resources - Reserved - Maximum	Station resources - Reserved - Configured	Station resources - Dynamic - Configured	Module resources - PLC_1 [CPU 1214C AC/DC/Rly] - Configured
Maximum number of resources:		62	6	68
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	12	0	0	0
S7 communication:	8	0	0	0
Open user communication:	8	0	0	0
Web communication:	30	-	-	-
Other communication:	-	-	0	0
Total resources used:		0	0	0
Available resources:		62	6	68
Overview of addresses\Overview of addresses\Overview of addresses				
Inputs	True	Outputs	True	Address gaps False
Slot	True			

Instrumentation Tools

Totally Integrated Automation Portal		
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Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 1
O	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 1
I	64	67	AI 2_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 33
O	1004	1005	Pulse_3	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 35
I	96	103	AI 4x13BIT/AQ 2x14BIT_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	8 Bytes	-	0	2
O	96	99	AI 4x13BIT/AQ 2x14BIT_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	2

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Program blocks

Main [OB1]

Main Properties

General

Name	Main	Number	1	Type	OB	Language	LAD
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Numbering	Automatic
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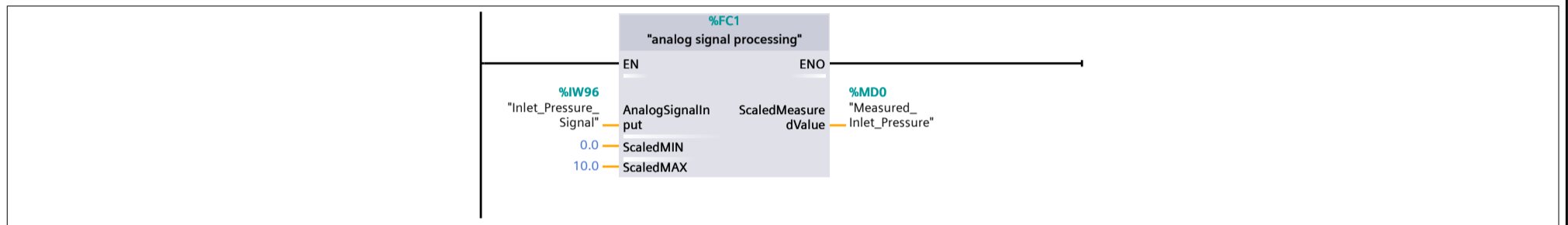
Information

Title	"Main Program Sweep (Cycle)"	Author		Comment		Family	
--------------	------------------------------	---------------	--	----------------	--	---------------	--

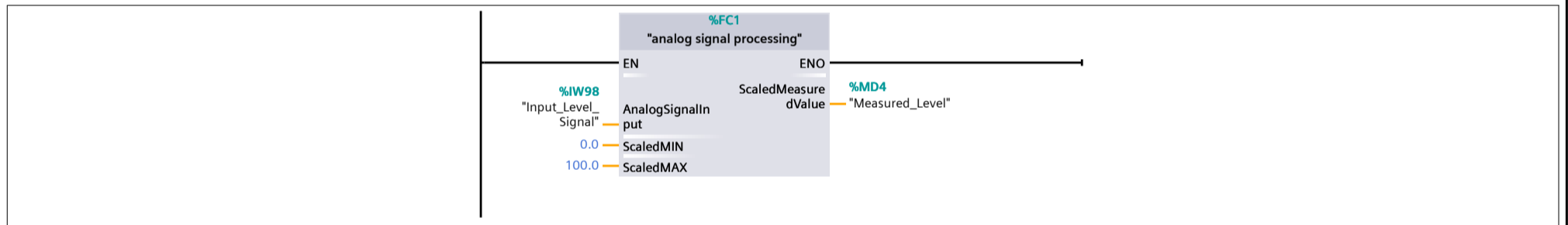
Version	0.1	User-defined ID	
----------------	-----	------------------------	--

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Remanence	Bool		=True, if remanent data are available
Temp			
Constant			

Network 1:



Network 2:



AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Program blocks analog signal processing [FC1]

analog signal processing Properties

General

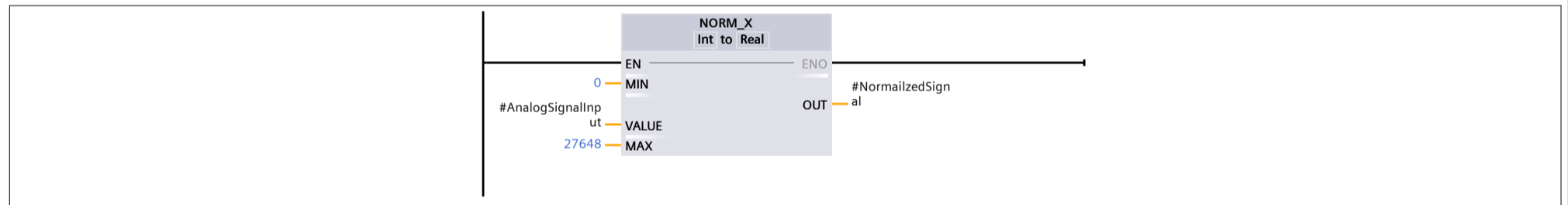
Name	analog signal processing	Number	1	Type	FC	Language	LAD
Numbering	Automatic						

Information

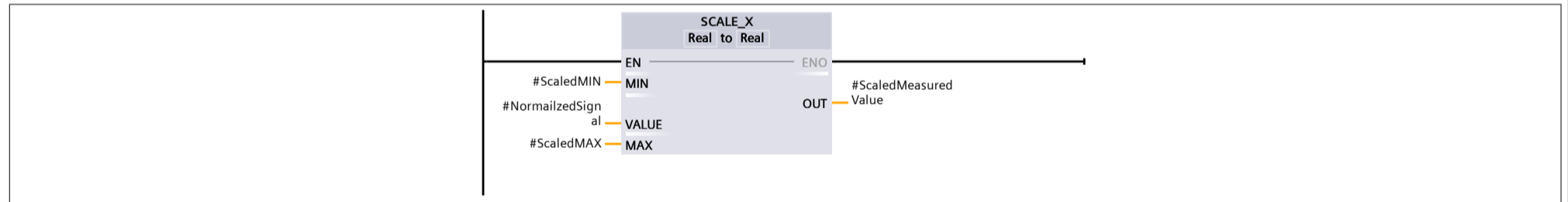
Title		Author		Comment		Family	
Version	0.1	User-defined ID					

Name	Data type	Default value	Comment
▼ Input			
AnalogSignalInput	Int		
ScaledMIN	Real		
ScaledMAX	Real		
▼ Output			
ScaledMeasuredValue	Real		
InOut			
▼ Temp			
NormalizedSignal	Real		
Constant			
▼ Return			
analog signal processing	Void		

Network 1: Normalize the analog input to a value between 0 and 27648



Network 2: scaling the normalized value from 0-27648 into the actual physical value of 0-10 bar pressure







AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly]

Technology objects

This folder is empty.





AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly]

PLC tags

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	Inlet_Pressure_Signal	Int	%IW96	True	True	
	Input_Level_Signal	Int	%IW98	True	True	
	Measured_Inlet_Pres- sure	Real	%MD0	True	True	
	Measured_Level	Real	%MD4	True	True	

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / PLC tags

Default tag table [34]

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	Inlet_Pressure_Signal	Int	%IW96	True	True	
	Input_Level_Signal	Int	%IW98	True	True	
	Measured_Inlet_Pressure	Real	%MD0	True	True	
	Measured_Level	Real	%MD4	True	True	

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly]

PLC data types

This folder is empty.

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment
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AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly]

Traces

Name

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Traces

Measurements

This folder is empty.

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Traces

Combined measurements

Name

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / OPC UA communication

Server interfaces

This folder is empty.

AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly]

PLC alarm text lists

This folder is empty.


Instrumentation Tools

Totally Integrated Automation Portal					
AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Local modules					
PLC_1 [CPU 1214C AC/DC/Rly]					
PLC_1					
General\Project information					
Name	PLC_1	Author	Mmuhamed	Comment	
Slot	1	Rack	0		
General\Catalog information					
Short designation	CPU 1214C AC/DC/Rly	Description	Work memory 100 KB; 120/240VAC power supply with DI14 x 24VDC SINK/SOURCE, DQ10 x relay and AI2 on board; 6 high-speed counters and 4 pulse outputs on-board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; PROFINET IO controller, I-device, transport protocol TCP/IP, secure Open User Communication, S7 communication, Web server, OPC UA: Server DA	Article number	6ES7 214-1BG40-0XB0
Firmware version	V4.4				
General\Identification & Maintenance					
Plant designation		Location identifier		Installation date	2023-06-04 10:49:48.612
Additional information					
General\Checksums					
Text lists	FA 70 E8 75 1D 5A 8E 29	Software	DF AD 8A 2D 4B 3E 96 BB		
PROFINET interface [X1]\General					
Name	PROFINET interface_1	Author	Mmuhamed	Comment	
PROFINET interface [X1]\General\Project information					
Name	DI 14/DQ 10_1	Comment		Name	AI 2_1
Comment					
PROFINET interface [X1]\Ethernet addresses\Interface networked with					
Subnet:	Not connected				
PROFINET interface [X1]\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	IP address:	192.168.0.1	Subnet mask:	255.255.255.0
Use router	False				
PROFINET interface [X1]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	Generate PROFINET device name automatically	True	PROFINET device name:	plc_1
Converted name:	plcxb1d0ed	Device number:	0		
PROFINET interface [X1]\Time synchronization					
Enable time synchronization via NTP server	Enable time synchronization via NTP server		IP addresses	Server 1	0.0.0.0
Server 2	0.0.0.0	Server 3	0.0.0.0	Server 4	0.0.0.0
Update interval	10sec			CPU synchronizes the modules of the device.	No synchronization
PROFINET interface [X1]\Digital inputs\Channel0					
Channel address	I0.0	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel0\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49152	Event name:	0
Hardware interrupt:	0	Rising edge0	Rising edge0		
PROFINET interface [X1]\Digital inputs\Channel0\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49280	Event name:	0
Hardware interrupt:	0	Falling edge0	Falling edge0		
PROFINET interface [X1]\Digital inputs\Channel1					
Channel address	I0.1	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel1\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49153	Event name:	0
Hardware interrupt:	0	Rising edge1	Rising edge1		
PROFINET interface [X1]\Digital inputs\Channel1\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49281	Event name:	0
Hardware interrupt:	0	Falling edge1	Falling edge1		
PROFINET interface [X1]\Digital inputs\Channel2					
Channel address	I0.2	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel2\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49154	Event name:	0
Hardware interrupt:	0	Rising edge2	Rising edge2		
PROFINET interface [X1]\Digital inputs\Channel2\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49282	Event name:	0
Hardware interrupt:	0	Falling edge2	Falling edge2		
PROFINET interface [X1]\Digital inputs\Channel3					
Channel address	I0.3	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel3\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49155	Event name:	0
Hardware interrupt:	0	Rising edge3	Rising edge3		

Instrumentation Tools

Totally Integrated Automation Portal					
PROFINET interface [X1]\Digital inputs\Channel3\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49283	Event name:	0
Hardware interrupt:	0	Falling edge3	Falling edge3		
PROFINET interface [X1]\Digital inputs\Channel4\					
Channel address	I0.4	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel4\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49156	Event name:	0
Hardware interrupt:	0	Rising edge4	Rising edge4		
PROFINET interface [X1]\Digital inputs\Channel4\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49284	Event name:	0
Hardware interrupt:	0	Falling edge4	Falling edge4		
PROFINET interface [X1]\Digital inputs\Channel5\					
Channel address	I0.5	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel5\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49157	Event name:	0
Hardware interrupt:	0	Rising edge5	Rising edge5		
PROFINET interface [X1]\Digital inputs\Channel5\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49285	Event name:	0
Hardware interrupt:	0	Falling edge5	Falling edge5		
PROFINET interface [X1]\Digital inputs\Channel6\					
Channel address	I0.6	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel6\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49158	Event name:	0
Hardware interrupt:	0	Rising edge6	Rising edge6		
PROFINET interface [X1]\Digital inputs\Channel6\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49286	Event name:	0
Hardware interrupt:	0	Falling edge6	Falling edge6		
PROFINET interface [X1]\Digital inputs\Channel7\					
Channel address	I0.7	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel7\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49159	Event name:	0
Hardware interrupt:	0	Rising edge7	Rising edge7		
PROFINET interface [X1]\Digital inputs\Channel7\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49287	Event name:	0
Hardware interrupt:	0	Falling edge7	Falling edge7		
PROFINET interface [X1]\Digital inputs\Channel8\					
Channel address	I1.0	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel8\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49160	Event name:	0
Hardware interrupt:	0	Rising edge8	Rising edge8		
PROFINET interface [X1]\Digital inputs\Channel8\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49288	Event name:	0
Hardware interrupt:	0	Falling edge8	Falling edge8		
PROFINET interface [X1]\Digital inputs\Channel9\					
Channel address	I1.1	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel9\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49161	Event name:	0
Hardware interrupt:	0	Rising edge9	Rising edge9		
PROFINET interface [X1]\Digital inputs\Channel9\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49289	Event name:	0
Hardware interrupt:	0	Falling edge9	Falling edge9		
PROFINET interface [X1]\Digital inputs\Channel10\					
Channel address	I1.2	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel10\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49162	Event name:	0
Hardware interrupt:	0	Rising edge10	Rising edge10		
PROFINET interface [X1]\Digital inputs\Channel10\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49290	Event name:	0
Hardware interrupt:	0	Falling edge10	Falling edge10		
PROFINET interface [X1]\Digital inputs\Channel11\					
Channel address	I1.3	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel11\					
Enable rising edge detection	0	RidPrefixRisingEdgeEvent	49163	Event name:	0
Hardware interrupt:	0	Rising edge11	Rising edge11		
PROFINET interface [X1]\Digital inputs\Channel11\					
Enable falling edge detection	0	RidPrefixFallingEdgeEvent	49291	Event name:	0
Hardware interrupt:	0	Falling edge11	Falling edge11		
PROFINET interface [X1]\Digital inputs\Channel12\					
Channel address	I1.4	Input filters	6.4 millise	Enable pulse catch	0
PROFINET interface [X1]\Digital inputs\Channel13\					
Channel address	I1.5	Input filters	6.4 millise	Enable pulse catch	0

Instrumentation Tools

Totally Integrated Automation Portal					
PROFINET interface [X1]\Analog inputs\Noise reduction					
Integration time	50 Hz (20 ms)				
PROFINET interface [X1]\Analog inputs\Channel0					
Channel address	IW64	Measurement type	Voltage	Voltage range	0..10 V
Smoothing	Weak (4 cycles)			Enable overflow diagnostics	1
PROFINET interface [X1]\Analog inputs\Channel1					
Channel address	IW66	Measurement type	Voltage	Voltage range	0..10 V
Smoothing	Weak (4 cycles)			Enable overflow diagnostics	1
PROFINET interface [X1]\Digital outputs					
Reaction to CPU STOP	Use substitute value				
PROFINET interface [X1]\Digital outputs\Channel0					
Channel address	Q0.0	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel1					
Channel address	Q0.1	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel2					
Channel address	Q0.2	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel3					
Channel address	Q0.3	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel4					
Channel address	Q0.4	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel5					
Channel address	Q0.5	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel6					
Channel address	Q0.6	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel7					
Channel address	Q0.7	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel8					
Channel address	Q1.0	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Digital outputs\Channel9					
Channel address	Q1.1	Substitute a value of 1 on a change from RUN to STOP.	0		
PROFINET interface [X1]\Operating mode					
IO controller	True	IO system		Device number	0
IO device	False				
PROFINET interface [X1]\I/O addresses\Input addresses					
Start address	0.0	End address	1.7	Organization block	0
Process image	0				
PROFINET interface [X1]\I/O addresses\Input addresses					
Start address	64	End address	67	Organization block	0
Process image	0				
PROFINET interface [X1]\I/O addresses\Output addresses					
Start address	0.0	End address	1.7	Organization block	0
Process image	0				
PROFINET interface [X1]\Advanced options\Interface options					
Support device replacement without exchangeable medium	True	Permit overwriting of device names of all assigned IO devices	False	Use IEC V2.2 LLDP mode	False
Keep-Alive connection monitoring:	30s				
PROFINET interface [X1]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X1]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	Calculated bandwidth for cyclic IO data:	0.000%		
PROFINET interface [X1]\Advanced options\Port [X1 P1]\General					
Name	Port_1	Author	Mmuhammed	Comment	
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1]	Medium:	Copper	Cable name:	---
					

Instrumentation Tools

Totally Integrated Automation Portal					
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port interconnection\Partner port:					
Monitoring of partner port is not possible	Partner port:		Any partner		
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Connection					
Transmission rate / duplex:	Automatic	Monitor	False	Enable autonegotiation	True
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Boundaries					
End of detection of accessible devices	False	End of topology discovery	False	End of the sync domain	False
PROFINET interface [X1]\Web server access					
Enable Web server for the IP address of this interface	False	The Web server must also be activated in the properties of the PLC.			
High speed counters (HSC)\HSC1\General\Enable					
Enable this high speed counter	0	Enable this high speed counter	0	Enable this high speed counter	0
Enable this high speed counter	0	Enable this high speed counter	0	Enable this high speed counter	0
High speed counters (HSC)\HSC1\General\Project information					
Name	HSC_1	Comment		Name	HSC_2
Comment		Name	HSC_3	Comment	
Name	HSC_4	Comment		Name	HSC_5
Comment		Name	HSC_6	Comment	
High speed counters (HSC)\HSC1\I/O addresses\Input addresses					
Start address	1000.0	End address	1003.7	Start address	1004.0
End address	1007.7	Organization block	0	Start address	1008.0
End address	1011.7	Organization block	0	Process image	0
Start address	1012.0	End address	1015.7	Organization block	0
Process image	0	Start address	1016.0	End address	1019.7
Organization block	0	Process image	0	Start address	1020.0
End address	1023.7	Organization block	0	Process image	0
Organization block	0	Process image	0	Process image	0
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Enable					
Enable this pulse generator	0	Enable this pulse generator	0		
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information					
Name	Pulse_1	Comment		Name	Pulse_2
Comment					
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses					
Start address	1000.0	End address	1001.7	Start address	1002.0
End address	1003.7	Organization block	0	Organization block	0
Process image	0	Process image	0		
Startup					
Startup after POWER ON	Warm restart - mode before POWER OFF	Comparison preset to actual configuration	Startup CPU even if mismatch	Configuration time	60000ms
OBs should be interruptible	1				
Cycle					
Cycle monitoring time	150ms			Enable minimum cycle time for cyclic OBs	0
Minimum cycle time	1ms				
Communication load					
Cycle load due to communication	20%				
System and clock memory\System memory bits					
Enable the use of system memory byte	0	Address of system memory byte (MBx)	1	First cycle	
Diagnostic status changed		Always 1 (high)		Always 0 (low)	
System and clock memory\Clock memory bits					
Enable the use of clock memory byte	0	Address of clock memory byte (MBx)	0	10 Hz clock	
5 Hz clock		2.5 Hz clock		2 Hz clock	
1.25 Hz clock		1 Hz clock		0.625 Hz clock	
0.5 Hz clock					
Web server\General					
Activate Web server on all modules of this device	False	Permit access only with HTTPS	True		
Web server\Automatic update					
Enable automatic update	True	Update interval	0s		
Web server\User management					
User name	Everybody			User rights	
Web server\User-defined web pages					
Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number
		index.htm	.htm;.html	333	334
Web server\Overview of interfaces					
Device	Interface		Enabled web server access		
PLC_1	PROFINET interface_1		False		

Instrumentation Tools

Totally Integrated Automation Portal				
User interface languages				
Assign project language		User interface languages		
English (United States)		German		
English (United States)		English		
English (United States)		French		
English (United States)		Spanish		
English (United States)		Italian		
English (United States)		Chinese (simplified)		
Time of day\Local time				
Time zone	(UTC +01:00) Berlin, Bern, Brussels, Rome, Stockholm, Vienna			
Time of day\Daylight saving time				
Activate daylight saving time	1	Difference between standard and daylight saving time	60mins	
Time of day\Daylight saving time\Start of daylight saving time				
Starting week of the month:	Last	Sunday	of	March
at	01:00 a.m.			
Time of day\Daylight saving time\Start of standard time				
Starting week of the month:	Last	Sunday	of	October
at	02:00 a.m.			
Protection & Security				
Level of protection	No protection			
Protection & Security\Connection mechanisms				
Permit access with PUT/GET communication from remote partner	False			
Protection & Security\Security event				
Summarize diagnostics in case of high message volume	True	Length of an interval	20	Unit seconds
Protection & Security\External load memory				
Disable copying from internal load memory to external load memory	False			
Configuration control\Configuration control for central configuration				
Allow to reconfigure the device via the user program	0			
Connection resources\				
	Station resources - Reserved - Maximum	Station resources - Reserved - Configured	Station resources - Dynamic - Configured	Module resources - PLC_1 [CPU 1214C AC/DC/Rly] - Configured
Maximum number of resources:		62	6	68
	Maximum	Configured	Configured	Configured
PG communication:	4	-	-	-
HMI communication:	12	0	0	0
S7 communication:	8	0	0	0
Open user communication:	8	0	0	0
Web communication:	30	-	-	-
Other communication:	-	-	0	0
Total resources used:		0	0	0
Available resources:		62	6	68
Overview of addresses\Overview of addresses\Overview of addresses				
Inputs	True	Outputs	True	Address gaps False
Slot	True			

Instrumentation Tools

Totally Integrated Automation Portal		
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Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 1
O	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 1
I	64	67	AI 2_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 33
O	1004	1005	Pulse_3	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	2 Bytes	-	0	1 35
I	96	103	AI 4x13BIT/AQ 2x14BIT_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	8 Bytes	-	0	2
O	96	99	AI 4x13BIT/AQ 2x14BIT_1	Automatic update	PLC_1 [CPU 1214C AC/DC/Rly]	-	4 Bytes	-	0	2

Totally Integrated Automation Portal					
AnalogInputSignalProcessing / PLC_1 [CPU 1214C AC/DC/Rly] / Local modules					
AI 4x13BIT/AQ 2x14BIT_1					
AI 4x13BIT/AQ 2x14BIT_1					
General\Project information					
Name	AI 4x13BIT/AQ 2x14BIT_1	Author	Mmuhammed	Comment	
Slot	2				
General\Catalog information					
Short designation	SM 1234 AI4/AQ2	Description	Analog input/output module AI4 + AQ2; plug-in terminal blocks; inputs: 13 bits, 2.5V, 5V, 10V and 0/4 to 20 mA; selectable frequency suppression; selectable smoothing; configurable diagnostics; outputs: +/-10V and 0 to 20 mA; configurable diagnostics; configurable substitute value for output	Article number	6ES7 234-4HE32-0XB0
Firmware version	V2.1				
AI 4/AQ 2\Project information					
Name	AI 4x13BIT/AQ 2x14BIT_1	Comment			
AI 4/AQ 2\Module diagnostics					
Enable power supply diagnostics	1	Additional diagnostics may be selected for each input/output.			
AI 4/AQ 2\Analog inputs\Noise reduction					
Integration time	50 Hz (20 ms)				
AI 4/AQ 2\Analog inputs\Channel0					
Channel address	IW96	Measurement type	Current	Current range	4..20 mA
Smoothing	Weak (4 cycles)			Enable broken wire diagnostics	1
Enable overflow diagnostics	1	Enable underflow diagnostics	0		
AI 4/AQ 2\Analog inputs\Channel1					
Channel address	IW98	Measurement type	Current	Current range	4..20 mA
Smoothing	Weak (4 cycles)			Enable broken wire diagnostics	0
Enable overflow diagnostics	1	Enable underflow diagnostics	0		
AI 4/AQ 2\Analog inputs\Channel2					
Channel address	IW100	Measurement type	Voltage	Voltage range	+/- 10 V
Smoothing	Weak (4 cycles)			Enable broken wire diagnostics	0
Enable overflow diagnostics	1	Enable underflow diagnostics	1		
AI 4/AQ 2\Analog inputs\Channel3					
Channel address	IW102	Measurement type	Voltage	Voltage range	+/- 10 V
Smoothing	Weak (4 cycles)			Enable broken wire diagnostics	0
Enable overflow diagnostics	1	Enable underflow diagnostics	1		
AI 4/AQ 2\Analog outputs					
Reaction to CPU STOP	Use substitute value				
AI 4/AQ 2\Analog outputs\Channel0					
Channel address	QW96	Analog output type	Voltage	Voltage range	+/- 10 V
Substitute value for channel on a change from RUN to STOP	0.000V			Enable short circuit diagnostics	1
Enable overflow diagnostics	1	Enable underflow diagnostics	1		
AI 4/AQ 2\Analog outputs\Channel1					
Channel address	QW98	Analog output type	Voltage	Voltage range	+/- 10 V
Substitute value for channel on a change from RUN to STOP	0.000V			Enable short circuit diagnostics	1
Enable overflow diagnostics	1	Enable underflow diagnostics	1		
AI 4/AQ 2\I/O addresses\Input addresses					
Start address	96	End address	103	Organization block	0
Process image	0				
AI 4/AQ 2\I/O addresses\Output addresses					
Start address	96	End address	99	Organization block	0
Process image	0				

Totally Integrated Automation Portal		
<h2 style="margin: 0;">AnalogInputSignalProcessing</h2> <h3 style="margin: 0;">PLC_2 [CPU 313C]</h3>		
PLC_2		
General		
Name	PLC_2	Author
Rack	0	Slot
General\Catalog information		
Short designation	CPU 313C	Description
Firmware version	V3.3	Article number
General\Identification & Maintenance		
Plant designation		Location identifier
MPI interface\General		
Name	MPI interface_1	Comment
MPI interface\MPI address\Interface networked with		
Subnet:	Not networked	
MPI interface\MPI address\Parameters		
Address:	2	Highest address:
Transmission speed:	187.5 kbps	
DI 24/DO 16\General		
Name	DI 24/DO 16_1	Comment
DI 24/DO 16\General\Catalog information		
Short designation	DI 24/DO 16	Description
DI 24/DO 16\Inputs\Channel group 0 - 3		
Input delay	3ms	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7		
Input delay	3ms	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 8 - 11		
Input delay	3ms	
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Rising (positive) edge		
Rising (positive) edge	False	
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Falling (negative) edge		
Falling (negative) edge	False	
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Rising (positive) edge		
Rising (positive) edge	False	

Instrumentation Tools

Totally Integrated Automation Portal					
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\I/O addresses\Input addresses					
Start address	124.0	End address	126.7	Process image	OB1-PI
Interrupt OB number	40				
DI 24/DO 16\I/O addresses\Output addresses					
Start address	124.0	End address	125.7	Process image	OB1-PI
AI 5/AO 2\General					
Name	AI 5/AO 2_1	Comment			
AI 5/AO 2\General\Catalog information					
Short designation	AI 5/AO 2	Description	Analogue I/O AI5 + AO2		
AI 5/AO 2\Inputs					
Temperature unit	Degrees Celsius				
AI 5/AO 2\Inputs\Channel 0					
Measurement type	Current	Measuring range	4..20mA	Interference frequency suppression	50Hz

Instrumentation Tools

Totally Integrated Automation Portal					
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 1					
Measurement type	Voltage	Measuring range	+/- 10V	Interference frequency suppression	50Hz
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 2					
Measurement type	Voltage	Measuring range	+/- 10V	Interference frequency suppression	50Hz
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 3					
Measurement type	Voltage	Measuring range	+/- 10V	Interference frequency suppression	50Hz
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 4					
Measurement type	Resistor (2-wire)	Measuring range	600 ohmsOhm		
AI 5/AO 2\Outputs\Output 0					
Output type	Voltage	Output range	+/- 10V		
AI 5/AO 2\Outputs\Output 1					
Output type	Voltage	Output range	+/- 10V		
AI 5/AO 2\I/O addresses\Input addresses					
Start address	752	End address	761	Process image	None
Interrupt OB number	40				
AI 5/AO 2\I/O addresses\Output addresses					
Start address	752	End address	755	Process image	None
Count\General					
Name	Count_1	Comment			
Count\General\Catalog information					
Short designation	Count	Description	3 channels; counting and frequency measurement at 30 kHz, pulse width modulation at 2.5 kHz switching frequency		
Count\Interrupt selection					
Interrupt selection	None				
Count\Channel 0					
Operating mode	Not configured				
Count\Channel 1					
Operating mode	Not configured				
Count\Channel 2					
Operating mode	Not configured				
Count\I/O addresses\Input addresses					
Start address	768	End address	783	Process image	None
Interrupt OB number	40				
Count\I/O addresses\Output addresses					
Start address	768	End address	783	Process image	None
Startup					
Startup if preset configuration does not match actual configuration	True	Startup after POWER ON	Warm restart		
Startup\Monitoring time for					
Ready message from modules	650x 100 ms	Parameter transfer to modules	100x 100 ms		
Cycle					
Cycle monitoring time	150ms	Cycle load due to communication	20%	Size of the process image input:	128
Size of the process image output:	128	OB85 call if I/O access error occurs	No OB85 call		
Clock memory					
Clock memory	False	Memory byte	0		
Interrupts\Time-of-day interrupts\					
OB number	Priority	Active	Execution	Start time	
OB 10	2	False	None	1994-01-01 00:00:00.000	
Interrupts\Time-delay interrupts\					
OB number	Priority	Process image partition(s)			
OB 20	3	None			
OB 21	4	None			
Interrupts\Cyclic interrupts\					
OB number	Priority	Execution	Phase offset	Unit	
OB 32	9	1000	0	ms	
OB 33	10	500	0	ms	
OB 34	11	200	0	ms	
OB 35	12	100	0	ms	
Interrupts\Hardware interrupts\					
OB number	Priority				
OB 40	16				
Interrupts\Asynchronous error interrupts\					
OB number	Priority				
OB 82	26				
OB 85	26				
OB 87	26				
Diagnostics system					
Number of alarms in the diagnostics buffer	10				
Diagnostics system\Report cause of STOP					
Report cause of STOP	True				

Instrumentation Tools

Totally Integrated Automation Portal										
System diagnostics\General										
Activate system diagnostics for this device	False									
Time of day										
Correction factor	0ms									
Time of day\Synchronization on PLC										
Type of synchronization	None			Time interval	None					
Time of day\Synchronization on MPI										
Type of synchronization	None			Time interval	None					
Retentive memory										
Number of memory bytes starting at MB 0	16			Number of S7 timers starting at T 0	0			Number of S7 counters starting at C 0	8	
Protection										
Password				Confirm password						
Protection\										
Level of protection	No protection									
Protection\ \Can be canceled with password										
Can be canceled with password	False									
Connection resources										
PG communication:	1			OP communication:	1			S7 basic communication:	0	
S7 communication:	0			Maximum number of S7 connection resources:	8					
Overview of addresses\Overview of addresses\Overview of addresses										
Inputs	True			Outputs	True			Address gaps	False	
Slot	True									
Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	124	126	DI 24/DO 16_1	OB1-PI	PLC_2 [CPU 313C]	-	3 Bytes	-	0	2 2
O	124	125	DI 24/DO 16_1	OB1-PI	PLC_2 [CPU 313C]	-	2 Bytes	-	0	2 2
I	752	761	AI 5/AO 2_1	OB1-PI	PLC_2 [CPU 313C]	-	10 Bytes	-	0	2 3
O	752	755	AI 5/AO 2_1	OB1-PI	PLC_2 [CPU 313C]	-	4 Bytes	-	0	2 3
I	768	783	Count_1	OB1-PI	PLC_2 [CPU 313C]	-	16 Bytes	-	0	2 4
O	768	783	Count_1	OB1-PI	PLC_2 [CPU 313C]	-	16 Bytes	-	0	2 4

AnalogInputSignalProcessing / PLC_2 [CPU 313C] / Program blocks

Main [OB1]

Main Properties

General

Name	Main	Number	1	Type	OB	Language	LAD
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Numbering	Manual
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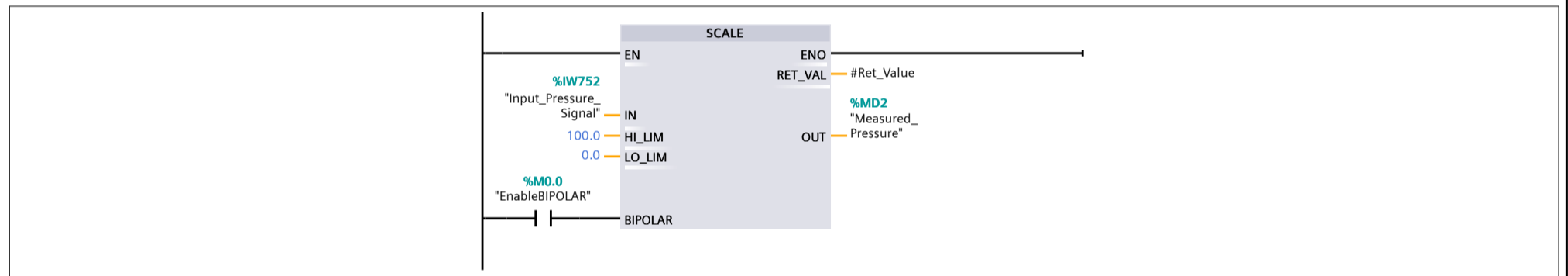
Information

Title	"Main Program Sweep (Cycle)"	Author		Comment		Family	
--------------	------------------------------	---------------	--	----------------	--	---------------	--

Version	0.1	User-defined ID	
----------------	-----	------------------------	--

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB1_EV_CLASS	Byte	0.0		Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)
OB1_SCAN_1	Byte	1.0		1 (Cold restart scan 1 of OB 1), 3 (Scan 2-n of OB 1)
OB1_PRIORITY	Byte	2.0		Priority of OB Execution
OB1_OB_NUMBR	Byte	3.0		1 (Organization block 1, OB1)
OB1_RESERVED_1	Byte	4.0		Reserved for system
OB1_RESERVED_2	Byte	5.0		Reserved for system
OB1_PREV_CYCLE	Int	6.0		Cycle time of previous OB1 scan (milliseconds)
OB1_MIN_CYCLE	Int	8.0		Minimum cycle time of OB1 (milliseconds)
OB1_MAX_CYCLE	Int	10.0		Maximum cycle time of OB1 (milliseconds)
OB1_DATE_TIME	Date_And_Time	12.0		Date and time OB1 started
Ret_Value	Word	20.0		
Constant				

Network 1:



AnalogInputSignalProcessing / PLC_2 [CPU 313C] / Program blocks / System blocks / Program resources
SCALE [FC105]

SCALE Properties

General

Name	SCALE	Number	105	Type	FC	Language	STL
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Numbering	Automatic
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Information

Title	SCALING VALUES	Author	SEA	Comment		Family	CONVERT
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Version	2.1	User-defined ID	SCALE
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Name	Data type	Offset	Default value	Comment
▼ Input				
IN	Int			input value to be scaled
HI_LIM	Real			upper limit in engineering units
LO_LIM	Real			lower limit in engineering units
BIPOLAR	Bool			1=bipolar; 0=unipolar
▼ Output				
OUT	Real			result of the scale conversion
InOut				
▼ Return				
Ret_Val	Word			




AnalogInputSignalProcessing / PLC_2 [CPU 313C]

Technology objects

This folder is empty.




AnalogInputSignalProcessing / PLC_2 [CPU 313C]

PLC tags

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	EnableBIPOLAR	Bool	%M0.0	True	True	
	Input_Pressure_Signal	Int	%IW752	True	True	
	Measured_Pressure	Real	%MD2	True	True	

AnalogInputSignalProcessing / PLC_2 [CPU 313C] / PLC tags

Default tag table [3]

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	EnableBIPOLAR	Bool	%M0.0	True	True	
	Input_Pressure_Signal	Int	%IW752	True	True	
	Measured_Pressure	Real	%MD2	True	True	

AnalogInputSignalProcessing / PLC_2 [CPU 313C]

PLC data types

This folder is empty.

AnalogInputSignalProcessing / PLC_2 [CPU 313C] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment
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AnalogInputSignalProcessing / PLC_2 [CPU 313C] / PLC supervisions & alarms

PLC alarms

PLC alarms

No entries

AnalogInputSignalProcessing / PLC_2 [CPU 313C] / PLC supervisions & alarms

User diagnostics alarms

User diagnostics alarms

No entries

AnalogInputSignalProcessing / PLC_2 [CPU 313C] / PLC supervisions & alarms

System alarms

System alarms

No entries

AnalogInputSignalProcessing / PLC_2 [CPU 313C]

PLC alarm text lists

This folder is empty.

Totally Integrated Automation Portal		
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AnalogInputSignalProcessing / PLC_2 [CPU 313C] / Local modules

PLC_2 [CPU 313C]

PLC_2					
General					
Name	PLC_2	Author	Mmuhaled	Comment	
Rack	0	Slot	2		
General\Catalog information					
Short designation	CPU 313C	Description	Work memory 128KB; 0.07ms/1000 instructions; DI24/DO16; AI5/AO2 integrated; 3 pulse outputs (2.5kHz); 3 channels counting and measuring with 24V (30kHz) incremental encoders; MPI interface; multi-tier configuration up to 31 modules; firmware 3.3	Article number	6ES7 313-5BG04-0AB0
Firmware version	V3.3				
General\Identification & Maintenance					
Plant designation		Location identifier			
MPI interface\General					
Name	MPI interface_1	Comment			
MPI interface\MPI address\Interface networked with					
Subnet:	Not networked				
MPI interface\MPI address\Parameters					
Address:	2	Highest address:	31	Transmission speed:	187.5 kbps
DI 24/DO 16\General					
Name	DI 24/DO 16_1	Comment			
DI 24/DO 16\General\Catalog information					
Short designation	DI 24/DO 16	Description	Digital input/output DI24 + DO16		
DI 24/DO 16\Inputs\Channel group 0 - 3					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Rising (positive) edge					
Rising (positive) edge	False				

Instrumentation Tools

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DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\I/O addresses\Input addresses					
Start address	124.0	End address	126.7	Process image	OB1-PI
Interrupt OB number	40				
DI 24/DO 16\I/O addresses\Output addresses					
Start address	124.0	End address	125.7	Process image	OB1-PI
AI 5/AO 2\General					
Name	AI 5/AO 2_1	Comment			
AI 5/AO 2\General\Catalog information					
Short designation	AI 5/AO 2	Description	Analogue I/O AI5 + AO2		
AI 5/AO 2\Inputs					
Temperature unit	Degrees Celsius				
AI 5/AO 2\Inputs\Channel 0					
Measurement type	Current	Measuring range	4..20mA	Interference frequency suppression	50Hz

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Integration time		20ms			
AI 5/AO 2\Inputs\Channel 1					
Measurement type	Voltage	Measuring range	+/- 10V	Interference frequency suppression	50Hz
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 2					
Measurement type	Voltage	Measuring range	+/- 10V	Interference frequency suppression	50Hz
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 3					
Measurement type	Voltage	Measuring range	+/- 10V	Interference frequency suppression	50Hz
Integration time		20ms			
AI 5/AO 2\Inputs\Channel 4					
Measurement type	Resistor (2-wire)	Measuring range	600 ohmsOhm		
AI 5/AO 2\Outputs\Output 0					
Output type	Voltage	Output range	+/- 10V		
AI 5/AO 2\Outputs\Output 1					
Output type	Voltage	Output range	+/- 10V		
AI 5/AO 2\I/O addresses\Input addresses					
Start address	752	End address	761	Process image	None
Interrupt OB number	40				
AI 5/AO 2\I/O addresses\Output addresses					
Start address	752	End address	755	Process image	None
Count\General					
Name	Count_1	Comment			
Count\General\Catalog information					
Short designation	Count	Description	3 channels; counting and frequency measurement at 30 kHz, pulse width modulation at 2.5 kHz switching frequency		
Count\Interrupt selection					
Interrupt selection	None				
Count\Channel 0					
Operating mode	Not configured				
Count\Channel 1					
Operating mode	Not configured				
Count\Channel 2					
Operating mode	Not configured				
Count\I/O addresses\Input addresses					
Start address	768	End address	783	Process image	None
Interrupt OB number	40				
Count\I/O addresses\Output addresses					
Start address	768	End address	783	Process image	None
Startup					
Startup if preset configuration does not match actual configuration	True	Startup after POWER ON	Warm restart		
Startup\Monitoring time for					
Ready message from modules	650x 100 ms	Parameter transfer to modules	100x 100 ms		
Cycle					
Cycle monitoring time	150ms	Cycle load due to communication	20%	Size of the process image input:	128
Size of the process image output:	128	OB85 call if I/O access error occurs	No OB85 call		
Clock memory					
Clock memory	False	Memory byte	0		
Interrupts\Time-of-day interrupts\					
OB number	Priority	Active	Execution	Start time	
OB 10	2	False	None	1994-01-01 00:00:00.000	
Interrupts\Time-delay interrupts\					
OB number	Priority	Process image partition(s)			
OB 20	3	None			
OB 21	4	None			
Interrupts\Cyclic interrupts\					
OB number	Priority	Execution	Phase offset	Unit	
OB 32	9	1000	0	ms	
OB 33	10	500	0	ms	
OB 34	11	200	0	ms	
OB 35	12	100	0	ms	
Interrupts\Hardware interrupts\					
OB number	Priority				
OB 40	16				
Interrupts\Asynchronous error interrupts\					
OB number	Priority				
OB 82	26				
OB 85	26				
OB 87	26				
Diagnostics system					
Number of alarms in the diagnostics buffer	10				
Diagnostics system\Report cause of STOP					
Report cause of STOP	True				

Instrumentation Tools

Totally Integrated Automation Portal										
System diagnostics\General										
Activate system diagnostics for this device	False									
Time of day										
Correction factor	0ms									
Time of day\Synchronization on PLC										
Type of synchronization	None			Time interval	None					
Time of day\Synchronization on MPI										
Type of synchronization	None			Time interval	None					
Retentive memory										
Number of memory bytes starting at MB 0	16			Number of S7 timers starting at T 0	0		Number of S7 counters starting at C 0	8		
Protection										
Password				Confirm password						
Protection\										
Level of protection	No protection									
Protection\ \Can be canceled with password										
Can be canceled with password	False									
Connection resources										
PG communication:	1			OP communication:	1		S7 basic communication:	0		
S7 communication:	0			Maximum number of S7 connection resources:	8					
Overview of addresses\Overview of addresses\Overview of addresses										
Inputs	True			Outputs	True		Address gaps	False		
Slot	True									
Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	124	126	DI 24/DO 16_1	OB1-PI	PLC_2 [CPU 313C]	-	3 Bytes	-	0	2 2
O	124	125	DI 24/DO 16_1	OB1-PI	PLC_2 [CPU 313C]	-	2 Bytes	-	0	2 2
I	752	761	AI 5/AO 2_1	OB1-PI	PLC_2 [CPU 313C]	-	10 Bytes	-	0	2 3
O	752	755	AI 5/AO 2_1	OB1-PI	PLC_2 [CPU 313C]	-	4 Bytes	-	0	2 3
I	768	783	Count_1	OB1-PI	PLC_2 [CPU 313C]	-	16 Bytes	-	0	2 4
O	768	783	Count_1	OB1-PI	PLC_2 [CPU 313C]	-	16 Bytes	-	0	2 4

AnalogInputSignalProcessing

Ungrouped devices

This folder is empty.

AnalogInputSignalProcessing

Security settings

This folder is empty.

AnalogInputSignalProcessing / Cross-device functions / Project traces

Measurements

This folder is empty.

AnalogInputSignalProcessing / Common data

Alarm classes

Alarm classes			
Name	Display name	Acknowledgment	Priority
Acknowledgement	A	True	0
No Acknowledgement	NA	False	0

AnalogInputSignalProcessing / Common data

Logs

This folder is empty.

AnalogInputSignalProcessing / Languages & resources

Project languages

Languages

Reference language

English (United States)

Editing language

English (United States)

Other project languages

Empty

AnalogInputSignalProcessing / Languages & resources / Project texts

Project texts

Project texts		
English (United States)	Category	Reference
"Main Program Sweep (Cycle)"	Block comment	AnalogInputSignalProcessing\PLC_1 [CPU 1214C AC/DC/Rly]\Program blocks\Main [OB1]\Block title
"Main Program Sweep (Cycle)"	Block comment	AnalogInputSignalProcessing\PLC_2 [CPU 313C]\Program blocks\Main [OB1]\Block title
A	Alarm class text	AnalogInputSignalProcessing\Acknowledgement\AlarmClassData_IDisplayNaming_Display-Name
A	Alarm class text	AnalogInputSignalProcessing\Acknowledgement\ShortName
NA	Alarm class text	AnalogInputSignalProcessing\No Acknowledgement\AlarmClassData_IDisplayNaming_Display-Name
NA	Alarm class text	AnalogInputSignalProcessing\No Acknowledgement\ShortName
Normalize the analog input to a value between 0 and 27648	Block comment	AnalogInputSignalProcessing\PLC_1 [CPU 1214C AC/DC/Rly]\Program blocks\analog signal processing [FC1]\Network 1\Title
scaling the normailzed value from 0-27648 into the actual physical value of 0-10 bar pressure	Block comment	AnalogInputSignalProcessing\PLC_1 [CPU 1214C AC/DC/Rly]\Program blocks\analog signal processing [FC1]\Network 2\Title
SCALING VALUES	Block comment	AnalogInputSignalProcessing\PLC_2 [CPU 313C]\Program blocks\System blocks\Program resources\SCALE [FC105]\Block title